

SUPPLY VOLTAGE MONITOR

ZSM330

ISSUE 1 – NOVEMBER 1995

DEVICE DESCRIPTION

The ZSM330 is a three terminal under voltage monitor circuit for use in microprocessor systems. The threshold voltage of the device has been set to 3.1 volts making it ideal for 3.3 volt circuits.

Included in the device is a precise voltage reference and a comparator with built in hysteresis to prevent erratic operation. The ZSM330 features an open collector output capable of sinking at least 10mA which only requires a single external resistor to interface to following circuits.

Operation of the device is guaranteed from one volt upwards, from this level to the device threshold voltage the output is held low providing a power on reset function. Should the supply voltage, once established, at any time drop below the threshold level then the output again will pull low.

The device is available in a TO92 package for through hole applications as well as SO8 and SOT223 for surface mount requirements.

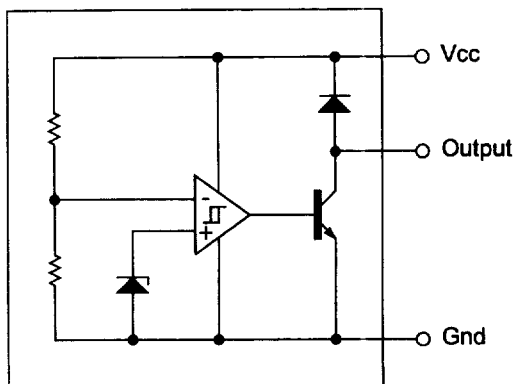
FEATURES

- SO8, SOT223 and TO92 packages
- Power on reset generator
- Automatic reset generation
- Low standby current
- Guaranteed operation from 1 volt
- Wide supply voltage range
- Internal clamp diode to discharge delay capacitor
- 3.1 volt threshold for 3.3 volt logic
- 20mV hysteresis prevents erratic operation

APPLICATIONS

- Microprocessor systems
- Computers
- Computer peripherals
- Instrumentation
- Automotive
- Battery powered equipment

SCHEMATIC DIAGRAM



CONNECTION TABLE

Pin	SO8	SOT223	TO92
1	Out	Vcc	Gnd
2	Vcc	Gnd	Vcc
3	N/C	Out	Out
4	Gnd	-	-
5	N/C	-	-
6	N/C	-	-
7	N/C	-	-
8	N/C	-	-
Pack	N8	G	C

see Diagrams Page 3 - 3

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ABSOLUTE MAXIMUM RATING

Input Supply Voltage	-1 to 10V	Power Dissipation	
Offstate Output Voltage	10V	TO92	780mW
Onstate Output		SOT223	2W(Note 2)
Sink Current(Note 1)	Internally limited	SO8	780mW(Note 2)
Clamp diode			
Forward Current(Note 1)	100mA		
Operating junction temperature	150°C		
Operating Temperature	-40 to 85°C		
Storage Temperature	-55 to 150°C		

TEST CONDITIONS

(T_{amb}=25°C for typical values, T_{amb}=-40 to 85°C for min/max values (Note3))

COMPARATOR

PARAMETER	SYMBOL	MIN	TYP.	MAX.	UNITS
Threshold Voltage High state output (V _{cc} increasing)	V _{IH}	3.01	3.09	3.15	V
Threshold Voltage Low state output (V _{cc} decreasing)	V _{IL}	3.01	3.07	3.15	V
Hysteresis	V _H	0.01	0.02	0.05	V

OUTPUT

PARAMETER	SYMBOL	MIN	TYP.	MAX.	UNITS
Output sink saturation: (V _{cc} =2.7V, I _{sink} =8.0mA)	V _{OL}		0.46	1.0	V
(V _{cc} =2.7V, I _{sink} =2.0mA)			0.15	0.4	V
(V _{cc} =1.0V, I _{sink} =0.1mA)				0.25	V
Onstate output sink current (V _{cc} , Output=2.7V)	I _{sink}	10	27	60	mA
Offstate output leakage current (V _{cc} , Output=3.3V)	I _{oh}		0.02	0.5	μA
Clamp diode forward voltage (I _f =10mA)	V _f	0.6	1.2	1.5	V
Propagation delay (V _{in} 3.3V to 2.7V, R _i =10k, T _{amb} =25°C)	T _d		2.2		μs

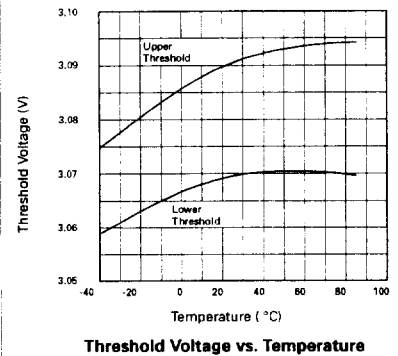
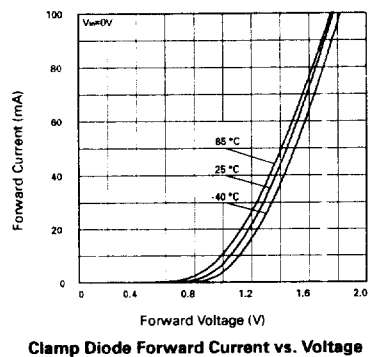
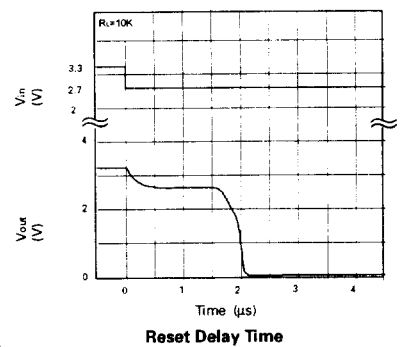
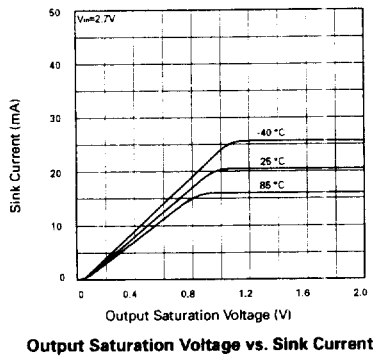
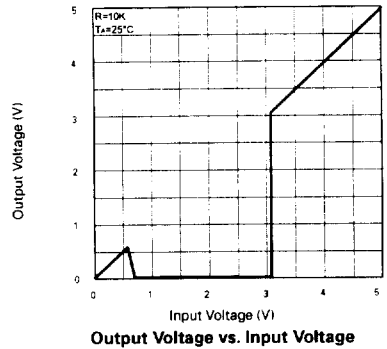
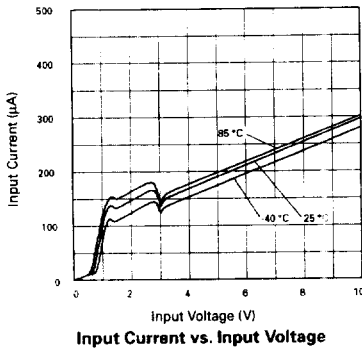
TOTAL DEVICE

PARAMETER	SYMBOL	MIN	TYP.	MAX.	UNITS
Operating input voltage range	V _{cc}	1.0 to 6.5			V
Quiescent input current (V _{cc} =3.3V)	I _q		120	180	μA

Note:

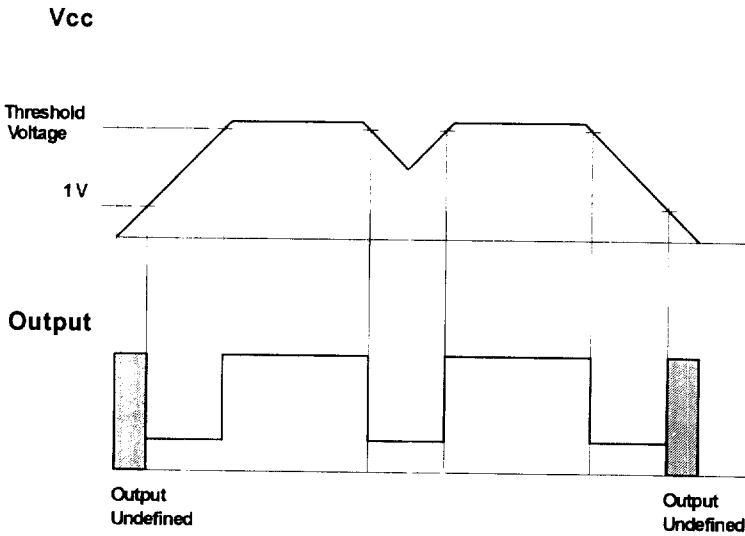
1. Maximum package power dissipation must be observed
2. Maximum power dissipation, for the SOT223 and SO8 packages, is calculated assuming that the device is mounted on a PCB measuring 2 inches square.
3. Low duty cycle pulse techniques are used during test to maintain junction temperatures as close to ambient as possible

TYPICAL CHARACTERISTICS

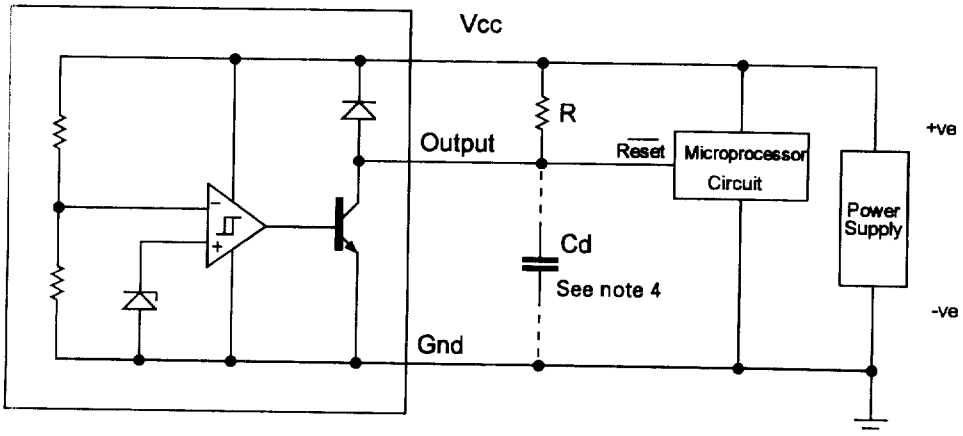


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TIMING DIAGRAM



APPLICATION CIRCUIT



Note 4: A time delayed reset can be accomplished with the additional Cd.

$$T_{DY} = RCd \ln \left(\frac{1}{1 - \frac{V_{TH(mpu)}}{V_{in}}} \right)$$

T_{DY} =Time (Seconds)

V_{TH} =Microprocessor Resistance Threshold(Ohms)

V_{in} =Power Supply Voltage