



Z16M27

PCMCIA-16 INTERFACE

FEATURES

Part	RAM* (Bytes)	Speed (MHz)
Z16M27	256	20
*Attribute		

Translation Capabilities

- PCMCIA to IDE Translation
- IDE to IDE Mapping, Pass Through Mode
- Direct Memory Access (DMA) Support

Supports Multiple Applications

- 256 Bytes of Attribute Memory
- Five Configuration Registers
- Three Additional Registers to Support EEPROM Programming
- Three Programmable Memory or I/O Map Ranges
- Conforms to PCMCIA Standards
- EXCA Register Compatible

DESCRIPTION

The Z16M27 is a general-purpose PCMCIA adapter chip used on the side of the interface. The Z16M27 contains special circuitry for PCMCIA-to-ATA/IDE applications, but easily configures to all types of memory and I/O mapped peripheral hardware, supporting a wide variety of PC card applications.

The Z16M27 can be used in a stand-alone configuration without the use of a local processor by providing all of the attribute memory, CCRs, range, and interrupt types through a serial EEPROM. The serial EEPROM is read automati-

cally using an internal EEPROM sequencer. The Z16M27 can also be configured through a local processor for use on intelligent controller systems.

Notes:

All Signals with a preceding front slash, "/", are active Low, e.g., B//W (WORD is active Low); /B/W (BYTE is active Low, only).

Power connections follow conventional descriptions below:

Connection	Circuit	Device
Power Ground	V_{CC} GND	V_{DD} V_{SS}

GENERAL DESCRIPTION (Continued)

The local processor connects to the Z16M27 through the serial interface or can be programmed through an external EEPROM. The Z16M27 provides for the PCMCIA to ATA/

IDE translation, ATA/IDE to ATA/IDE mapping, or PCMCIA to three general-purpose maps.

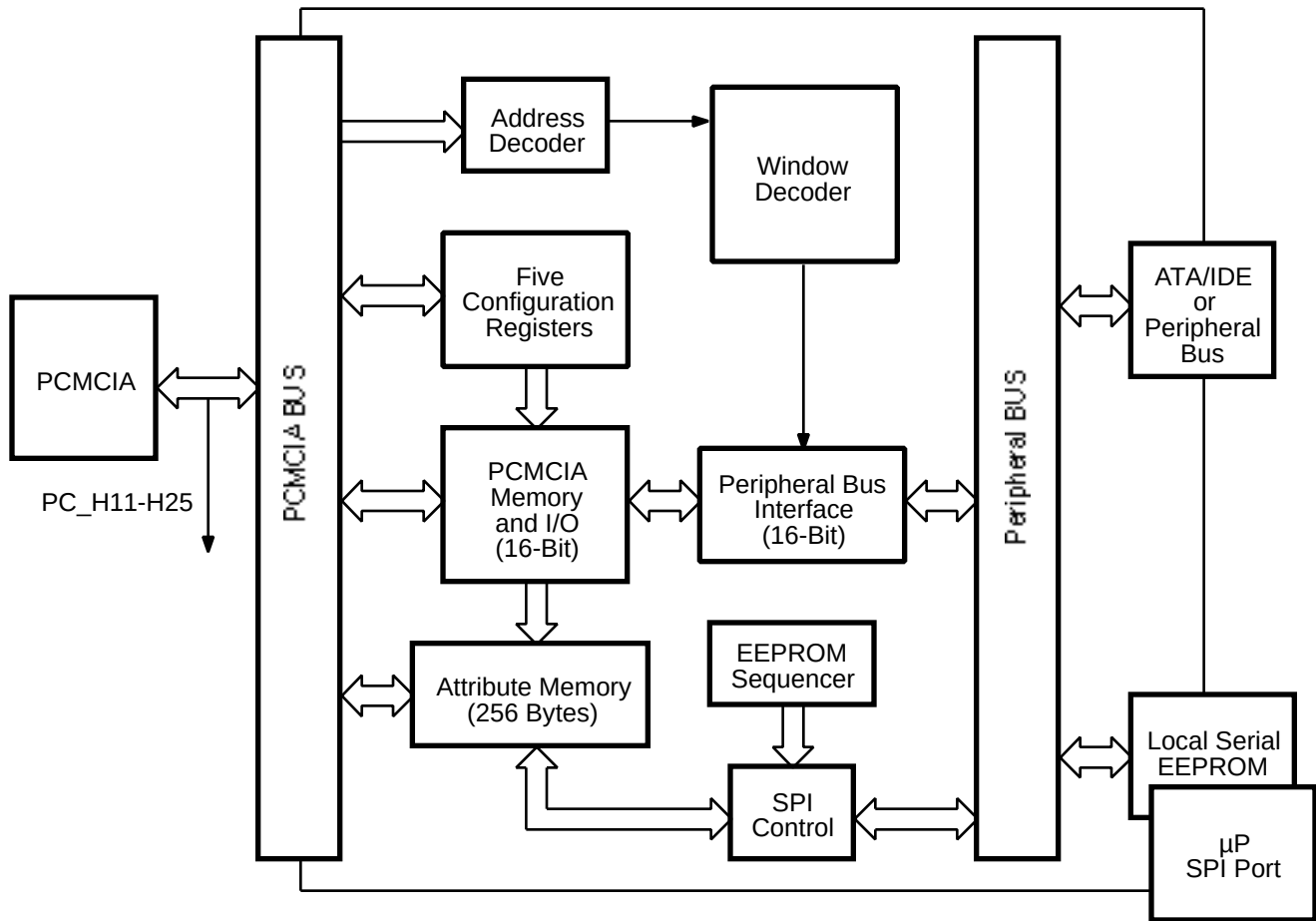


Figure 1. Z16M27 Functional Block Diagram

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Zilog, Inc. 210 East Hacienda Ave.
Campbell, CA 95008-6600
Telephone (408) 370-8000
Telex 910-338-7621
FAX 408 370-8056
Internet: <http://www.zilog.com>