YAMAHA LSI

YGV619

AVDP6

Advanced Video Display Processor 6

■ Outline

YGV619 is a VDP (Video Display Processor) adopting OSD display control system which is best suited to the data broadcasting. The digital image interface of this device for connection with MPEG decoder has been improved. The use of this device allows screen composition that is suited to mobile information terminals, car navigation system, etc. Scan timing conforming to the display standard of digital TVs can be made.

Two built-in PLL circuits allows to realize superimposition of external image signal on original image signal, and to produce clock best suited to SDRAM that is adopted as external video memory.

■ Features

● Display planes**:** External digital image is overlaid with OSD images composed of regions.
Up to four planes, which are individually composed of back drop plane (plane on which external images are inputted) + region, are available.

● OSD image format**:**
8bit/dot palette mode, and 16 bit RGB or YCbCr format can be selected.
YCbCr conforms to the conversion method of ITU601.
Color palette (256 colors in 16777 k colors) can be specified by region.

● Digital image input format**:**
· 18bitR6G6B6 (Max. dot clock frequency: 80 MHz)
· 16bitYCbCr422 (Max. dot clock frequency: 80 MHz)
· 8bitITU656 (Dot clock frequency 27 MHz)

● Digital image output format**:**
· R6G6B6 + 2 bit AT
· 18bitYCbCr444 + 2 bit AT
· 16bitYCbCr422 + 2 bit AT
· 8bitITU656 + 2 bit AT + 6 bit α blending coefficient

● Max. OSD resolution**:** 960 dots × 1080 lines
(However, max. resolution of overlaid external image is 1920 ×1080 lines)

● Applicable digital TV image format**:**
· 525i
· 525p
· 1125i

YAMAHA CORPORATION

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