

Crystek Corporation
239-561-3311 FAX 239-561-1025

5.0V Surface Mount Crystal Clock Oscillator

Dimensions inches (mm)

All dimensions are Max unless otherwise specified.

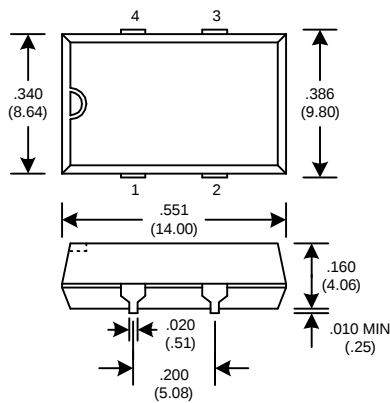
**SURFACE MOUNT CRYSTAL CLOCK
OSCILLATOR**

Y & YE 3290 / 3292 / 3291

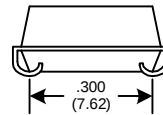
Available as $\pm 100\text{ppm}$, $\pm 50\text{ppm}$, $\pm 25\text{ppm}$

SPECIFICATIONS		Model Y & YE 3290 / 3292 / 3291	
Frequency Range		1.000 MHz - 26.000 MHz	26.001 MHz - 66.6667 MHz
Frequency Stability		3290 $\pm 100\text{ppm}$ 3292 $\pm 50\text{ppm}$, 3291 $\pm 25\text{ppm}$	
Operating Temperature Range		Y= -10°C to $+70^{\circ}\text{C}$ Std. YE = $(-40^{\circ}\text{C}$ to $+85^{\circ}\text{C})\{\pm 100\text{ppm} \ \& \ \pm 50\text{ppm} \ \text{Only}\}$	
Storage Temperature Range		-55°C to $+125^{\circ}\text{C}$	
Supply Voltage		VDD 5.0 $\pm 0.5\text{V}$	
Supply Current		23mA Max	45 mA Max
Duty Cycle	TTL level	45% to 55%	-
	CMOS level	40% to 60%	40% to 60%
Output Voltage		VOH: VDD - 0.4V Min	VOH: VDD - 0.4V Min
		VOL: 0.4V Max	VOL: 0.4V Max
Output Load	TTL	10TTL Max	-
	CMOS	50pF Max	50pF Max
Rise and Fall Time		Tr: 8 nsec Max	Tr: 7 nsec Max
		Tf: 8 nsec Max	Tf: 7 nsec Max
Start up Time		4 msec Max	10 msec Max
Input (OE) Voltage		VIH: 2.0V Min	VIH: 2.0V Min
		VIL: 0.8V Max	VIL: 0.8V Max
Disable Current		12mA Max	20mA Max
Aging (first year)		$\pm 5\text{ppm/year}$ Max	
Shock Resistance		$\pm 20\text{ppm}$ Max	

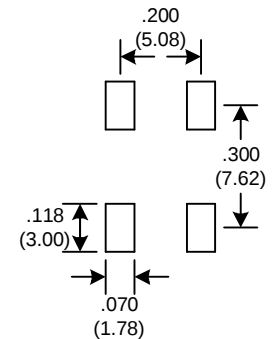
DIMENSIONS



PIN	Function
1	OE
2	GND
3	OUT
4	VDD



SUGGESTED PAD LAYOUT



Specifications subject to change without notice.

Spec.	f1
Rev.	E