

1. Specification

Nominal Frequency; $U_C =$ V, $T = 25^\circ\text{C}$:	155.52 MHz
Nominal frequency tolerance:	$< \pm 10$ ppm
Frequency stability in the temperature range -40°C to $+85^\circ\text{C}$: vs. supply voltage changes $U_B \pm 5\%$: vs. load changes $\pm 10\%$:	$< \pm 15$ ppm $< \pm 2.0$ ppm $< \pm 0.2$ ppm
Aging @ 25°C :	$< \pm 2$ ppm / first year $< \pm 1$ ppm / year
Tri-state:	Pin 1 High for output clock Low for high resistance output
Supply voltage U_B :	$3.3\text{ V} \pm 5\%$
Current consumption:	≤ 50 mA
Output voltage : load : duty cycle : rise time, fall time 20% ... 80%:	LVHCMOS 1 kOhm // 5 pF 40 % ... 60 % $\leq$ ns
Temperature ranges Operating: Storage:	$-40^\circ\text{C} \dots +85^\circ\text{C}$ $-55^\circ\text{C} \dots +105^\circ\text{C}$

2. Environmental conditions

According to KVG Product Qualification Procedure AA-QM-200

3. Marking

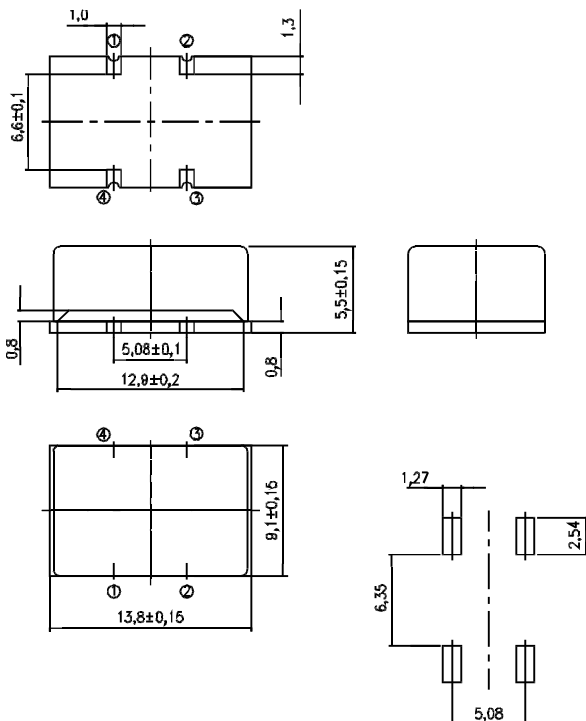
Manufacturer's name, date code(week/year)
Specification
Center frequency

4	Output Enabled for HIGH	17.02.00	H.-J. Herzog	Vectron International GmbH P.O.Box 61 D-74924 Neckarbischofsheim Tel. +49 (0) 7263 / 648-0 Fax. +49 (0) 7263 / 6196
3	Supply voltage stability ± 2 ppm	01.07.99	H.-J. Herzog	
2	Test circuit	26.02.99	H.-J. Herzog	
1		12.02.99	H.-J. Herzog	
ED	Description	Date	Name	



4. Case

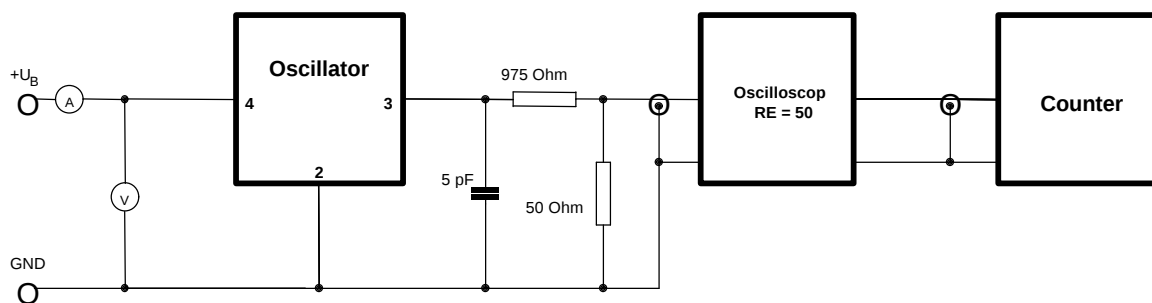
Case style: BF-157-5.5B



1. Pin configuration

1. Tri-state Input
2. Ground, case
3. RF-output
4. Supply voltage U_B

5. Test circuit



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