2 Megabit Puma Module XM28C020P 64K x 32 Bit

High Speed 5 Volt Byte Alterable Nonvolatile Memory Array

FEATURES

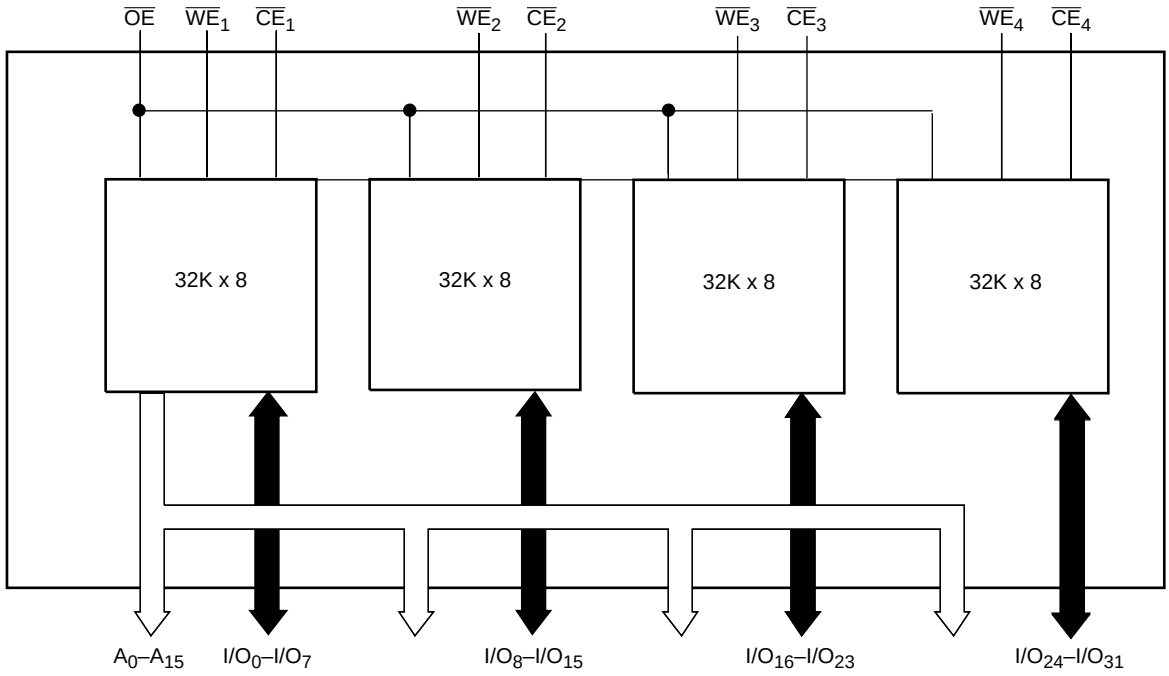
- **High Speed, High Density Memory Module**
  - **—200ns, 150ns, 120ns access times available**
  - **—2 megabit memory in 1 square inch.**
- **Flexible Multiplane Architecture**
  - **—Four separate chip selects**
  - **—32 separate I/Os**
    - **user configurable I/Os—x8, x16, or x32**
    - **user configurable page size—64 double-words, 128 words, or 256 bytes**
  - **—Concurrent read/write operations**
    - **able to continue reading during a non-volatile write cycle**
  - **—5 volt byte or page alterable**
    - **no erase before write**
- **Software Data Protection**
- **Early End of Write Polling**
  - **—$\overline{\text{DATA}}$ polling**
  - **—Toggle bit polling**
- **High Reliability**
  - **—Endurance: 100,000 cycles**
  - **—Data retention: 100 years**

DESCRIPTION

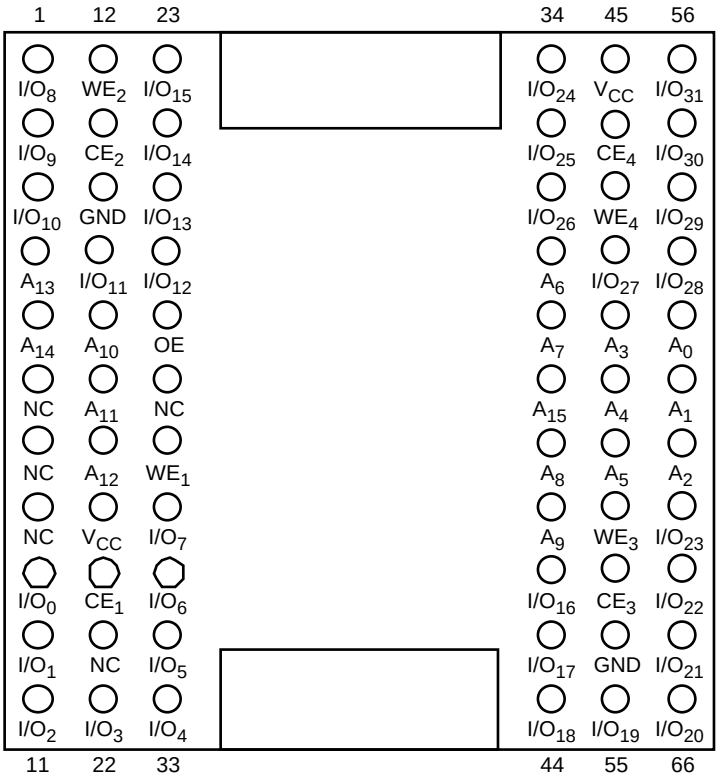
The XM28C020P is a high speed, high density CMOS byte alterable nonvolatile memory array constructed on a co-fired ceramic substrate using Xicor's high speed X28C513 components in 32-pad leadless chip carriers. The substrate is a 66-pin ceramic pin grid array.

The module is configured with four separate chip enable and write enable inputs and 32 separate I/Os. This, along with the small footprint, provides the end user with a large degree of flexibility in board layout and memory configuration. In addition, with the large number of pins and the growth path being implemented, the module will be able to grow to 16 megabits.

BLOCK DIAGRAM



PIN CONFIGURATION



Ordering Information