CFPS-8

ISSUE 1; 30 JULY 1999

Delivery Options

- Please contact our sales office for current leadtimes

Output Compatibility

- Tri-state HCMOS (3.0V)

Package Outline

- 5.0 × 3.2 × 0.95mm SMD (surface mount device) ceramic package.

Operating Temperature Range

- 0 to 70°C

Storage Temperature Range

- –55 to 125°C

Solder Reflow

- Pre-heat: 150 to 180°C/55 to 70 seconds max.
- Reflow: 180°C/40 to 60 seconds max., 200°C/40 seconds max., 220°C ±5°C/5 to 15 seconds max.

Ageing

- ≤±5ppm per year

Tri-state Operation

- Logic '1' to pad 1 enables oscillator output, 2.2V min
- Logic '0' to pad 1 disables oscillator output; when disabled the oscillator output goes to the high impedance state, 0.8V max
- No connection to pad 1 enables oscillator output

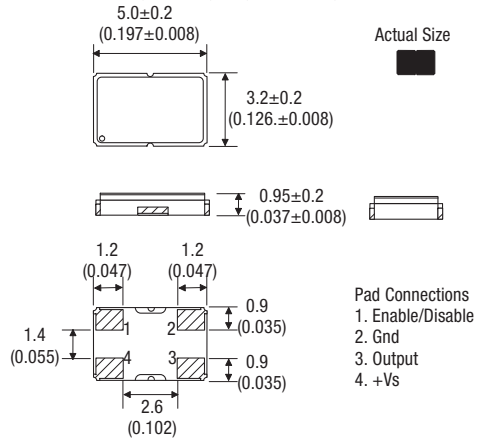
Marking

- Model number (+ Operating Temperature Code; if applicable)
- Frequency Stability Code
- Frequency

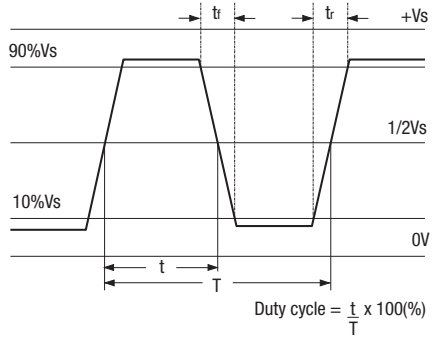
Minimum Order Information Required

- Frequency + Model Number + Operating Temperature Code (if applicable)+ Frequency Stability

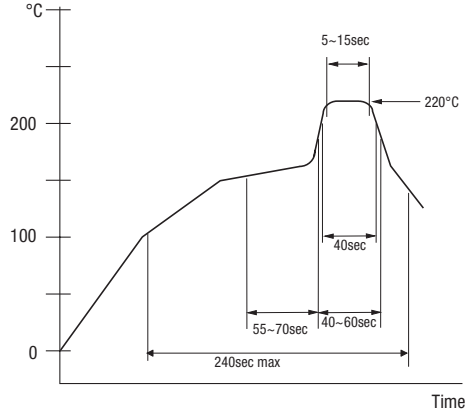
Outline in mm (inches) - (scale 2:1)



Output Waveform



Typical Solder Condition - Infrared Reflow



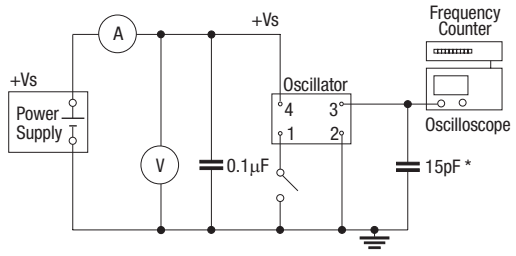
Electrical Specification – maximum limiting values when measured in test circuit

Frequency Range	Frequency Stability	Supply Voltage	Supply Current	Rise Time (t_r)	Fall Time (t_f)	Duty Cycle	Model Number
2.5 to < 55.0MHz	±100ppm	3.0V±0.3V	50mA	6ns	6ns	40/60%	CFPS-8

Ordering Example 24.0MHz CFPS-8 C

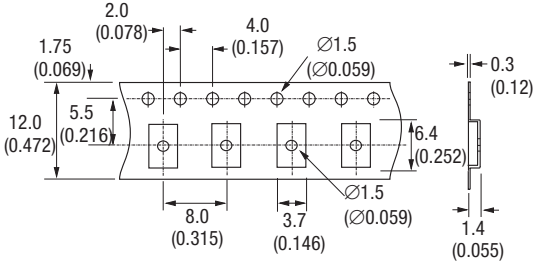
Frequency
Model No
Frequency Stability: C = ±100ppm

Test Circuit



* Inclusive of jigging & equipment capacitance

Outline in mm (inches) - Tape



Outline in mm (inches) - Reel (scale 1:8)