

- ◆ CMOS 2-Input NAND Gate
- ◆ High Speed Operation tpd=7ns TYP
- ◆ Operating Voltage Range 2V~6V
- ◆ Low Power Consumption 1 μ A MAX

■ Applications

- Palmtops
- Digital Equipment

■ General Description

The XC74UH00AAM is a 2-input CMOS NAND gate, manufactured using silicon gate CMOS fabrication.

CMOS low power circuit operation makes high speed LS-TTL operations achievable.

With a wave forming buffer connected internally, stabilized output can be achieved as the circuit offers high noise immunity.

As the XC74UH00AAM is integrated into a mini molded, SOT-25 package, high density mounting is possible.

■ Features

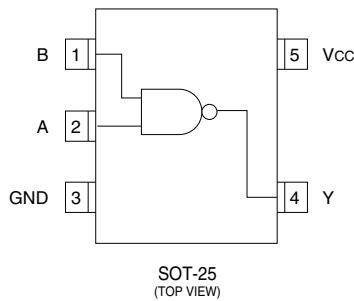
High Speed Operation: tpd=7ns TYP

Operating Voltage Range: 2V~6V

Low Power Consumption: 1 μ A MAX

Space Saving Package: SOT-25

■ Pin Configuration



■ Function

INPUT		OUTPUT
A	B	Y
L	L	H
L	H	H
H	L	H
H	H	L

H=High level, L=Low level

■ Absolute Maximum Ratings

Ta=25°C

PARAMETER	SYMBOL	RATINGS	UNITS
Power Supply Voltage	VCC	-0.5 ~ +7.0	V
Input Voltage	VIN	-0.5 ~ VCC +0.5	V
Output Voltage	VOUT	-0.5 ~ VCC +0.5	V
Input Diode Current	I _{IK}	± 20	mA
Output Diode Current	I _{OK}	± 20	mA
Output Current	I _{OUT}	± 25	mA
VCC ,GND Current	I _{CC} , I _{GND}	± 25	mA
Continuous Total Power Dissipation	Pd	200	mW
Storage Temperature	T _{STG}	-65 ~ +150	°C

Note: Voltage is all Ground standardized.

■ DC Electrical Characteristics

PARAMETER	SYMBOL	Vcc(V)	CONDITIONS	Ta=25°C			Ta=-40~85°C		UNITS
				MIN	TYP	MAX	MIN	MAX	
Input Voltage	VIH	2.0		1.5	-	-	1.5	-	V
		4.5		3.15	-	-	3.15	-	
		6.0		4.2	-	-	4.2	-	
	VIL	2.0		-	-	0.5	-	0.5	
		4.5		-	-	1.35	-	1.35	
		6.0		-	-	1.8	-	1.8	
Output Voltage	VOH	2.0	VIN=VIH or VIL	1.9	2.0	-	1.9	-	V
		4.5		4.4	4.5	-	4.4	-	
		6.0		5.9	6.0	-	5.9	-	
		4.5		4.18	4.36	-	4.13	-	
		6.0		5.68	5.84	-	5.63	-	
	VOL	2.0	VIN=VIH	-	0.0	0.1	-	0.1	V
		4.5		-	0.0	0.1	-	0.1	
		6.0		-	0.0	0.1	-	0.1	
		4.5		-	0.11	0.26	-	0.33	
		6.0		-	0.13	0.26	-	0.33	
Input Current	IIN	6.0	VIN=VCC or GND	-	-	±0.1	-	±1.0	μA
Quiescent Supply Current	Icc	6.0	VIN=VCC or GND, IOUT=0μA	-	-	1.0	-	10.0	

■ Switching Electrical Characteristics

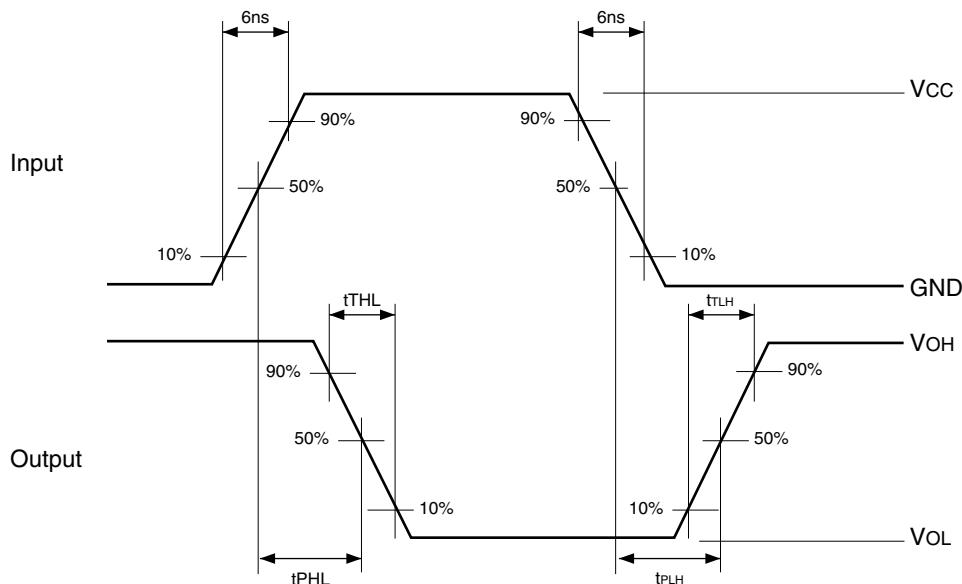
CL=15pF, tr=6ns, VCC=5V

PARAMETER	SYMBOL	Vcc(V)	CONDITIONS	Ta=25°C			UNITS
				MIN	TYP	MAX	
Output Transition Time	tTLH			-	4	10	ns
	tTHL			-	3	10	ns
Propagation Delay Time	tPLH			-	5	15	ns
	tPHL			-	5	15	ns

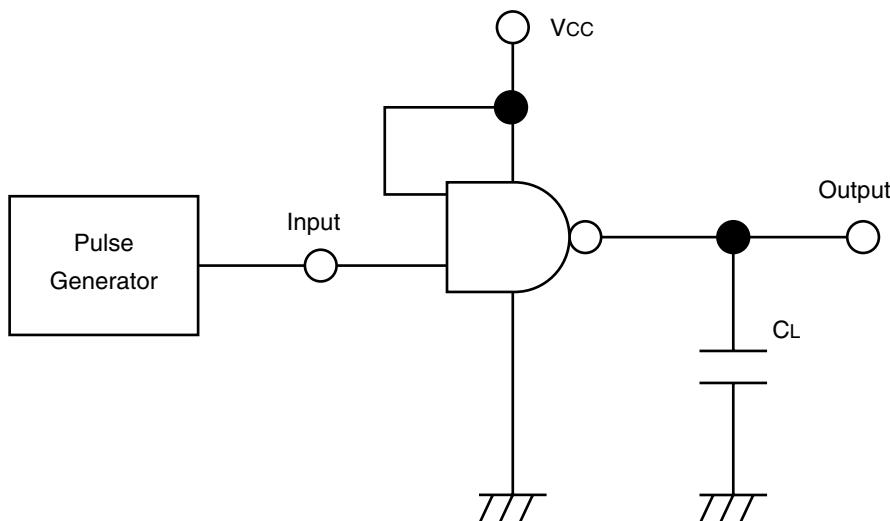
CL=50pF, tr=tf=6ns

PARAMETER	SYMBOL	Vcc(V)	CONDITIONS	Ta=25°C			Ta=-40~85°C		UNITS
				MIN	TYP	MAX	MIN	MAX	
Output Transition Time	tTLH	2.0		-	18	125	-	155	ns
		4.5		-	7	25	-	31	
		6.0		-	6	21	-	26	
	tTHL	2.0		-	14	125	-	155	
		4.5		-	6	25	-	31	
		6.0		-	6	21	-	26	
Propagation Delay Time	tPLH	2.0		-	16	100	-	125	ns
		4.5		-	8	20	-	25	
		6.0		-	7	17	-	21	
	tPHL	2.0		-	16	100	-	125	
		4.5		-	6	20	-	25	
		6.0		-	5	17	-	21	
Input Capacitance	CIN	-		-	5	10	-	10	pF

■ Waveforms



■ Typical Application Circuit



Note: open output when measuring supply current

9

■ Recommended Operating Conditions

PARAMETER	SYMBOL	CONDITIONS	UNITS
Supply Voltage	V_{CC}	2 ~ 6	V
Input Voltage	V_{IN}	0 ~ V_{CC}	V
Output Voltage	V_{OUT}	0 ~ V_{CC}	V
Operating Temperature	T_{OPR}	-40 ~ +85	°C
Input Rise and Fall Time	t_r, t_f	0 ~ 1000 (V _{CC} =2.0V)	ns
		0 ~ 500 (V _{CC} =4.5V)	
		0 ~ 400 (V _{CC} =6.0V)	