

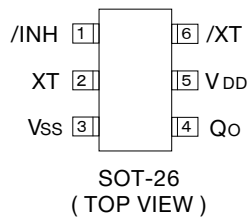
XC2173 Series

ICs for use with Crystal Oscillators (PLL built-in)

General Description

The XC2173 series are high frequency, low power consumption CMOS ICs with built-in crystal oscillator, divider and clock multiplier PLL circuits. Output is selectable from any one of the following values for f_0 : $f_0 \times 5$, $f_0 \times 6$, $f_0 \times 7$, $f_0 \times 8$, $f_0/2$, $f_0/4$, $f_0/8$. With an oscillation capacitor & oscillation feedback resistor built-in, a stable oscillator circuit can be put together using only an external crystal oscillator. By connecting an external standard clock, the above mentioned output frequencies can be achieved.

Pin Configuration



Features

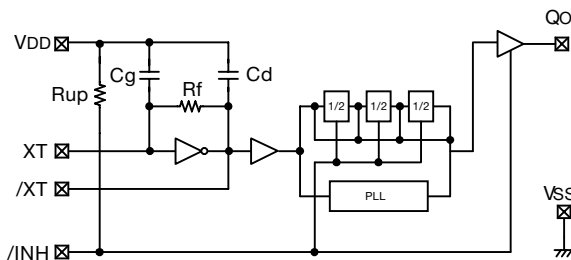
- Oscillation Frequency:** 10MHz to 25MHz
- Divider Ratio:** $f_0/2$, $f_0/4$, $f_0/8$
- Multiplier:** $f_0 \times 5$, $f_0 \times 6$, $f_0 \times 7$, $f_0 \times 8$
- Output:** 3 state
- Operating Voltage Range:** $3.3V \pm 10\%$ and $5.0V \pm 10\%$
- Small Consumption Current:** Stand-by function included*
* oscillation intermittent in stand-by
- Ultra Small Package:** SOT-26 (150mW) mini mold

Pin Assignment

PIN NUMBER	PIN NAME	FUNCTION
1	/INH	Stand-by control*
2	XT	Crystal Oscillator Connection (Input)
3	VSS	GND
4	Qo	Clock Output
5	VDD	Power Supply
6	/XT	Crystal Oscillator Connection (Output) / Standard Clock Input

* Stand-by control pin has pull-up resistance built-in.

Block Diagram



Ordering Information

XC2173①②③④⑤⑥

DESIGNATOR	SYMBOL	DESCRIPTION
①	C	CMOS (VDD/2)
②	M	Multiplier Output
	D	Divider Output
③	Multiplier Ratio or Divider Ratio	
	2	$f_0 / 2$
	4	$f_0 / 4$
	5	$f_0 \times 5$
	6	$f_0 \times 6$
	7	$f_0 \times 7$
	8	$f_0 / 2$ & $f_0 \times 8$
④	Input Oscillation Frequency Range	
	1	10MHz to 25MHz
⑤	M	SOT-26 Package
⑥	R	Embossed Tape (orientation of device : right)
	L	Embossed Tape (orientation of device : left)