



Clock Oscillators

Surface Mount 1.5MHz to 70.0MHz



FEATURES

- HCMOS/TTL compatible.
- Industrial temperature range optional.

ELECTRICAL SPECIFICATIONS

Operating Temperature: 0°C to +70°C (-40°C to +85°C optional for 0.005% (A) and 0.01% (B) stabilities).

Frequency Stability: $\pm 0.01\%$ Standard (0.0025% and 0.005% optional).

Input Voltage: +5.0VDC ± 0.5 V.

Enable Input Voltage: 4.0V minimum.

Disable Input Voltage: 0.8V maximum.

Output Load: 50pF or 10 TTL loads. (1.5 to 50MHz)
30pF or 5 TTL loads (50.001 to 70MHz).

ENVIRONMENTAL SPECIFICATIONS

Temperature Cycle: -55°C to +85°C, 3 cycles.

Shock: 1000g, 0.35 millisecond, 1/2 sine wave, 3 shocks each plane.

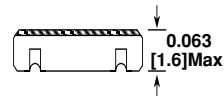
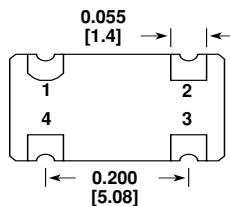
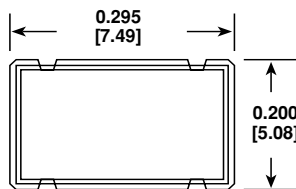
Vibration: 0.06 D.A., 10 - 55Hz, 20g, 55 - 3000Hz.

Humidity: 85% relative humidity at +85°C, 240 hours.

STANDARD ELECTRICAL SPECIFICATIONS

FREQUENCY RANGE (MHz)	INPUT CURRENT (mA) (Max.)	WAVEFORM SYMMETRY @ 50%Vdd	RISE AND FALL TIME (nS) (Max.)	"ZERO" LEVEL Vdd	"ONE" LEVEL Vdd
1.5 to 32.0	20	40/60	5	10%	90%
32.001 to 50.0	35	40/60	5	10%	90%
50.001 to 70.0	60	40/60	5	10%	90%

DIMENSIONAL CONFIGURATIONS [Numbers in brackets indicate millimeters]



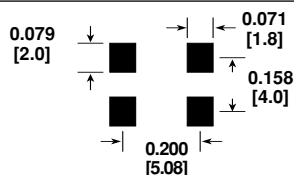
PIN	CONNECTION
1	N.C. or E/D
2	Ground
3	Output
4	+5VDC

ENABLE/DISABLE FUNCTION

*Pin 1 E/D	Pin 3 Output
Open	Active
High (1)	Active
Low (0)	High Z

*An internal pull-up resistor is connected to Pin 1 allowing active output if Pin 1 is left open.

SUGGESTED SOLDER PAD LAYOUT



A 0.01 μ F bypass capacitor should be placed between Vdd and GND to minimize power supply line noise.

PACKAGING

16mm wide tape and reel packaging for automatic assembly.

1000 pieces per reel.

HOW TO ORDER

XOSM-57 MODEL	B FREQUENCY STABILITY	R OTR	E ENABLE/DISABLE	50M FREQUENCY/MHz
	AA = 0.0025% (25PPM) A = 0.005% (50PPM) B = 0.01% (100PPM) Standard	Blank = Standard R = -40°C to +85°C	Blank = Pin 1 open E = Disable to Tristate	