

WJ VC4001

DRO FOR OC-768 SONET APPLICATIONS

- ◆ 40 Gbit/s
- ◆ Output Buffer Amplifier
- ◆ Phase Noise -83dBc/Hz Typ. at 10 kHz offset

Specifications

Characteristics	Typical	Guaranteed	
		+25°C	0°C to +70°C
Frequency ⁽¹⁾ @ Tune Voltage of 0 Volts (MHz)	39813.12	39813.12±8	39813.12±16
Frequency Tuning Range (MHz)	80		
Phase Noise (dBc/Hz)			
1 kHz offset	-58		-33
10 kHz offset	-83		-63
100 kHz offset	-108		-93
1 MHz offset	-133		-113
10 MHz offset	-139		-133
Output Power (dBm)			
Max			+4
Min	+1		-2
Tuning Voltage (Volts)	-5 to +5	-5 to +5	-5 to +5
Modulation Sensitivity (MHz/Volt)			7.0-14.0
Modulation Bandwidth (MHz)			1.5
Harmonic Suppression (dBc)			
nf ₀			-30
n/2f ₀			-40
In-Band Spurious Suppression (dBc)			-65
RF Output VSWR			2.0:1
Frequency Pushing (kHz/Volt)			80
Frequency Pulling ⁽²⁾ (kHz)			400
Bias Current (mA)			
+12V			225
-12V			25

(1) Higher frequency options available to support greater forward error correction

(2) Measured looking into a 2.0:1 load mismatch at all phase angles.

Absolute Maximum Ratings

Operating Temperature0°C to +85°C

Storage Temperature-55°C to +125°C

DC Voltage0 to 15 V

Tune Voltage-12 to +12 V

Weight 4.0oz



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