

VSX40MD23

40 Watt Dual Output Quarter Brick DC/DC Converter









- 2.5V & 3.3V Dual Output
- 2.3" x 1.5" x 0.5"
- 89% Efficiency
- Low Output Noise
- Input Filtering
- Remote On/Off, Input Side
- Output Voltage Trim, +/-10%
- Fixed Frequency Operation
- -40°C to +100°C Baseplate Temp.
- Output Current Limit, Self-Start
- 1,500 Vdc Isolation, Input to Output

- UL/CUL 1950, EN60 950
- 36 to 75 Vdc Input Models
- Continuous Short Circuit Protection
- Non-latching Protection: Input Undervoltage Input Overvoltage Output Overvoltage Overtemperature
- Output Voltage Tracking at Turn-on and Turn-off
- No Minimum Load Current

APPLICATIONS

- Distributed Power Architectures
- Workstations
- EDP Equipment
- Telecommunications

OPTIONS

- Choice of Remote On/Off logic Configuration
- Heatsink Available for Extended Operation

ADDITIONAL INFORMATION

 See Application Note DCAN-41 at www.cdpowerelectronics.com The VSX40 Series are dual output converters with a 36-75V input. The industry quarter-pak size of 2.3" x 1.5" x 0.5" coupled with 89% efficiency is an industry high-density breakthrough.

These converters utilize Vx high density technology. This technology has been featured in our highly efficient VKP and VKA series now successfully in use worldwide. The very high 89% efficiency minimizes the requirement for

heat-sinking and the low output ripple minimizes the need for additional filtering. For maximum flexibility, power can be traded between outputs as required. The VSX40 series feature virtually all of the options required by design engineers but not at the competition's typical additional price for each option. This multitude of features are standard on the VSX40 series.

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	MIN	MAX	UNIT			
Input Voltage:							
VSX40MD23	Vi		100	Vdc			
I/O Isolation Voltage			1500	Vdc			
I/P to case			1500	Vdc			
O/P to case			200	Vdc			
Operating Case Temperature	Т	-40	100	∞			

SPECIFICATIONS, ALL MODELS

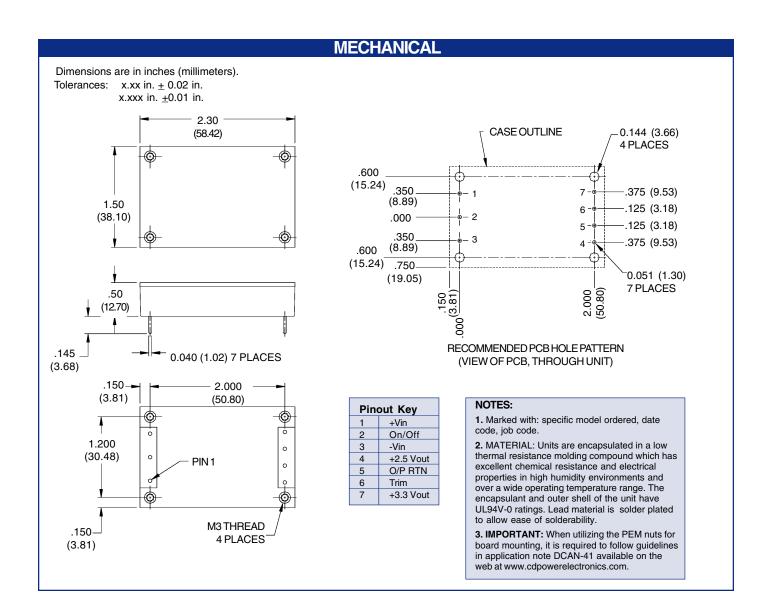
Specifications are at $T_{CASE} = +40$ °C nominal input voltage unless otherwise specified.

	PARAMETER	SYMBOL	MIN	TYP	MAX	UNITS
5	Operating Input Voltage					
	VSX40MD23	Vi	36	48	75	VDC
	Maximum Input Current					
	(Vi=0V to Vi max, lo=lo max)					
一	VSX40MD23	li max			1.5	Α
_	I/P Reflected Ripple Current				260	mA p-p
	No Load Input Current	liNL		35		mA
	On/Off Activated Input Current	liQ		20		mA

	PARAMETER	SYMBOL	MIN	TYP	MAX	UNITS
2	Output voltage (Note 1)					
)	Over all conditions of I/P voltage,					
at c	load and temperature)					
2 H	2.5 Vout (V2)	2.5 Vo	2.375	_	2.555	Vdc
al to	3.3 Vout (V1)	3.3 Vo	3.225	_	3.450	Vdc
nbe	Output Voltage Setpoint					
ō	(Vi=48, Io ₂ =9A, Io ₃ =6A, Tc=25°C)					
ter	2.5 (V2)	2.5 Vo,set	2.450		2.510	Vdc
)rea	3.3 (V1)	3.3 Vo,set	3.310		3.390	Vdc
o, O	Output Ripple and Noise Voltage					
ys k	(peak-to-peak, 100 MHz BW)					
wa	2.5 (V2)	_	_	_	60	mv p-p
_a ≡	3.3 (V1)	_	_	_	80	mv p-p
×	Output Current					
ż	(Total module O/P power should					
о <u>ө</u>	not exceed 40 Watts)					
ltag	2.5 (V2)	lo ₂	-		16	Α
0	3.3 (V1)	lo ₃	_	_	12.12	Α
the	Output Current Limit Inception					
ns,	(Vo=95% of Vo nom)					
iţi	2.5 (V2)	lo ₂ cli	16.8	18.5	21.0	Α
ouo;	3.3 (V1)	lo ₃ cli	12.7	14.0	15.9	Α
ې ک	Output Short Ckt Current					
r ar	(Max impedance across short circuit = $65m\Omega$)					
nde	2.5 Vo		15	19	22	Α
, 5	3.3 Vo		11	13.2	17	Α
5	Efficiency (Vi=48V, Io ₂ =8A,Io ₃ =6A, Tc=40°C)	η	88	89	_	%
OUTPUT Under any conditions, the voltage of V1 will always be greater or equal to that of V2.	Dynamic Response					
	(ΔIo/Δt=0.2A/μsec.Vi=48V, Tc=25°C, either O/P)					
	Load change of 50% lo max; at any operating					
	load up to lomax or Pomax Peak Deviation					
	outside settling point	_		2	_	%Vo nom

NOTE:1. Worst case voltage conditions occur with full load drawn from one output only, zero being drawn from the other. For worst case voltages at less extreme loading conditions, consult the factory.

	PARAMETER	SY	MBOL	MIN	TYP	MAX	UNITS
	Isolation Specifications						
	Isolation Capacitance		_	_	1000	_	pF
	Isolation Resistance		_	10	_	_	MΩ
	Feature Specifications Remote On/Off (open collector equivalent, signal referenced to -Vin terminal) VSx40MD23 Preferred Logic (negative)						
	Logic Low - Module On						
	Logic High - Module Off						
	VSX40MD23-1 - Optional Logic (positive) Logic Low - Module Off Logic High - Module On						
-	Logic Low: At Von/off = 0V	,	Von/off	0	_	50	Vdc
	20gio 20w. 7tt von/on = 0v		Ion/off	Ů	_	200	μΑ
	Turn On Time (Vo within 1% of steady st						, , , , , , , , , , , , , , , , , , ,
	From Application of Vin		_	_	7	10	mSecs
	From Remote On/Off Activation)		_	-	3	4	mSecs
GENERAL	Input Undervoltage Lockout (Turn Off & Turn On Voltages Track)						
Z	Turn On		_	30	33	36	Vdc
넁	Turn Off		-	27	30	33	Vdc
	Input Overvoltage Lockout (Turn Off & Turn On Voltages Track)						
	Turn Off		-	76	80	84	Vdc
	Turn On		_	74.5	78.5	82.5	Vdc
	Output Overvoltage Set Point (Non-latching independent control loop)						
	2.5 Vo		V clamp	2.7	2.9	3.2	Vdc
	3.3 Vo	VO3C	V clamp	3.6	3.9	4.2	Vdc
	Overtemperature Shutdown		Тс	105	115	125	∞
	Hysteresis				10		∞
	Weight VSX40MD23, VSX40MD23-1				67		Grams
	VSX40MD23-U, VSX40MD23-1U						
	Output Trim						
	Tie Trim to +2.5 Vo for trim down 2.5 Vo		O ₂ td	_	-10	_	%
	3.3 Vo		'O ₃ td	_	-10	-	%
	Tie Trim to O/P RTN for trim up 2.5 Vo		O ₂ td	_	10	_	%
	3.3 Vo) V	O ₃ td	_	10	_	%



ORDERING INFORMATION To Find Model Number To Find Part No. VSX40MD23 - 1 U MODEL NO. PART NO. Device Family VSX40MD23 VSX40MD23 VSX40MD23 (Quarter Brick, 40 Watt DC/DC) VSX40MD23-U VSX40MD23-U Logic: No Number = Preferred Logic (Negative); 1 = Optional Logic (Positive) VSX40MD23-1 VSX40MD23-1 Package VSX40MD23-1U VSX40MD23-1U No Letter = Encapsulated; U = Unencapsulated

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