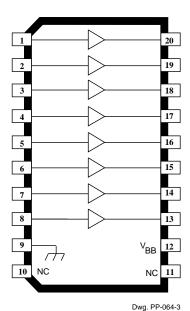
### A6118SLW



### **ABSOLUTE MAXIMUM RATINGS** at $T_A$ = +25°C

Output Current, I<sub>OUT</sub> . . . . . . . . . . . -40 mA Allowable Package Power Dissipation, **P**<sub>D</sub> . . . . . . . . . . . **See Graph** Operating Temperature Range, T<sub>A</sub>....--20°C to +85°C Storage Temperature Range, T<sub>S</sub> . . . . . . . -55°C to +150°C

Caution: The high input impedance of these devices makes them susceptible to static discharge damage associated with handling and testing. Techniques similar to those used for handling MOS devices should be employed.

Consisting of eight npn Darlington output stages and the associated common-emitter input stages, these drivers are designed to interface between low-level digital logic and vacuum fluorescent displays. Both devices are capable of driving the digits and/or segments of these displays and are designed to permit all outputs to be activated simultaneously. Pull-down resistors are incorporated into each output and no external components are required for most fluorescent display applications.

With any device, the output load is activated when the input is pulled towards the positive supply (active 'high'). The UDN6118A is furnished in a standard 18-pin plastic DIP; the A6118SLW is in a 20lead wide-body SOIC. Both units operate over the temperature range of -20°C to +85°C. These devices are also available for operation over the temperature range of  $-40^{\circ}$ C to  $+85^{\circ}$ C by changing the part number to UDQ6118A or A6118ELW.

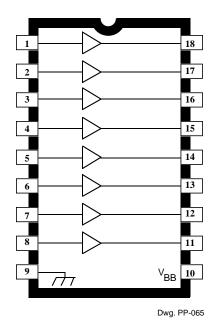
#### **FEATURES**

- Digit or Segment Drivers
- Low Input Current
- Integral Output Pull-Down Resistors
- High Output Breakdown Voltage
- Single or Split Supply Operation
- Automotive Capable

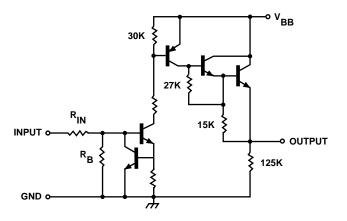
Always order by complete part number, e.g., UDN6118A.



#### **UDN6118A**

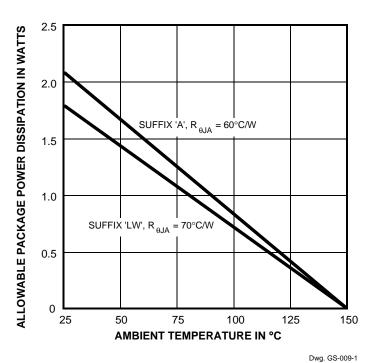


### PARTIAL SCHEMATIC ONE DRIVER (ALL TYPES)



Dwg. No. A-10,592C

R <sub>IN</sub>	R <sub>B</sub>			
10 kΩ	30 kΩ			







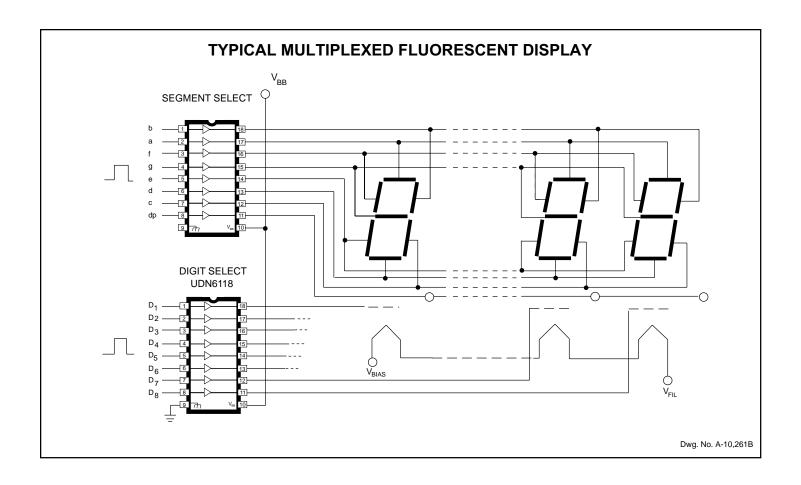
## ELECTRICAL CHARACTERISTICS (over operating temperature range) at $V_{_{\mathrm{BB}}}$ = 80 V.

			Limits			
Characteristic	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Output Leakage Current	I <sub>OUT</sub>	V <sub>IN</sub> = 0.4 V	_	_	15	μΑ
Output OFF Voltage	V <sub>OUT</sub>	V <sub>IN</sub> = 0.4 V	_	_	1.0	V
Output Pull-Down Current	I <sub>OUT</sub>	Input Open, V <sub>OUT</sub> = V <sub>BB</sub>	450	650	1100	μΑ
Output ON Voltage	V <sub>OUT</sub>	V <sub>IN</sub> = 2.4 V, I <sub>OUT</sub> = -25 mA	77	78	_	V
Input ON Current	I <sub>IN</sub>	V <sub>IN</sub> = 2.4 V	_	120	225	μΑ
		V <sub>IN</sub> = 5.0 V	_	375	650	μΑ
Supply Current	I <sub>BB</sub>	All Inputs Open	_	10	100	μΑ
		All Inputs = 2.4 V	-	6.0	9.0	mA

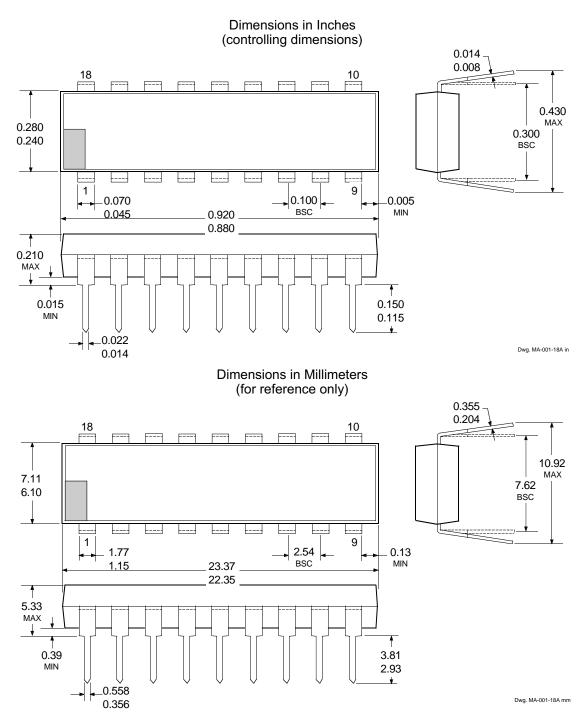
### RECOMMENDED OPERATING CONDITIONS

			Limits			
Characteristic	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Supply Voltage	V <sub>BB</sub>		5.0	_	70	V
Input ON Voltage	V <sub>IN</sub>		2.4	_	15	V
Output ON Current	I <sub>OUT</sub>		_	_	-25	mA

NOTE: Positive (negative) current is defined as going into (coming out of) the specified device terminal.



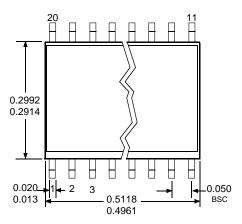
### **UDN6118A**

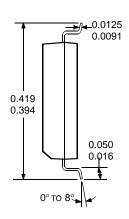


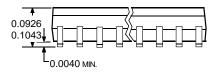
- NOTES: 1. Exact body and lead configuration at vendor's option within limits shown.
  - 2. Lead spacing tolerance is non-cumulative.
  - 3. Lead thickness is measured at seating plane or below.
  - 4. Supplied in standard sticks/tubes of 21 devices.

### **A6118SLW**

# Dimensions in Inches (for reference only)

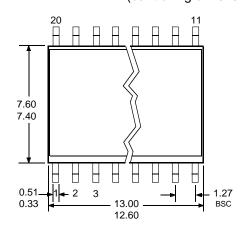


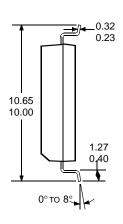


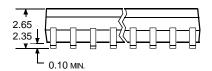


Dwg. MA-008-20 in

# Dimensions in Millimeters (controlling dimensions)







Dwg. MA-008-20 mm

- NOTES:1. Exact body and lead configuration at vendor's option within limits shown.
  - Lead spacing tolerance is non-cumulative.
  - 3. Supplied in standard sticks/tubes of 37 devices or add "TR" to part number for tape and reel.



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### HIGH-VOLTAGE (≥60 V) PERIPHERAL POWER AND DISPLAY DRIVERS

### IN ORDER OF 1) OUTPUT VOLTAGE, 2) OUTPUT CURRENT, 3) NUMBER OF DRIVERS

Output Ratings*		Features						
V	mA	#	Serial Input	Latched Drivers	Diode Clamp	Saturated Outputs	Internal Protection	Part Number †
60	-25	8	-	Χ	_	_	_	5815
	-25	10	X	X	Active Pull-Dov	vn –	_	5810-F and 6810
	-25	12	X	X	Active Pull-Dov	vn –	_	5811 and 6811
	-25	20	X	X	Active Pull-Dov	vn –	_	5812-F and 6812
	-25	32	X	X	Active Pull-Dov	vn –	_	5818-F and 6818
	300	4	_	_	X	Χ	X	2557
	600	4	_	_	_	Χ	X	2547
	600	4	_	_	X	Χ	X	2549
	700	4	_	_	X	Χ	X	2559
	700	4	_	_	X	Χ	X	2543
	4000	4	_	_	Х	_	_	2944
80	-350	8	_	_	X	_	_	2983
	350	8	X	X	_	_	_	5822
	350	8	X	X	X	_	_	5842
	-350	8	Χ	Χ	X	_	_	5890
95	300	7	_	_	X	_	_	2023
	350	7	_	_	X	_	_	2024

<sup>\*</sup> Current is maximum test condition; voltage is absolute maximum allowable. Negative current is defined as coming out of (sourcing) the output.



<sup>†</sup> Complete part number includes additional characters to indicate operating temperature range and package style.