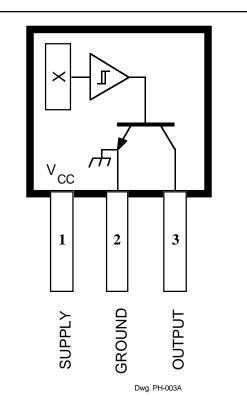
3175 AND 3177

HALL-EFFECT LATCHES



Pinning is shown viewed from branded side.

ABSOLUTE MAXIMUM RATINGS

Supply Voltage, V _{CC} 18 V
Reverse Battery Voltage, V _{RCC} 18 V
Magnetic Flux Density, B Unlimited
Output OFF Voltage, V _{OUT} 18 V
Continuous Output Current, I _{OUT} . 15 mA
Operating Temperature Range,
T_A 20°C to +85°C
Storage Temperature Range,
$T_S \dots -65^{\circ}C$ to +150°C

These Hall-effect latches are temperature-stable and stress-resistant sensors especially suited for electronic commutation in brushless dc motors using multipole ring magnets. Each device includes a voltage regulator, quadratic Hall voltage generator, temperature compensation circuit, signal amplifier, Schmitt trigger, and an open-collector output on a single silicon chip. The on-board regulator permits operation with supply voltages of 4.5 volts to 18 volts. The switch output can sink 10 mA. With suitable output pull up, they can be used directly with bipolar or MOS logic circuits.

The three package styles available provide a magnetically optimized package for most applications. Suffix 'LT' is a surface-mount SOT-89/ TO-243AA package; suffixes 'U' and 'UA' feature wire leads for through-hole mounting.

FEATURES

- Symmetrical Response
- 4.5 V to 18 V Operation
- Open-Collector Output
- Reverse Battery Protection
- Activate With Small, Commercially Available Permanent Magnets
- Solid-State Reliability
- Small Size
- Superior Temperature Stability
- Resistant to Physical Stress

The UGN3175U is last-time buy. Orders accepted until October 28, 2002.

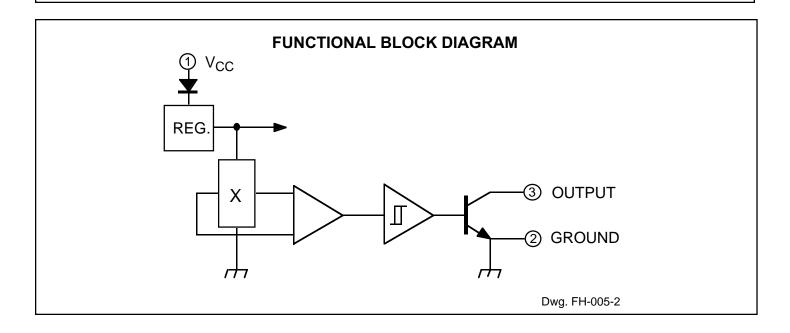
The UGN3177U is discontinued — shown for reference only.

Always order by complete part number, e.g., UGN3175LT.

See Magnetic Characteristics table for differences between devices.



3175 AND 3177 HALL-EFFECT LATCHES



ELECTRICAL CHARACTERISTICS at $T_A = +25^{\circ}C$, $V_{CC} = 4.5 \text{ V}$ to 18 V (unless otherwise noted).

			Limits			
Characteristic	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Supply Voltage	V _{CC}	Operating	4.5	_	18	V
Output Saturation Voltage	V _{OUT(SAT)}	V _{CC} = 18 V, I _{OUT} = 10 mA, B > B _{OP}	_	200	300	mV
Output Leakage Current	I _{OFF}	V _{OUT} = 18 V, B < B _{RP}	_	0.05	5.0	μΑ
Supply Current	I _{cc}	V _{CC} = 4.5 V, B < B _{RP} (Output OFF)	_	5.0	10	mA
Output Rise Time	t _r	V_{CC} = 12 V, R_{L} = 1.1 k Ω , C_{L} = 20 pF	_	0.04	2.0	μs
Output Fall Time	t _f	V_{CC} = 12 V, R_L = 1.1 k Ω , C_L = 20 pF	_	0.18	2.0	μs

MAGNETIC CHARACTERISTICS in gauss; $V_{CC} = 4.5 \text{ V}$ to 18 V.

	Part	T _A = +25°C			T _A = -20°C to +85°C			
Characteristic	Number*	Min.	Тур.	Max.	Min.	Тур.	Max.	
Operate Point, B _{OP}	UGN3175	25		170	15		180	
	UGN3177	50	_	150	25	_	150	
Release Point, B _{RP}	UGN3175	-170	_	-25	-180	_	-15	
	UGN3177	-150		-50	-150		-25	
Hysteresis, B _{hys}	UGN3175	100	200	_	80	180	_	
	UGN3177	100	200	_	50	180	_	

NOTE: As used here, negative flux densities are defined as less than zero (algebraic convention). Complete part number includes a suffix denoting package type (LT, U, or UA).



3175 AND 3177 HALL-EFFECT LATCHES

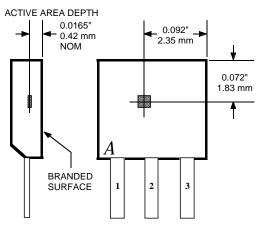
SENSOR LOCATIONS

(±0.005" [0.13mm] die placement)

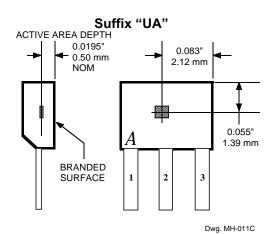
Suffix "LT" ACTIVE AREA DEPTH 0.0305" 0.775 mm NOM 2.30 mm 0.045" 1.15 mm

Dwg. MH-008-1C

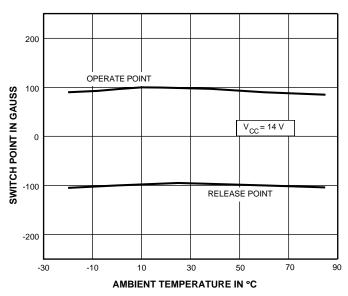
Suffix "U"



Dwg. MH-002-3C



TYPICAL OPERATING CHARACTERISTICS



Dwg. GH-020

The products described herein are manufactured under one or more of the following U.S. patents: 5,045,920; 5,264,783; 5,442,283; 5,389,889; 5,581,179; 5,517,112; 5,619,137; 5,621,319; 5,650,719; 5,686,894; 5,694,038; 5,729,130; 5,917,320; and other patents pending.

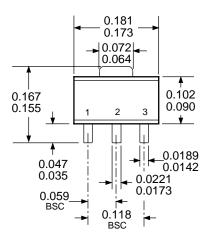
Allegro MicroSystems, Inc. reserves the right to make, from time to time, such departures from the detail specifications as may be required to permit improvements in the performance, reliability, or manufacturability of its products. Before placing an order, the user is cautioned to verify that the information being relied upon is current.

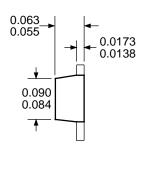
Allegro products are not authorized for use as critical components in life-support appliances, devices, or systems without express written approval.

The information included herein is believed to be accurate and reliable. However, Allegro MicroSystems, Inc. assumes no responsibility for its use; nor for any infringements of patents or other rights of third parties that may result from its use.

PACKAGE DESIGNATOR 'LT'

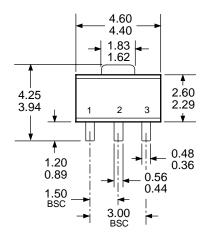
Dimensions in Inches (for reference only)

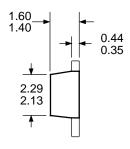




Dwg. MA-009-3A in

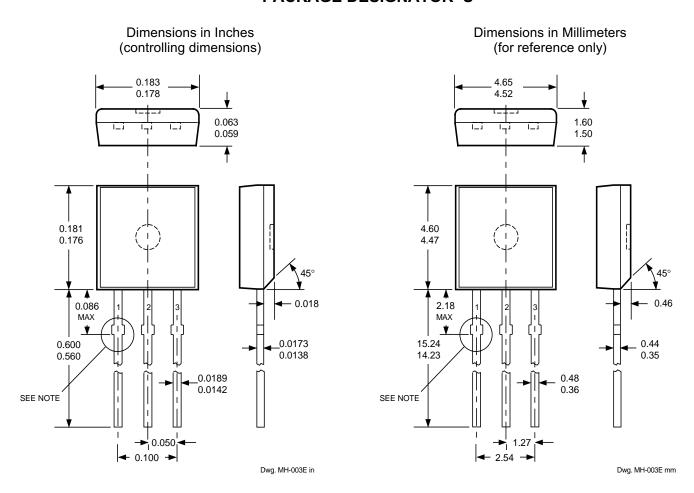
Dimensions in Millimeters (controlling dimensions)





Dwg. MA-009-3A mm

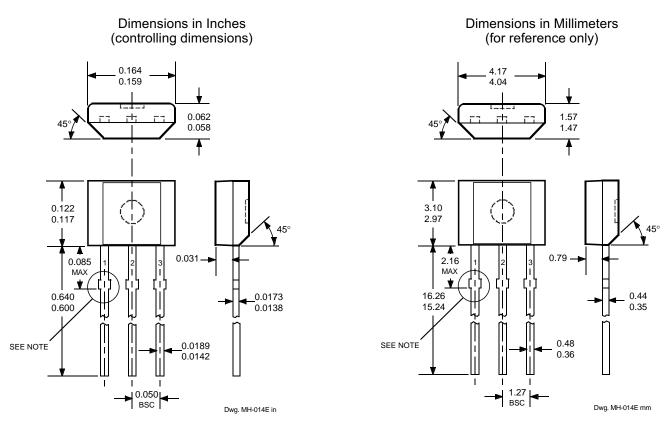
PACKAGE DESIGNATOR 'U'



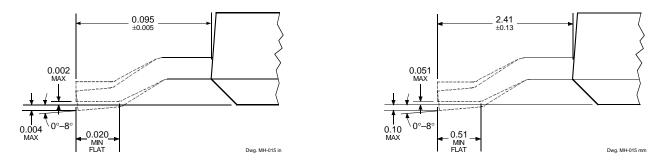
Devices in the 'U' package are NOT RECOMMENDED FOR NEW DESIGN

- NOTES: 1. Tolerances on package height and width represent allowable mold offsets. Dimensions given are measured at the widest point (parting line).
 - 2. Exact body and lead configuration at vendor's option within limits shown.
 - 3. Height does not include mold gate flash.
 - 4. Recommended minimum PWB hole diameter to clear transition area is 0.035" (0.89 mm).

PACKAGE DESIGNATOR 'UA'



Surface-Mount Lead Form (add '-TL' or '-TS' to part number).



NOTES: 1. Tolerances on package height and width represent allowable mold offsets. Dimensions given are measured at the widest point (parting line).

- 2. Exact body and lead configuration at vendor's option within limits shown.
- 3. Height does not include mold gate flash.
- 4. Recommended minimum PWB hole diameter to clear transition area is 0.035" (0.89 mm).
- 5. Where no tolerance is specified, dimension is nominal.
- 6. Supplied in bulk pack (no suffix or suffix '-TL', 500 pieces per bag) or tape and reel (suffix '-TS', 4000 devices per reel).

