

UTC 51494

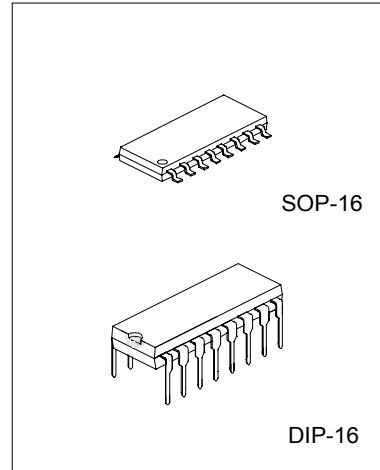
LINEAR INTEGRATED CIRCUIT

DESCRIPTION

The UTC 51494 is a monolithic bipolar integrate circuit that provides both 494 function and built in power good signal circuit for easy using 51494 can be easily implemented by just adding a capacitor.

FEATURES

- *Fully integrated with compact 16-pin dip
- *All necessary functions included for most popular half bridge circuit.
- *Built-in power good delay and power fail lead function.
- *Power good delay time is linearly.
- *Proportional to external capacitor value.
- *Reduced external components for cost down and components for cost down and compact size.



MAXIMUM RATINGS(Unless otherwise noted ,all is over operating free-air temperature Range)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	V _{cc}	42	V
Voltage from any pin to ground (except pin8 & pin11)	V _{in}	V _{cc} +0.3	V
Collector Output Voltage	V _{c1} , V _{c2}	42	V
Peak Collector Output	I _{c1} , I _{c2}	250	mA
Power dissipation	P _D	1500	mW
Operating Temperature Range	T _{opr}	0 to +70	°C
Storage Temperature Range	T _{sag}	-65 to +150	°C

UTC

UNISONIC TECHNOLOGIES CO. LTD

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QW-R103-005,A

ELECTRICAL CHARACTERISTICS

(Unless otherwise specified, $T_a=0\sim70^{\circ}\text{C}$, $V_{CC}=15\text{V}$, $f=10\text{kHz}$.)

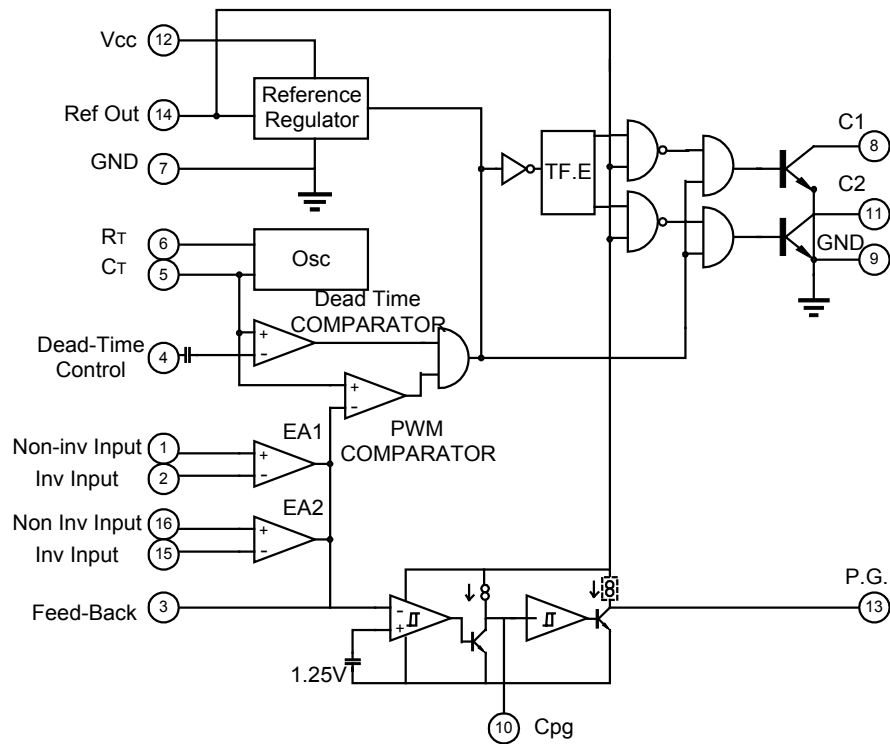
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reference Section						
Reference voltage	Vref	Iref = 1.0mA	4.75	5	5.25	V
Line Regulation	Vline	$7\text{V} < V_{CC} < 40\text{V}$		2	25	mV
Load Regulation	Vload	$1\text{mA} < I_{\text{ref}} < 5\text{mA}$		1	15	mV
Temperature Coefficient		$0^{\circ}\text{C} < T_a < 70^{\circ}\text{C}$		0.01	0.03	%/ $^{\circ}\text{C}$
Oscillator section						
Oscillator Frequency	Fosc	$C_T=0.01\mu\text{F}$, $R_T=12\text{k}\Omega$		10		kHz
Oscillator Frequency Change Over Operating Temperature Range	Δf_{osc}	$C_T=0.01\mu\text{F}$, $R_T=12\text{k}\Omega$			2	%
Dead Time Control Section						
Input Bias Current (Pin 4)	lib(dt)	$V_{CC}=15\text{V}$; $0\text{V} < V_4 < 5.25\text{V}$		-2	-10	μA
Maximum Duty cycle, Each output	Dc(max)	$V_{CC}=15\text{V}$; Pin 4 = 0V Output Control Pin = Vref	43		45	%
Input Threshold Voltage : Zero Duty Max Duty	Vth		0	3	3.3	V
Error Amplifier Section						
Input offset Voltage	Vics	$V_3=2.5\text{V}$		2	10	mV
Input offset Current	Iics	$V_3=2.5\text{V}$		25	250	nA
Input bias Current	lib	$V_3=2.5\text{V}$		0.2	1	μA
Input Common-mode Voltage Range	Vicr	$7\text{V} < V_{CC} < 40\text{V}$	-0.3		V_{CC}	V
Large Signal Open-Loop Voltage Range	Gvo	$0.5\text{V} < V_3 < 3.5\text{V}$	60	74		dB
Unity-Gain Band width	fc			650		kHz
Output Section						
Collector off-state current	Ic(off)	$V_{CC}=V_C=40\text{V}$, $V_E=0$		2	100	μA
Emitter off-state Current	Ie(off)	$V_{CC}=V_C=40\text{V}$, $V_E=0$			-100	μA
Output Saturation Voltage Common-Emitter	Vce(sat)	$V_E=15\text{V}$, $I_C=200\text{mA}$		1.1	1.3	V
Output Control (pin13)						
Standby Power Supply Current	Icc			6	10	mA
Output AC Characteristic						
Raise Time Common-Emitter	Tr			100	200	ns
Fall Time Common-Emitter	Tr			25	100	ns
PWM Comparator Section						
Inhibit Threshold Voltage	Vthi	Zero Duty cycle		4	4.5	V
Output Source Current	Io+	$0.5\text{V} < V_3 < 3.5\text{V}$	2			mA
Output Sink Current	Io-	$0.5\text{V} < V_3 < 3.5\text{V}$	-0.2	-0.6		mA

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Power Good Section						
Power Good Delay Time	tpd	Cd = 1 μ	230	280	330	ms
		Cd = 0.47 μ	108	130	160	
Power Fail Lead Time	Tp1			4		ms
Output High Voltage	Voh	Vpinn = 5V, IL = 1mA	4.75			V
Output Saturation Voltage	Vsat	Vpinn = 5V, Isink = 4mA			0.4	V
Output Leakage Current	Ioh				100	μ A

PIN DESCRIPTION

PIN	NAME	FUNCTION
1	EA1+	Error amplifier noninverting input, same as pin 1 of 494
2	EA1-	Error amplifier inverting input, same as pin 2 of 494
3	EA0	Error amplifier output and feedback, same as pin 3 of 494
4	DT	Dead time control input, same as pin 4 of 494
5	CT	Connect capacitor to oscillator circuit for operating frequency, same as pin 5 of 494
6	RT	Connect resistor to oscillator circuit for operating frequency, same as pin 6 of 494
7	GND	Ground terminal of IC, same as pin 7 of 494
8	C1	Collector of output transistor one, same as pin 8 of 494
9	GND	Ground terminal of IC
10	Cpg	Terminal for capacitor to determine power good delay time
11	C2	Collector of output transistor two, same as pin 11 of 494
12	Vcc	Supply voltage, same as pin 12 of 494
13	P.G.	Output for power good signal
14	Vref	Reference voltage output, same as pin 14 of 494
15	EA2-	Error amplifier inverting input, same as pin 15 of 494
16	EA+	Error amplifier noninverting input, same as pin 16 of 494

BLOCK DIAGRAM



LINEAR INTEGRATED CIRCUIT

