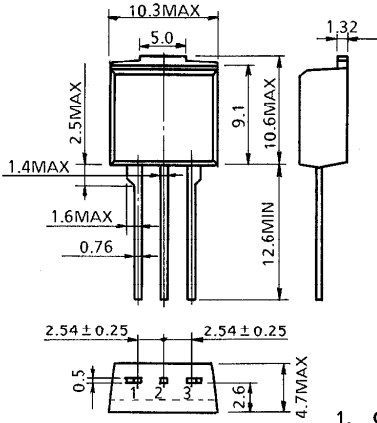
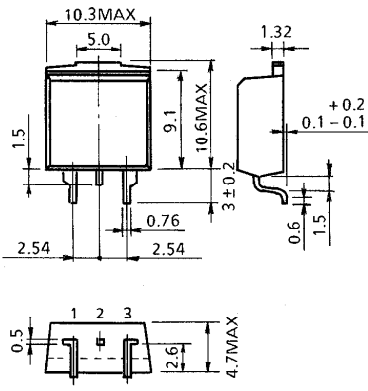


TOSHIBA THYRISTOR SILICON PLANAR TYPE

SF8G48, SF8J48, USF8G48, USF8J48

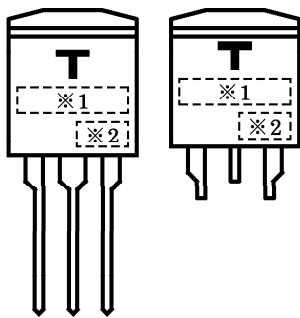
MEDIUM POWER CONTROL APPLICATIONS.

- Repetitive Peak Off-State Voltage : V_{DRM}
Repetitive Peak Reverse Voltage : V_{RRM} } = 400, 600V
- Average On-State Current : $I_{T(AV)}=8A$
- Gate Trigger Current : $I_{GT}=10mA$ Max.

SF8G48-SF8J48	USF8G48-USF8J48
 1. CATHODE 2. ANODE 3. GATE	 1. CATHODE 2. ANODE (BACK SIDE) 3. GATE
JEDEC —	JEDEC —
EIAJ —	EIAJ —
TOSHIBA 13-10J1B	TOSHIBA 13-10J2B

Weight : 1.7g

MARK



※ 1	MARK	F8G48	TYPE	SF8G48, USF8G48
		F8J48	NAME	SF8J48, USF8J48
※ 2	Lot Number			
	□ □ ← Month (Starting from Alphabet A) ↑ Year (Last Number of the Christian Era)			

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● TOSHIBA is continually working to improve the quality and the reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to observe standards of safety, and to avoid situations in which a malfunction or failure of a TOSHIBA product could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent products specifications. Also, please keep in mind the precautions and conditions set forth in the TOSHIBA Semiconductor Reliability Handbook.

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT
Repetitive Peak Off-State Voltage and Repetitive Peak Reverse Voltage	SF8G48	V _{DRM} V _{RRM}	400	V
	USF8G48			
	SF8J48		600	
	USF8J48			
Non-Repetitive Peak Reverse Voltage (Non-Repetitive<5ms T _j =0~125℃)	SF8G48	V _{RSM}	500	V
	USF8G48			
	SF8J48		720	
	USF8J48			
Average On-State Current		I _{T(AV)}	8	A
R.M.S On-State Current		I _{T(RMS)}	12.6	A
Peak One Cycle Surge On-State Current (Non-Repetitive)		I _{TSM}	120 (50Hz)	A
			132 (60Hz)	
I ² t Limit Value (t=1~10ms)		I ² t	72	A ² s
Critical Rate of Rise of On-State Curren (Note 1)		di /dt	100	A / μs
Peak Gate Power Dissipation		P _{GM}	5	W
Average Gate Power Dissition		P _{G(AV)}	0.5	W
Peak Forward Gate Voltage		V _{FGM}	10	V
Peak Reverse Gate Voltage		V _{RGM}	−5	V
Peak Forward Gate Current		I _{GM}	2	A
Junction Temperature		T _j	−40~125	℃
Strage Temperature Range		T _{stg}	−40~125	℃

(Note1) V_{DRM} = 0.5 × Rated
I_{TM} ≤ 25A
T_{gw} ≥ 10 μs
T_{gr} ≤ 250ns
i_{gp} = I_{GT} × 2.0

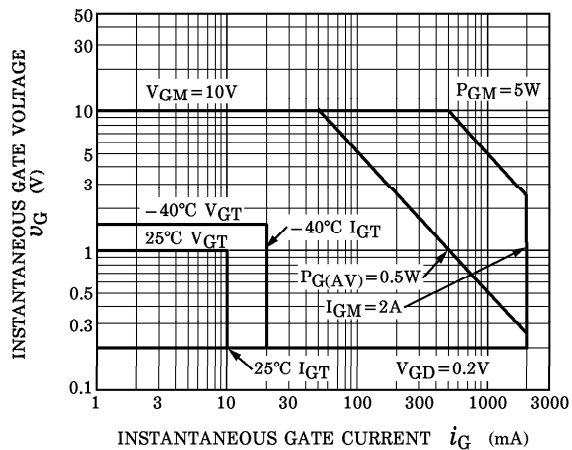
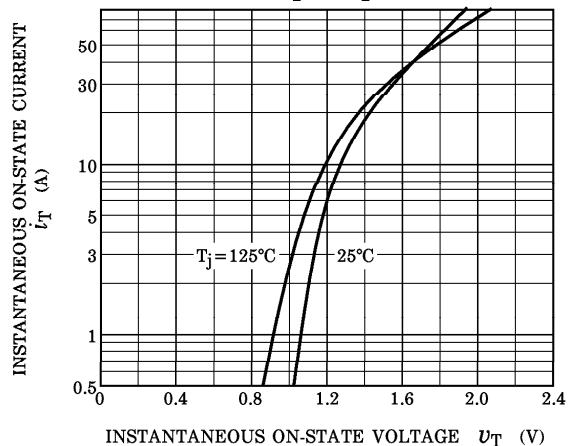
ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Repetitive Peak Off-State Current and Repetitive Peak Reverse	I _{DRM} I _{RRM}	V _{DRM} = V _{RRM} = Rated	—	—	10	μA
Peak On-State Voltage	V _{TM}	I _{TM} = 25A	—	—	1.5	V
Gate Trigger Voltage	V _{GT}	V _D = 6V, R _L = 10Ω	—	—	1.0	V
Gate Trigger Current	I _{GT}		—	—	10	mA
Gate Non-Trigger Voltage	V _{GD}	V _D = Rated × 2 / 3, T _c = 125°C	0.2	—	—	V
Critical Rate of Rise of Off-State Voltage	dv / dt	V _{DRM} = Rated, T _c = 125°C Exponential Rise	—	50	—	V / μs
Holding Current	I _H	V _D = 6V, I _{TM} = 1A	—	—	40	mA
Latching Current	I _L	V _D = 6V, f = 50Hz T _{gw} = 50 μs, i _G = 30mA	—	—	50	mA
Thermal Resistance	R _{th(j-c)}	Junction to Case, DC	—	—	2.8	°C / W

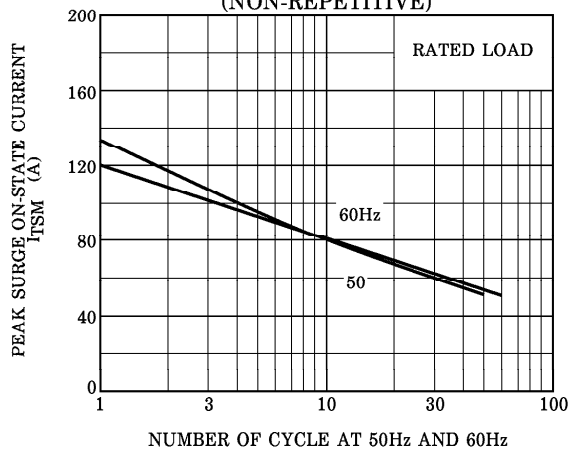
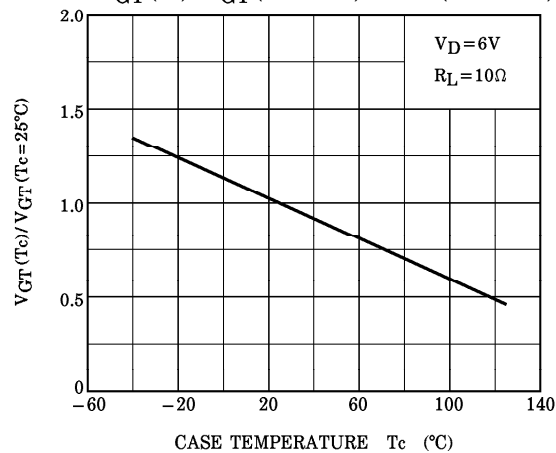
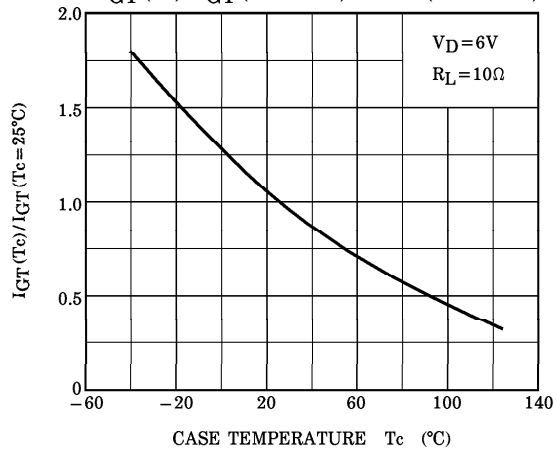
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- The information contained herein is subject to change without notice.

GATE TRIGGER CHARACTERISTIC

 $i_T - v_T$ 

SURGE ON-STATE CURRENT (NON-REPETITIVE)

 $V_{GT}(T_c) / V_{GT}(T_c = 25^\circ C) - T_c$ (TYPICAL) $I_{GT}(T_c) / I_{GT}(T_c = 25^\circ C) - T_c$ (TYPICAL) $I_H(T_c) / I_H(T_c = 25^\circ C) - T_c$ (TYPICAL)