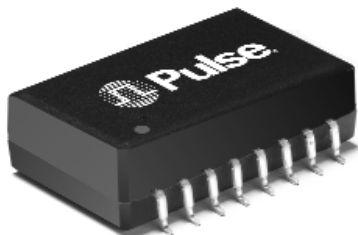


# ISDN S-INTERFACE LOW PROFILE DUAL SMT TRANSFORMERS



- ⊕ Meets pulse waveform template of CCITT I.430 when recommended transformer and chip pair are used
- ⊕ Excellent longitudinal balance
- ⊕ 2 KVrms isolation voltage
- ⊕ Available in tape and reel, or tube packaging
- ⊕ 235°C peak solder reflow temperature rating
- ⊕ Recognized to UL 1459 and UL 1950
- ⊕ Approval to EN60950 (pending)

## Electrical Specifications @ 25°C — Operating Temperature 0°C to 70°C

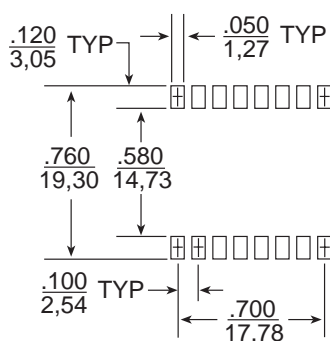
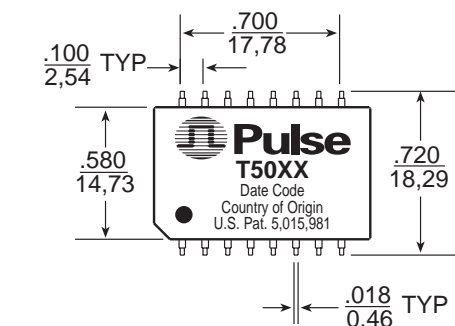
| Part Number | Ratio <sup>A</sup> (±2%) |       | OCL Pri (mH MIN) | L <sub>L</sub> Sec (μH MAX) |    | C <sub>w/w</sub> (pF MAX) | CD Pri <sup>B</sup> (pF MAX) |     | DCR Pri (Ω ±25%) |     | DCR Sec (Ω ±25%) |     | Δ I <sub>bc</sub> <sup>C</sup> (mA MAX) |
|-------------|--------------------------|-------|------------------|-----------------------------|----|---------------------------|------------------------------|-----|------------------|-----|------------------|-----|---|
|             | A                        | B     |                  | A                           | B  |                           | A                            | B   | A                | B   | A                | B   |   |
| T5005       | 1:2.5                    | 1:2.5 | 22               | 40                          | 40 | 100                       | 110                          | 110 | 2.4              | 2.4 | 5.3              | 5.3 | 1                                       |
| T5006       | 1:2                      | 1:2   | 22               | 11                          | 11 | 100                       | 180                          | 180 | 2.4              | 2.4 | 4.4              | 4.4 | 1                                       |
| T5007       | 1:2                      | 1:2   | 22               | 5                           | 5  | 85                        | 80                           | 80  | 2.4              | 2.4 | 4.2              | 4.2 | 1                                       |
| T5008       | 1:1                      | 1:1   | 22               | 5                           | 5  | 100                       | 42                           | 42  | 2.4              | 2.4 | 2.4              | 2.4 | 1                                       |
| T5009       | 1:1                      | 1:2   | 22               | 4                           | 5  | 100                       | 42                           | 80  | 2.4              | 2.4 | 2.4              | 4.2 | 1                                       |
| T5010       | 1:1.8                    | 1:1.8 | 22               | 15                          | 15 | 100                       | 160                          | 160 | 2.4              | 2.4 | 3.8              | 3.8 | 1                                       |

U.S. Patent No. 5,015,981

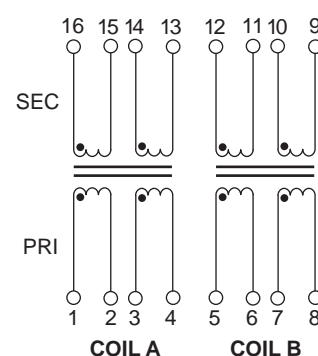
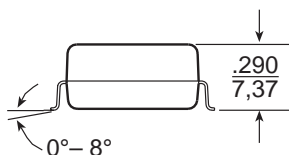
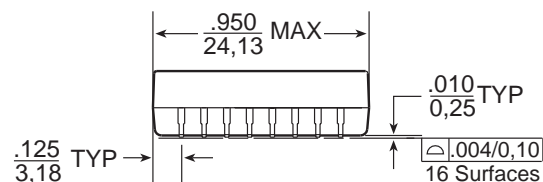
## Mechanical

## Schematic

### TSJ



### SUGGESTED PAD LAYOUT



Dimensions:  $\frac{\text{Inches}}{\text{mm}}$

Unless otherwise specified all tolerances are  $\pm .010$   
 $0,25$

(See other side for packaging information)

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PRELIMINARY T613.P (11/98)

# ISDN S-INTERFACE LOW PROFILE DUAL SMT TRANSFORMERS



| IC Manufacturer       | IC Part Number                                  | Dual Surface Mount<br>Trans & Rec |
|-----------------------|---|-----------------------------------|
| AMD                   | Am 79C30A/79C32A                                | T5006                             |
| AT&T/Lucent           | T7250/7256/7259<br>T7903                        | T5005<br>T5007                    |
| Mietec                | MTC-2072  | T5007                             |
| Mitel                 | MT8930  | T5007                             |
| Motorola              | MC145474/145475<br>MC145574                     | T5008<br>T5005                    |
| National <sup>D</sup> | TP3420/3421                                     | T5007                             |
| SGS Thomson           | ST5420, ST5421                                  | T5007                             |
| Siemens               | PEB 2080/2081/2085<br>PSB 2186<br>PEB 2084/2086 | T5007                             |
| Yamaha                | 7405B, YTD421                                   | T5007                             |
| Intel                 | 29C53   | T5010                             |

## Notes

- A center tapped winding can be created by connecting two ends of a split center winding together on the printed circuit board. In ISDN-S applications, the primary winding is the line side transformer winding.
- Minimum primary inductance and maximum distributed capacitance satisfy the transmitter output and receiver input impedance requirements of CCITT I.430 for both the TE and the NT. The maximum distributed capacitance allows sufficient margin for the capacitance of the IC and a protection diode network. It is consistent with the overall maximum value specified and the permitted length of the basic access TE cord.
- The maximum specified unbalanced DC current capability is based on 20 mH minimum primary OCL.
- National recommends a 1:2 receive transformer, but used as a 1:1 ratio by connecting only half the secondary winding.
- Standard packaging for all transformers on this data sheet is anti-static tubes. Optional anti-static tray packaging can be ordered by adding an "R" suffix to part numbers, (i.e: T5005R). Optional Tape & Reel packaging can be ordered by adding a "T" suffix, (i.e: T5005T).

## Packaging Information

| Type | Part Weight | Parts/Tube | Parts/Tray <sup>E</sup> | Parts/Reel <sup>E</sup> | Reel Diameter | Tape Width | Pitch |
|------|-------------|------------|-------------------------|-------------------------|---------------|------------|-------|
| SMT  | 7 grams     | 20         | 50                      | 200                     | 13 inches     | 44 mm      | 24 mm |

## Application Notes

**1. General Information** – The S-interface is the standardized four-wire digital telephone access point defined by the CCITT I-series recommendations for the Integrated Services Digital Network. This "basic rate access" accommodates two 64 Kbps "B-channels" for information, one 16 Kbps "D-channel" intended for signaling and control, and 48 Kbps for framing and other purposes, giving a total rate of 192 Kbps. The CCITT physical layer recommends that the user network interface be transformer coupled.

The transformer provides isolation for the line card or the terminal from the line. It is also a way to provide phantom power feeding to the terminal over the S-loop. Each end requires a transmit and a receive transformer. Chokes are used in some applications to reduce common mode noise.

The transformers described in this data sheet are matched to the transceivers offered by the major IC manufacturers listed. The use of a transformer-chip pair assures that all requirements of CCITT I.430 are met with respect to pulse waveform templates, impedance and longitudinal balance. In addition, the transformers provide the isolation voltages required by the regulatory agencies and are capable of passing surge voltage tests.

- 2. Safety Standards Recognition** – All transformers listed in this data sheet are recognized to UL 1459 and UL 1950 as a basic insulation barrier.
- 3. Flammability** – Materials used in the products are recognized UL94-VO. Products meet the requirements of IEC 695-2-2 (needle flame test).
- 4.** For more application notes please refer to data sheet **T604**.

## Common Mode Chokes

Please refer to data sheet **G002** for selection of low and high frequency common mode chokes.

## For More Information :

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### Distributor

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