

REV. Status
PRELIMINARY
02/03/00 MP

HDSL transformer designed for use with Brooktree HD SL IC chips BT8921/BT8970 at 1168kbps rates

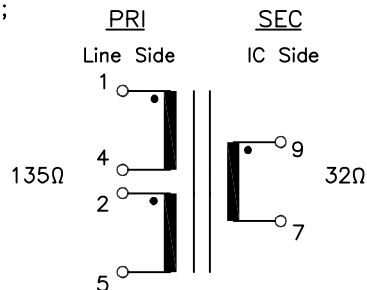
A. Electrical Specifications (@ 25°C)

1. Primary (Line side) Impedance; 135 Ω
2. Secondary (IC side) Impedance; 32 Ω
3. Longitudinal Balance; 50dBm MIN @ 40KHz TO 300KHz, 0dBm
4. Frequency Response; ± 0.1 dB @ 33KHz to 110KHz, 0dBm, 40KHz reference
5. Insertion Loss; 0.5dB MAX @ 40KHz, 0dBm
6. Total Harmonic Distortion; -70dB MAX @ 20KHz, 1.85Vrms
7. Primary Open Circuit Inductance; 2.0mH $\pm 10\%$ @ 10KHz, 0.1Vrms Measured (1-5) with 2 and 4 shorted
8. Leakage Inductance; 11 μ H MAX @ 100KHz, 0.1Vrms Measured (1-5) with 2 & 4, and 7 & 9 shorted
9. DC Current; 160mA
10. DC Resistance; (1-4)=1.25 Ω $\pm 10\%$
(2-5)=1.25 Ω $\pm 10\%$
(7-9)=1.25 Ω $\pm 10\%$
11. Turns Ratio; (1-4):(2-5)=1:1.00 $\pm 2\%$
(7-9):(1-5)=1:2.00 $\pm 2\%$
2 & 4 shorted
12. Dielectric Strength; 1875Vrms 1 second @ Pri to Sec

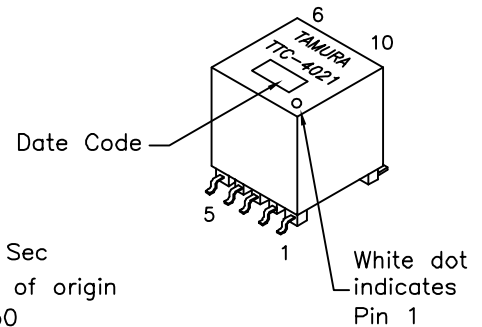
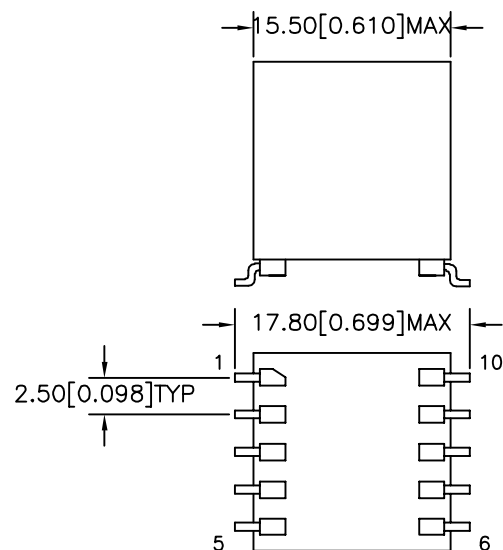
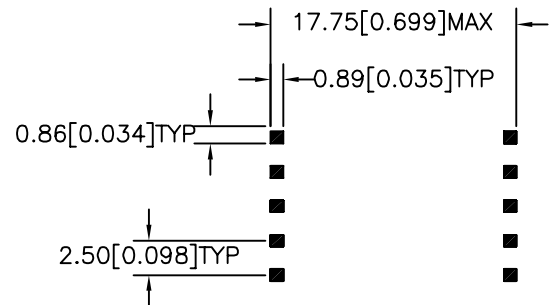
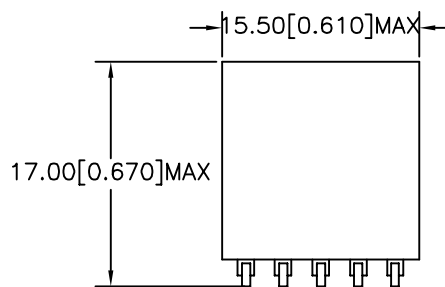
B. Marking; TTC-4021, TAMURA, date code and country of origin

C. Safety; Complies with UL1950 3rd Edition and IEC950

D. Schematic;



E. Mechanical Specifications;



MODEL NUMBER

TTC-4021

PREPARED BY:

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ENGINEER:

M. Pitchai

QUALITY CONTROL:

D. Kelley

APPROVED:

J. Coleman

DWG CONTROL NO.
P-A1-12349
ACAD\TTC\A1123491.DWG

REV
PRE

HD SL
TRANSFORMER

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TTC-4021

MODEL SPECIFICATION

DIM: mm(In) SCL: 2/1 SH: 1 OF 1

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