Switched Capacitor Voltage Converter

FEATURES

- **Charge Pump in 5-Pin SOT-23A Package**
- **>95% Voltage Conversion Efficiency**
- **Voltage Inversion and/or Doubling**
- **Low 38µA Quiescent Current**
- **Operates from +1.8V to +5.5V**
- **Up to 25mA Output Current**
- **Only Two External Capacitors Required**
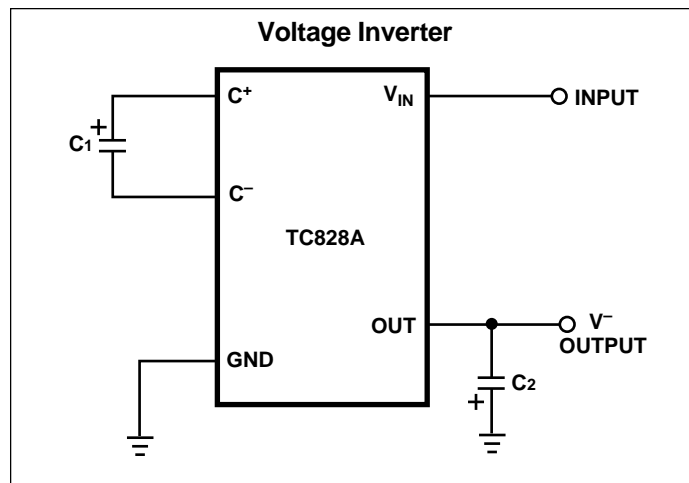- **Lower Power Version of TCM828**

APPLICATIONS

- **LCD Panel Bias**
- **Cellular Phones**
- **Pagers**
- **PDAs, Portable Dataloggers**
- **Battery-Powered Devices**

PIN CONFIGURATION

5-Pin SOT-23A

OUT	1	5	C+
VIN	2		
		TC828ECT	
C−	3	4	GND

NOTE: *5-Pin SOT-23A is equivalent to EIAJ SC-74A
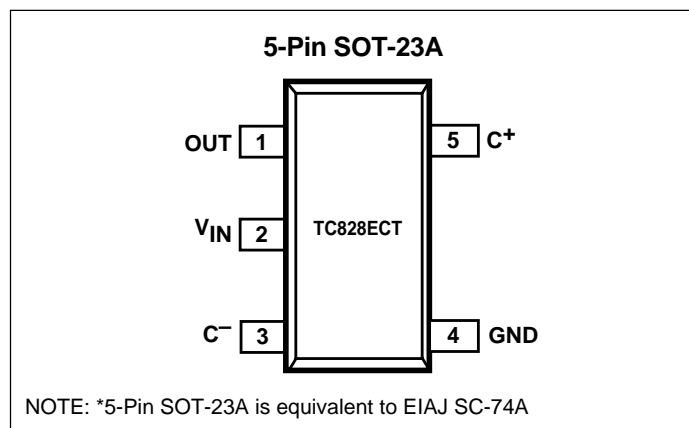
TYPICAL OPERATING CIRCUIT

Voltage Inverter

GENERAL DESCRIPTION

The TC828A is a CMOS "charge-pump" voltage converter in an ultra-small 5-Pin SOT-23A package. It can invert and/or double an input voltage that can range from +1.8V to +5.5V. Conversion efficiency is typically >95%, and switching frequency is 12KHz.

The external component requirement is only two capacitors (10µF nominal) for standard voltage inverter applications. With a few additional components a positive doubler can also be built. All other circuitry, including control, oscillator, power MOSFETs are integrated on-chip. Supply current is 38µA typically.

The TC828A is available in a 5-Pin SOT-23A surface mount package.

ORDERING INFORMATION

Part No.	Package	Temp. Range
TC828AECT	5-Pin SOT-23A	− 40°C to +85°C

NOTE: 5-Pin SOT-23A is equivalent to EIAJ-SC74A.

TC828A

ABSOLUTE MAXIMUM RATINGS*

Input Voltage (V_{IN} to GND) .... +6.0V, – 0.3V
Output Voltage (OUT to GND) .... –6.0V, + 0.3V
Current at OUT Pin 50mA
Short-Circuit Duration – OUT to GND .... Indefinite
Operating Temperature Range .... – 40°C to +85°C

Power Dissipation ($T_A \leq 70°C$)
    5-Pin SOT-23A 240mW
Storage Temperature (Unbiased) .... – 65°C to +150°C
Lead Temperature (Soldering, 10 sec) .... +300°C

*This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS: T_A = –40°C to +85°C, V_{IN} = +5V, C1 = C2 = 10 μF, unless otherwise noted. Typical values are at T_A = +25°C.

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
I_{DD}	Supply Current		—	38	80	μA
V^+	Minimum Supply Voltage	R_{LOAD} = 1 KΩ	1.8	—	—	V
V^+	Maximum Supply Voltage	R_{LOAD} = 1 KΩ	—	—	5.5	V
F_{OSC}	Oscillator Frequency		6	12	20	KHz
P_{EFF}	Power Efficiency	R_{LOAD} = 1 KΩ, T_A = +25°C	—	96	—	%
V_{EFF}	Voltage Conversion Efficiency	R_{LOAD} = ∞	95	99.9	—	%
R_{OUT}	Output Resistance	I_{OUT} = 5 mA, T_A = +25°C	—	25	50	Ω
		T_A = –40°C to +85°C	—	—	65	

NOTE: 1. Capacitor contribution is approximately 20% of the output impedance [ESR = 1 / pump frequency x capacitance)].
2. All – 40°C to +85°C specifications above are guaranteed by design.

PIN DESCRIPTION

Pin No. (5-Pin SOT-23A)	Symbol	Description
1	OUT	Inverting charge pump output.
2	V_{IN}	Positive power supply input.
3	C_1^-	Commutation capacitor negative terminal.
4	GND	Ground.
5	C_1^+	Commutation capacitor positive terminal.

2

Switched Capacitor Voltage Converters

DETAILED DESCRIPTION

The TC828A charge pump converter inverts the voltage applied to the V_{IN} pin. Conversion consists of a two-phase operation (Figure 1). During the first phase, switches S2 and S4 are open and S1 and S3 are closed. During this time, C1 charges to the voltage on V_{IN} and load current is supplied from C2. During the second phase, S2 and S4 are closed, and S1 and S3 are open. This action connects C1 across C2, restoring charge to C2.
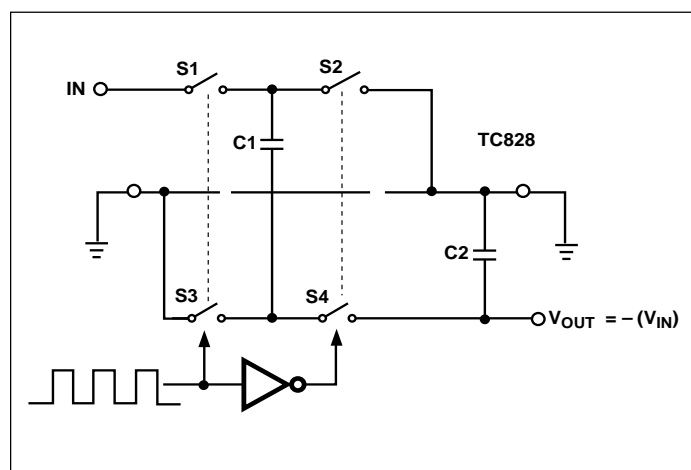

Figure 1. Ideal Switched Capacitor Charge Pump

APPLICATIONS INFORMATION

Output Voltage Considerations

The TC828A performs voltage conversion but does not provide *regulation*. The output voltage will droop in a linear manner with respect to load current. The value of this equivalent output resistance is approximately 25Ω nominal at +25°C and V_{IN} = +5V. V_{OUT} is approximately – 5V at light loads, and droops according to the equation below:

$$V_{DROOP} = I_{OUT} \times R_{OUT}$$
$$V_{OUT} = -(V_{IN} - V_{DROOP})$$

Charge Pump Efficiency

The overall power efficiency of the charge pump is affected by four factors:

(1) Losses from power consumed by the internal oscillator, switch drive, etc. (which vary with input voltage, temperature and oscillator frequency).
(2) I^2R losses due to the on-resistance of the MOSFET switches on-board the charge pump.
(3) Charge pump capacitor losses due to effective series resistance (ESR).

(4) Losses that occur during charge transfer (from the commutation capacitor to the output capacitor) when a voltage difference between the two capacitors exists.

Most of the conversion losses are due to factors (2), (3) and (4) above. These losses are given by Equation 1.

$$P_{LOSS\,(2,3,4)} = I_{OUT}^2 \times R_{OUT}$$
$$\cong I_{OUT}^2 \times \left[\frac{1}{(f_{OSC})\,C1} + 8R_{SWITCH} + 4ESR_{C1} + ESR_{C2}\right]$$

Equation 1.

The $1/(f_{OSC})(C1)$ term in Equation 1 is the effective output resistance of an ideal switched capacitor circuit (Figures 2a, 2b).

The losses in the circuit due to factor (4) above are also shown in Equation 2. The output voltage ripple is given by Equation 3.

$$P_{LOSS\,(4)} = \left[(0.5)(C1)(V_{IN}^2 - V_{OUT}^2) + (0.5)(C2)(V_{RIPPLE}^2 - 2V_{OUT}V_{RIPPLE})\right] \times f_{OSC}$$

Equation 2.

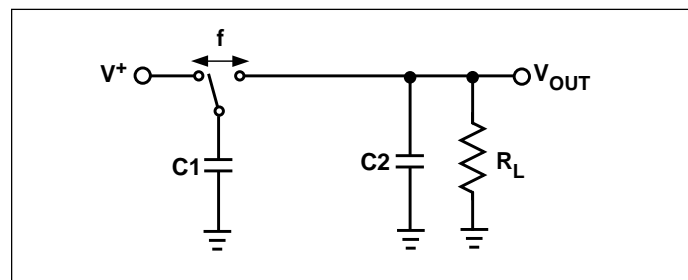$$V_{RIPPLE} = \frac{I_{OUT}}{(f_{OSC})(C2)} + 2(I_{OUT})(ESR_{C2})$$

Equation 3.

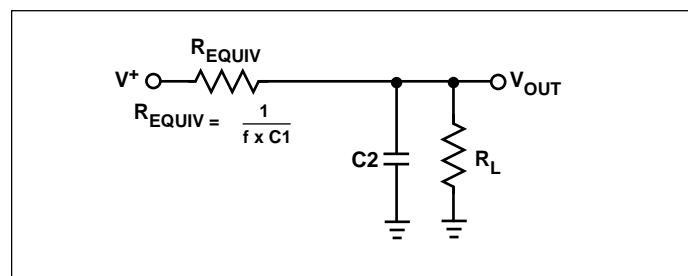**Figure 2a. Ideal Switched Capacitor Model**

Figure 2b. Equivalent Output Resistance

TC828A

Capacitor Selection

In order to maintain the lowest output resistance and output ripple voltage, it is recommended that low ESR capacitors be used. Additionally, larger values of C1 will lower the output resistance and larger values of C2 will reduce output ripple. (See Equation 1(b)).

Table 1 shows various values of C1 and the corresponding output resistance values @ +25°C. It assumes a 0.1Ω ESR_{C1} and 2Ω R_{SW}. Table 2 shows the output voltage ripple for various values of C2. The V_{RIPPLE} values assume 10mA output load current and 0.1Ω ESR_{C2}.

Table 1. Output Resistance vs. C1 (ESR = 0.1Ω)

C1(µF)	R_{OUT} (Ω)
0.1	850
1	100
3.3	42
10	25
47	18.3
100	17.3

Table 2. Output Voltage Ripple vs. C2 (ESR = 0.1Ω) I_{OUT} 10mA

C2(µF)	V_{RIPPLE} (mV)
1	835
3.3	254
10	85
47	19.7
100	10.3

Input Supply Bypassing

The V_{IN} input should be capacitively bypassed to reduce AC impedance and minimize noise effects due to the switching internal to the device. The recommended capacitor depends on the configuration of the TC828A.

If the device is loaded from OUT to GND it is recommended that a large value capacitor (at least equal to C1) be connected from the input to GND. If the device is loaded from IN to OUT a small (0.1 µF) capacitor from IN to OUT is sufficient.

Voltage Inverter

The most common application for charge pump devices is the inverter (Figure 3). This application uses two external capacitors – C1 and C2 (plus a power supply bypass capacitor, if necessary). The output is equal to $\overline{V_{IN}}$ plus any voltage drops due to loading. Refer to Table 1 and Table 2 for capacitor selection.
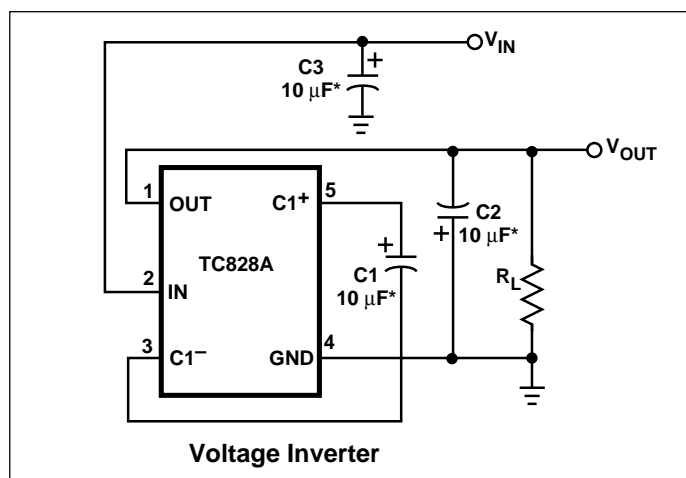

Figure 3. Test Circuit

Cascading Devices

Two or more TC828As can be cascaded to increase output voltage (Figure 4). If the output is lightly loaded, it will be close to (– 2 x V_{IN}) but will droop at least by R_{OUT} of the first device multiplied by the I_Q of the second. It can be seen that the output resistance rises rapidly for multiple cascaded devices. For large negative voltage requirements see the TC682 or TCM680 data sheets.
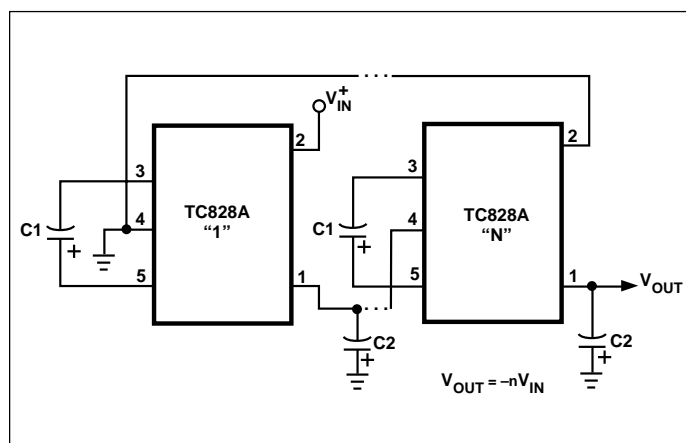

Figure 4. Cascading TC828As to Increase Output Voltage

Paralleling Devices

To reduce the value of R_{OUT}, multiple TC828As can be connected in parallel (Figure 5). The output resistance will be reduced by a factor of N where N is the number of TC828As. Each device will require it's own pump capacitor (C1), but all devices may share one reservoir capacitor (C2). However, to preserve ripple performance the value of C2 should be scaled according to the number of paralleled TC828As.
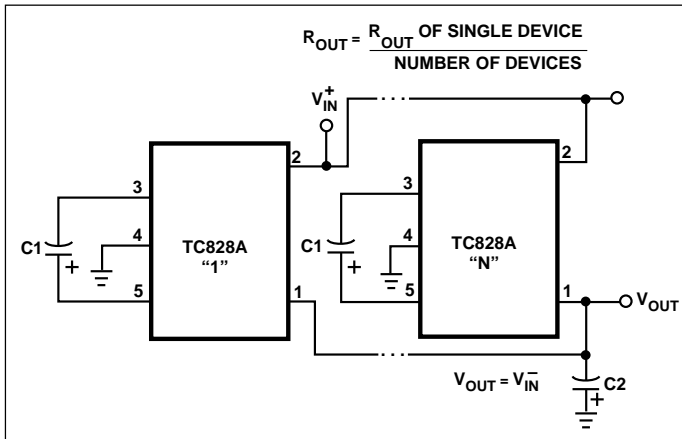
Switched Capacitor Voltage Converters

TC828A

$$R_{OUT} = \frac{R_{OUT} \text{ OF SINGLE DEVICE}}{\text{NUMBER OF DEVICES}}$$

Figure 5. Paralleling TC828As to Reduce Output Resistance

Voltage Doubler/Inverter

Another common application of the TC828A is shown in Figure 6. This circuit performs two functions in combination. C1 and C2 form the standard inverter circuit described above. C3 and C4 plus the two diodes form the voltage doubler circuit. C1 and C3 are the pump capacitors and C2 and C4 are the reservoir capacitors. Because both sub-circuits rely on the same switches if either output is loaded, both will droop toward GND. Make sure that the total current drawn from both the outputs does not total more than 40 mA.
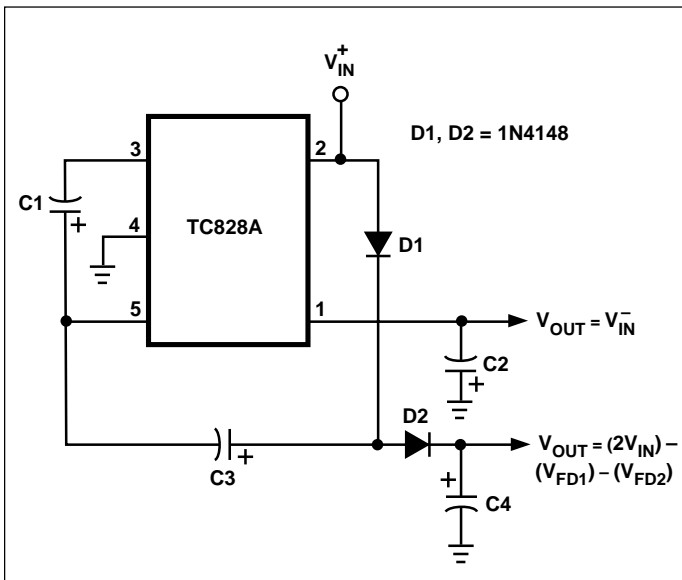

Figure 6. Combined Doubler and Inverter

Diode Protection for Heavy Loads

When heavy loads require the OUT pin to sink large currents being delivered by a positive source, diode protection may be needed. The OUT pin should not be allowed to be pulled above ground. This is accomplished by connecting a Schottky diode (1N5817) as shown in Figure 7.
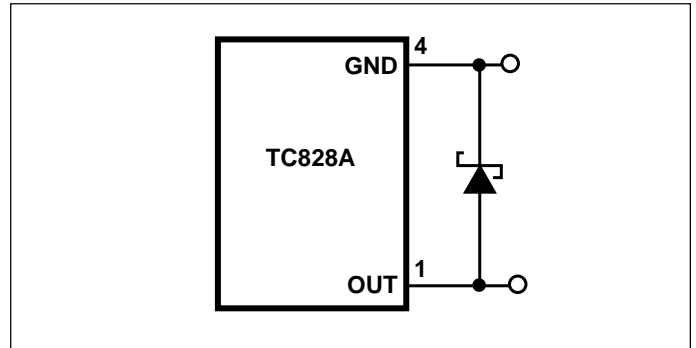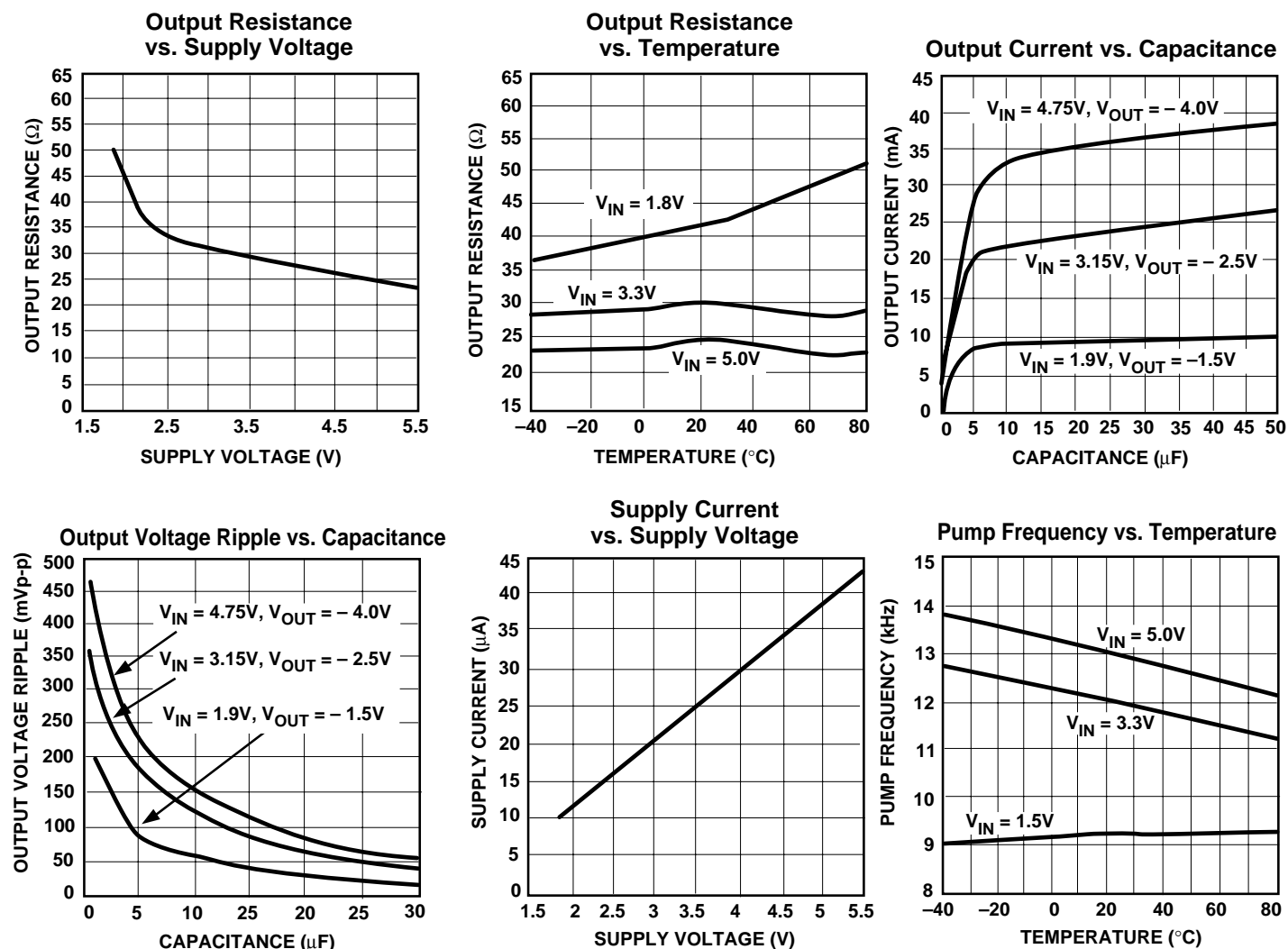

Figure 7. High V⁻ Load Current

Layout Considerations

As with any switching power supply circuit good layout practice is recommended. Mount components as close together as possible to minimize stray inductance and capacitance. Also use a large ground plane to minimize noise leakage into other circuitry.
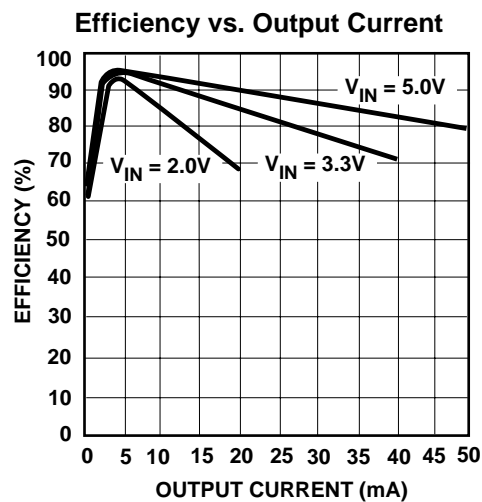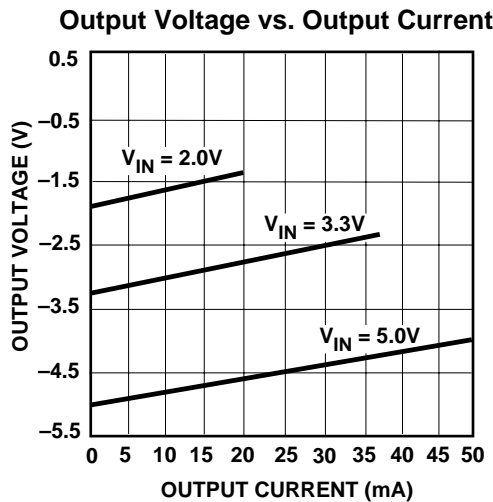
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TC828A

TYPICAL CHARACTERISTICS

Circuit of Figure 3, V_{IN} = +5V, C1 = C2 = C3, T_A = +25°C, unless otherwise noted.

**Output Resistance
vs. Supply Voltage**

**Output Resistance
vs. Temperature**

Output Current vs. Capacitance

Output Voltage Ripple vs. Capacitance

**Supply Current
vs. Supply Voltage**
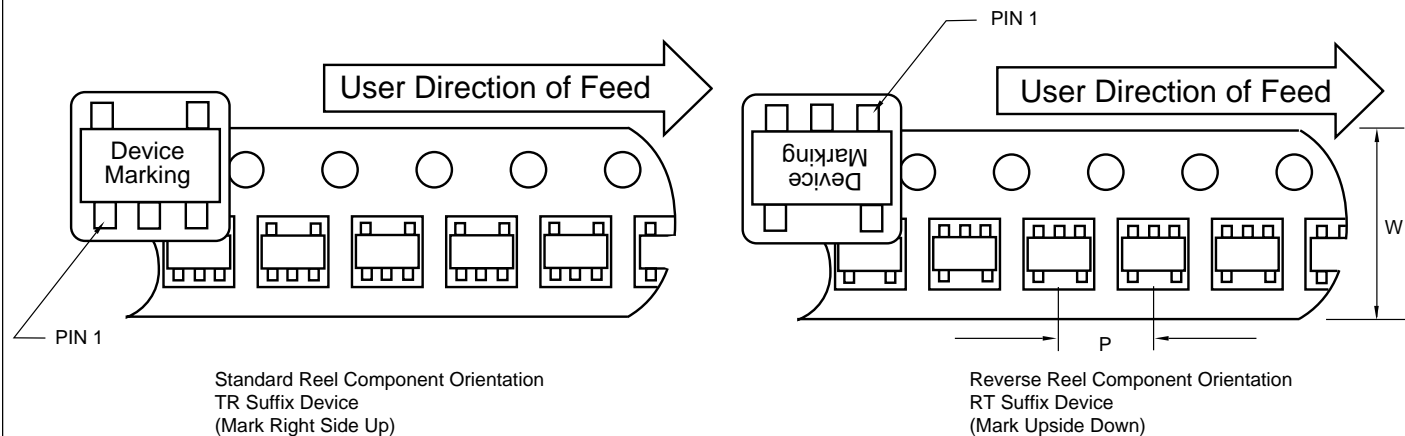
Pump Frequency vs. Temperature

Switched Capacitor Voltage Converters

TYPICAL CHARACTERISTICS (Cont.)

Circuit of Figure 3, V_{IN} = +5V, C1 = C2 = C3, T_A = +25°C, unless otherwise noted.

Output Voltage vs. Output Current

Efficiency vs. Output Current

TAPING FORM

Component Taping Orientation for 5-Pin SOT-23A (EIAJ SC-74A) Devices

User Direction of Feed

Device Marking

PIN 1

Standard Reel Component Orientation
TR Suffix Device
(Mark Right Side Up)

Reverse Reel Component Orientation
RT Suffix Device
(Mark Upside Down)

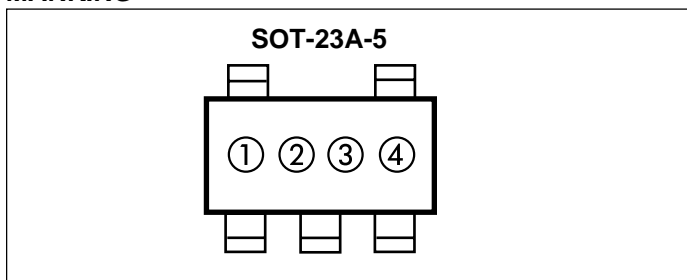**Carrier Tape, Number of Components Per Reel and Reel Size**

Package	Carrier Width (W)	Pitch (P)	Part Per Full Reel	Reel Size
5-Pin SOT-23A	8 mm	4 mm	3000	7 in

TC828A

MARKING

SOT-23A-5

① ② ③ ④

③ represents year and 2-month code

④ represents lot ID number

Part Numbers and Part Marking

① & ② = part number code + temperature range
(two-digit code).

TC828A	**Code**
TC828AECT	CC

ex: 828ECT = ©©○○

PACKAGE DIMENSIONS

5-Pin SOT-23A (EIAJ SC-74A)

.075 (1.90)
REF.

.122 (3.10)
.098 (2.50)

.071 (1.80)
.059 (1.50)

.020 (0.50)
.012 (0.30)

PIN 1

.037 (0.95)
REF.

.122 (3.10)
.106 (2.70)

.057 (1.45)
.035 (0.90)

.006 (0.15)
.000 (0.00)

10° MAX.

.010 (0.25)
.004 (0.09)

.024 (0.60)
.004 (0.10)
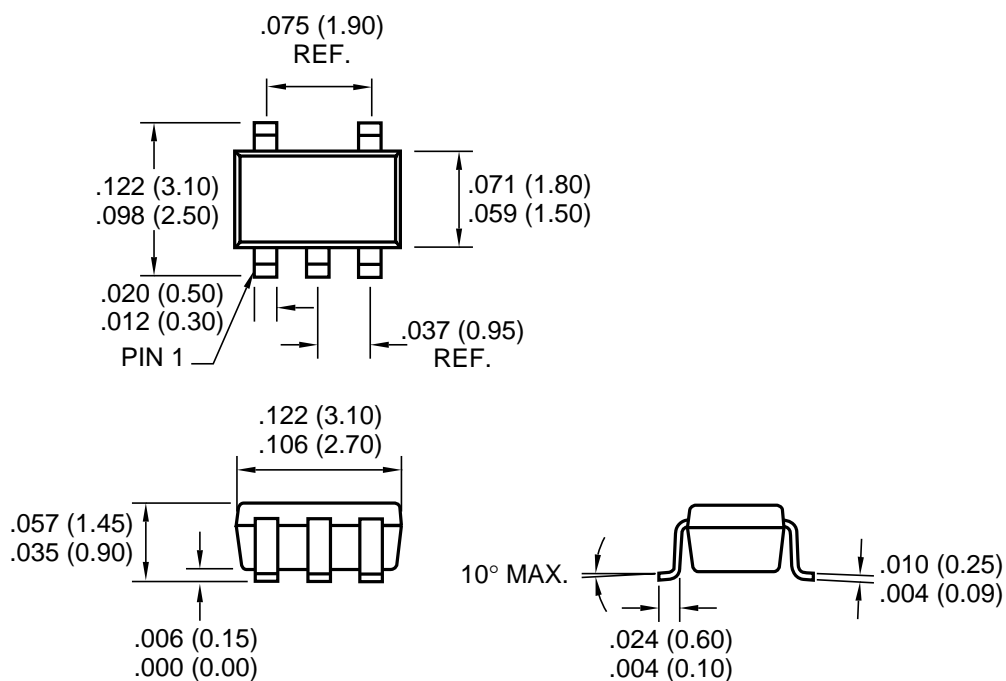
Dimensions: inches (mm)

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Dallas
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Addison, TX 75001
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Dayton
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Miamisburg, OH 45342
Tel: 937-291-1654 Fax: 937-291-9175

Detroit
Tri-Atria Office Building
32255 Northwestern Highway, Suite 190
Farmington Hills, MI 48334
Tel: 248-538-2250 Fax: 248-538-2260

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New York
150 Motor Parkway, Suite 202
Hauppauge, NY 11788
Tel: 631-273-5305 Fax: 631-273-5335

San Jose
Microchip Technology Inc.
2107 North First Street, Suite 590
San Jose, CA 95131
Tel: 408-436-7950 Fax: 408-436-7955

Toronto
6285 Northam Drive, Suite 108
Mississauga, Ontario L4V 1X5, Canada
Tel: 905-673-0699 Fax: 905-673-6509

ASIA/PACIFIC

China - Beijing
Microchip Technology Beijing Office
Unit 915
New China Hong Kong Manhattan Bldg.
No. 6 Chaoyangmen Beidajie
Beijing, 100027, No. China
Tel: 86-10-85282100 Fax: 86-10-85282104

China - Shanghai
Microchip Technology Shanghai Office
Room 701, Bldg. B
Far East International Plaza
No. 317 Xian Xia Road
Shanghai, 200051
Tel: 86-21-6275-5700 Fax: 86-21-6275-5060

Hong Kong
Microchip Technology Hongkong Ltd.
Unit 901,Tower 2, Metroplaza
Kwai Fong, N.T., Hong Kong
Tel: 852-2401-1200 Fax: 852-2401-3431

India
Microchip Technology Inc.
India Liaison Office
Divyasree Chambers
1 Floor, Wing A (A3/A4)
No. 11, OíShaugnessey Road
Bangalore, 560 025, India
Tel: 91-80-2290061 Fax: 91-80-2290062

Japan
Microchip Technology Intl. Inc.
Benex S-1 6F
3-18-20, Shinyokohama
Kohoku-Ku, Yokohama-shi
Kanagawa, 222-0033, Japan
Tel: 81-45-471- 6166 Fax: 81-45-471-6122

Korea
Microchip Technology Korea
168-1, Youngbo Bldg. 3 Floor
Samsung-Dong, Kangnam-Ku
Seoul, Korea
Tel: 82-2-554-7200 Fax: 82-2-558-5934

ASIA/PACIFIC (continued)

Singapore
Microchip Technology Singapore Pte Ltd.
200 Middle Road
#07-02 Prime Centre
Singapore, 188980
Tel: 65-334-8870 Fax: 65-334-8850

Taiwan
Microchip Technology Taiwan
11F-3, No. 207
Tung Hua North Road
Taipei, 105, Taiwan
Tel: 886-2-2717-7175 Fax: 886-2-2545-0139

EUROPE

Australia
Microchip Technology Australia Pty Ltd
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Australia
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Germany
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Gustav-Heinemann Ring 125
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Tel: 49-89-627-144 0 Fax: 49-89-627-144-44

Germany
Analog Product Sales
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Tel: 49-89-895650-0 Fax: 49-89-895650-22

Italy
Arizona Microchip Technology SRL
Centro Direzionale Colleoni
Palazzo Taurus 1 V. Le Colleoni 1
20041 Agrate Brianza
Milan, Italy
Tel: 39-039-65791-1 Fax: 39-039-6899883

United Kingdom
Arizona Microchip Technology Ltd.
505 Eskdale Road
Winnersh Triangle
Wokingham
Berkshire, England RG41 5TU
Tel: 44 118 921 5869 Fax: 44-118 921-5820

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