

T1/CEPT/ISDN-PRI TRANSFORMER

P/N: T8ST5382 DATA SHEET

A. GENERAL SPECIFICATIONS:

1. EXTENDED AND STANDARD TEMPERATURE RANGE MODELS.
2. DUAL SURFACE MOUNT PACKAGE CONTAINS BOTH TRANSMIT AND RECEIVE.
3. MODELS MATCHED TO LEADING TRANSCEIVER ICs
4. UL 1459 RECOGNIZED.
5. 1500Vrms ISOLATION VOLTAGE.

B. ELECTRICAL SPECIFICATIONS: (25° C)

1. OCL INDUCTANCE: 1.20uH MIN PIN 1-4 (2,3 SHORT) 100KHz/0.2V
2. LEAKAGE INDUCTANCE: 2.0uH Max PIN 1-4 (2,3 short), 8-5 (7,6 short) @ 100KHz/0.2V
3. Cw/w: 6pF Max PIN 1-8 (2,3&6,7 short) @ 100KHz/0.2V
4. TURN RATIO (PRI: SEC $\pm 5\%$): 1:1:1:1
5. DC RESISTANCE: 0.25 Ω MAX PIN 1-4 (short)
6. HI-POT: 2000Vrms/0.5mA FOR 1.0 MINUTE

C. DIMENSIONS & SCHEMATIC & MARKING (UNIT: mm)

