TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

TA4009F

1.9GHz BAND PRE AMPLIFER APPLICATION

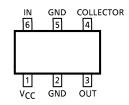
FEATURES

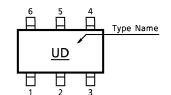
Low current : $I_{CC} = 12mA$ (Typ.) High gain : $G_P = 12dB (Typ.)$

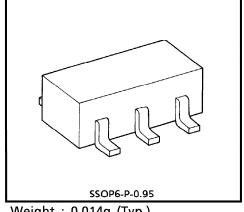
Recommended operating voltage : $V_{CC} = 2.7 \sim 3.3 \text{V}$

PIN ASSIGNMENT (TOP VIEW)









Weight: 0.014g (Typ.)

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	Vcc	5	V
Total Power Dissipation	P _D (*)	300	mW
Operating Temperature	T _{opr}	- 40∼8 5	°C
Storage Temperature Range	T _{stg}	- 55~125	°C

^(*) When mounted on the glass epoxy board of 2.5cm² x 1.6t.

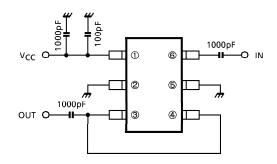
ELECTRICAL CHARACTERISTICS ($V_{CC} = 3V$, Ta = 25°C, $Zg = ZI = 50\Omega$)

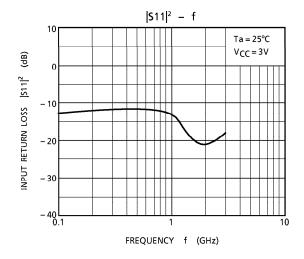
CHARACTERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Circuit Current	Icc	_	Non Carrier	_	12	16	mA
Frequency Range	f _{range}	_	_	1895	_	1918	MHz
Power Gain	G _P	1	f = 1895~1918MHz	10	12	_	dB
Noise Figure	NF	1		_	6	_	dB
Isolation	ISL	1		15	20	_	dB
Input VSWR	VSWR _{in}	1		_	1.2	2.0	_
Output VSWR	VSWR _{out}	1		_	2.5	_	_
Output Power At 1dB Gain Comperssion	PO1dB	1		_	-8	_	dBmW

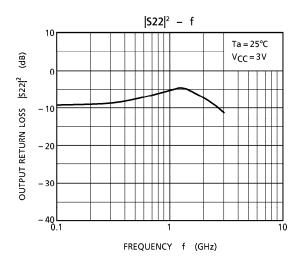
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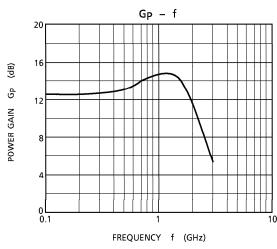
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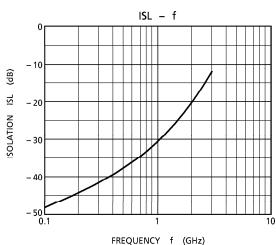
TEST CIRCUIT 1.









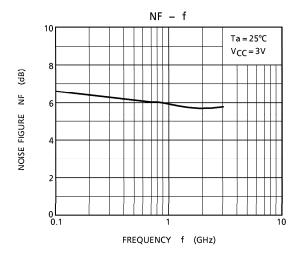


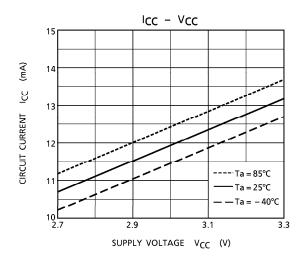
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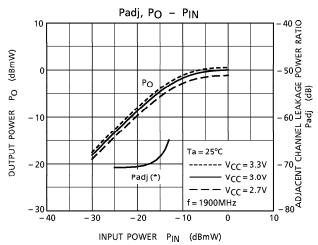
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(*) Input signal is modulated to $\pi/4$ QPSK (α = 0.5). Bit rate is 384kbps. Δf = 600kHz.