

CMOS 4-BIT MICROCONTROLLER

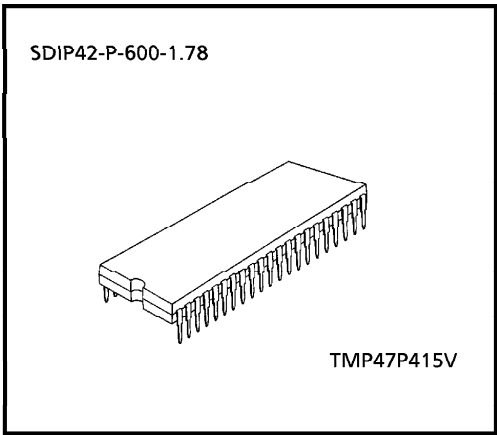
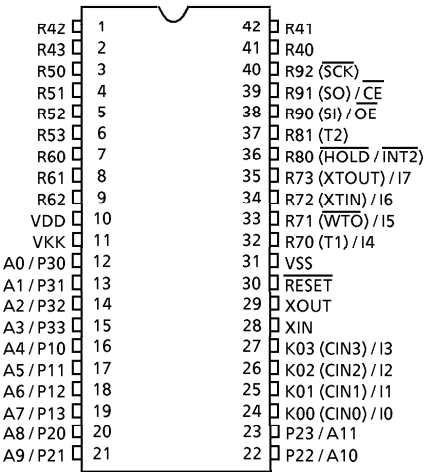
TMP47P415VN

The 47P415V is the system evaluation LSI of 47C215/415 with 32K bits one-time PROM. The 47P415 programs / verifies using an adapter socket to connect with PROM programmer, as it is in TMM27256AD. In addition, the 47P415 and the 47C215 are pin compatible. The 47P415 operates as the same as 47C215/415 by programming to the internal PROM.

PART No.	EPROM	RAM	PACKAGE	ADAPTER SOCKET
TMP47P415VN	OTP 4096 × 8-bit	256 × 4-bit	SDIP42-P-600-1.78	BM1191

PIN ASSIGNMENT (TOP VIEW)

SDIP42P-600-1.78



PIN FUNCTION

The 47P415V has MCU mode and PROM mode.

(1) MCU mode

The 47C215/415 is pin compatible.

(2) PROM mode

PIN NAME	INPUT / OUTPUT	FUNCTIONS	PIN NAME(MCU mode)
A11 to A8	INPUT	Address inputs	P23 to P20
A7 to A4			P13 to P10
A3 to A0			P33 to P30
I7 to I4	I/O	Data inputs / outputs	R73 to R70
I3 to I0			K03 to K00
$\overline{CE}$	Input	Chip Enable input	R91
$\overline{OE}$		Output Enable input	R90
VPP	Power supply	+ 12.5V / 5V (Program supply voltage)	$\overline{RESET}$
VCC		+ 5 V	VDD
VSS		0 V	VSS
R43 to R40	I/O	Be fixed to low level for input processing.	
R53 to R50			
R62 to R60		PROM mode setting pin. Be fixed to low level.	
R81 to R80 (HOLD)			
R92			
XIN	Input	Input the clock from the external oscillator.	
XOUT	Output	Be fixed to Low level.	
VKK	Power supply	Be fixed to VCC level.	

OPERATIONAL DESCRIPTION

The following is an explanation of hardware configuration and operation in relation to the 47P415V. The 47P415V is the same as the 47C215/415 except that an OTP is used instead of a built-in mask ROM.

1. OPERATION mode

The 47P415V has a MCU mode and a PROM mode.

1.1 MCU mode

The MCU mode is set by attaching a resonator between the XIN and Xout pins. Operation in the MCU mode is the same as for the 47C215/415.

1.1.1 Program Memory

The program storage area is the same as for the 47C415. Data conversion tables must be set in two locations when using the 47P415V to check 47C215 operation.

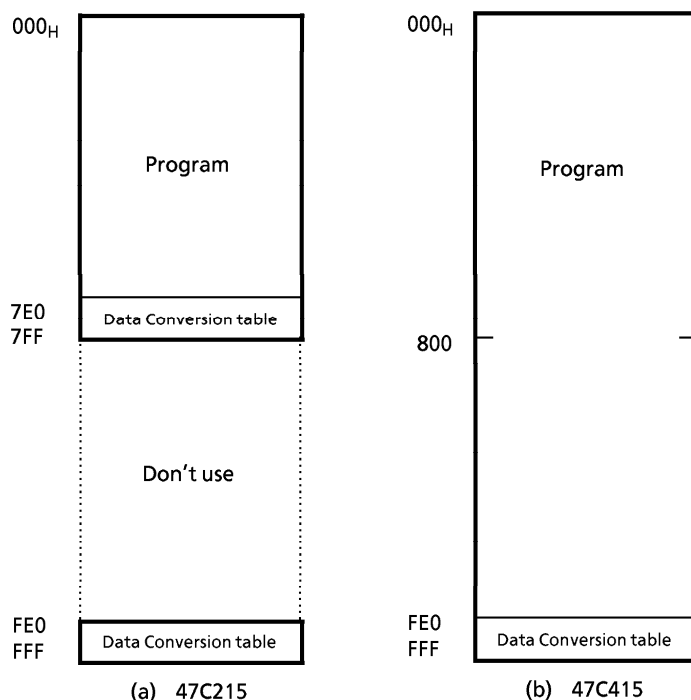


Figure 1-1. Program area (ROM)

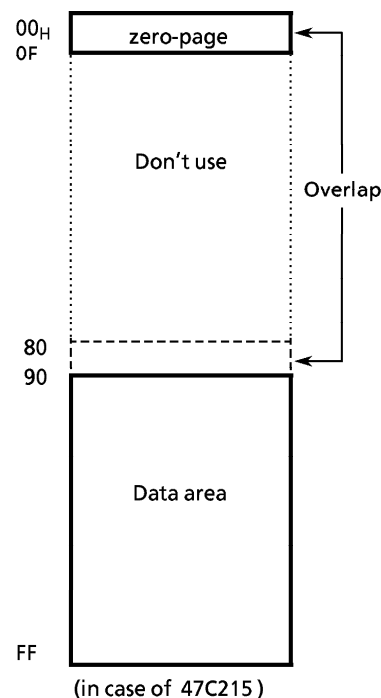


Figure 1-2. RAM addressing

1.1.2 Data Memory

The 47P415V contains 256×4-bit (equivalent to 47C415) data memory. When the 47P415V is used as evaluator of the 47C215, programming should be performed assuming that the RAM is assigned to addresses 00 to 0F_H and 90 to FF_H as show in Figure 1-2 by considering the application software evaluation.

1.1.3 Input/Output Circuitry

(1) Pin Configuration

47P415V is the same as 47C215/415.

(2) I/O ports

The I/O circuitries of 47P415V I/O ports are the same as the code MA type I/O circuitries of the 47C215/415.

When using as an evaluator of other I/O codes (MB, MC), external resistors are required. Figure 1-3 shows the external circuitry.

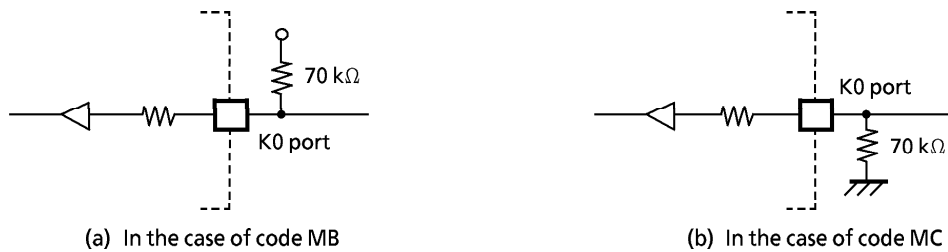


Figure 1-3. I/O Circuitry Code and External Circuitry

1.2 PROM mode

The PROM mode is set by inputting the external check to the XIN pin when XOUT pin is pulled down to the VSS level. The PROM mode can be used as a general-purpose PROM writer for program writing and verification (A high-speed program mode is used set the ROM type the same as for the TMM 27256AD.)

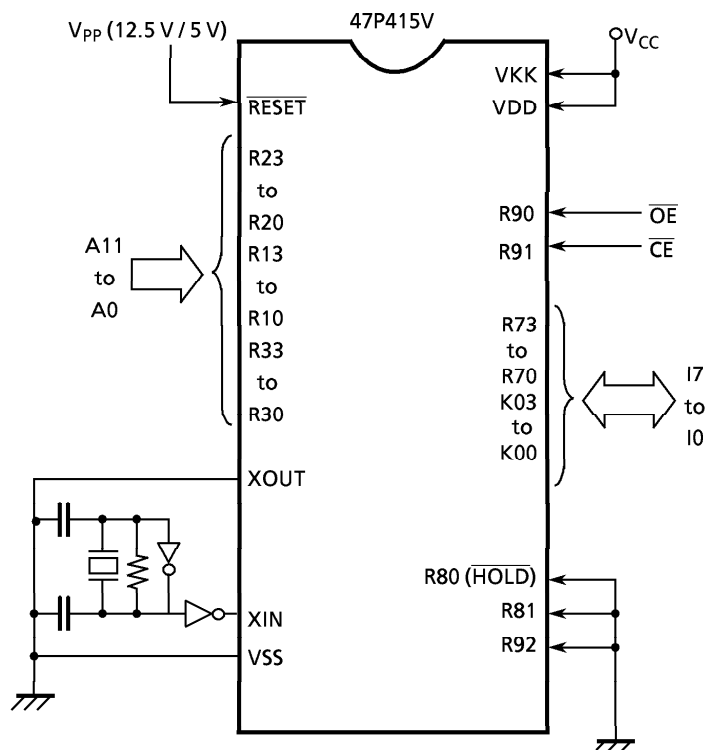


Figure 1-4. Setting for PROM mode

1.2.1 Program Writing

When writing a program, set a ROM type to "27256A" (PROGRAMMING VOLTAGE : 12.5 V). Since the 47P415V has a 4096 × 8-bit internal PROM (000 to FFF_H), set a stop address of a prom writer to "FFF_H". For a general-purpose PROM write, use the writer which does not have or can release an electric signature mode.

Note. When the data written to OTP is same as the data of PROM programmer, there is the possibility that the security writing can not be executed, which is depended on the types of PROM programmers.
In this case, set the data of PROM programmer to "00" and execute the security writing after writing the data to OTP.

1.2.2 High Speed Programming Mode

The program time can be greatly decreased by using this high speed programming mode. The device is set up in the high speed programming mode when the programming voltage (+ 12.5 V) is applied to the V_{PP} terminal with $V_{CC} = 6\text{ V}$ and $\overline{CE} = V_{IH}$.

The programming is achieved by applying a single low level 1ms pulse the $\overline{CE}$ input after addresses and data are stable. Then the programmed data is verified by using Program Verify Mode.

If the programmed data is not correct, another program pulse of 1ms is applied and then programmed data is verified. This should be repeated until the program operates correctly (max. 25 times).

After correctly programming the selected address, one additional program pulse with pulse width 3 times that needed for programming is applied.

When programming has been completed, the data in all addresses should be verified with $V_{CC} = V_{PP} = 5\text{ V}$.

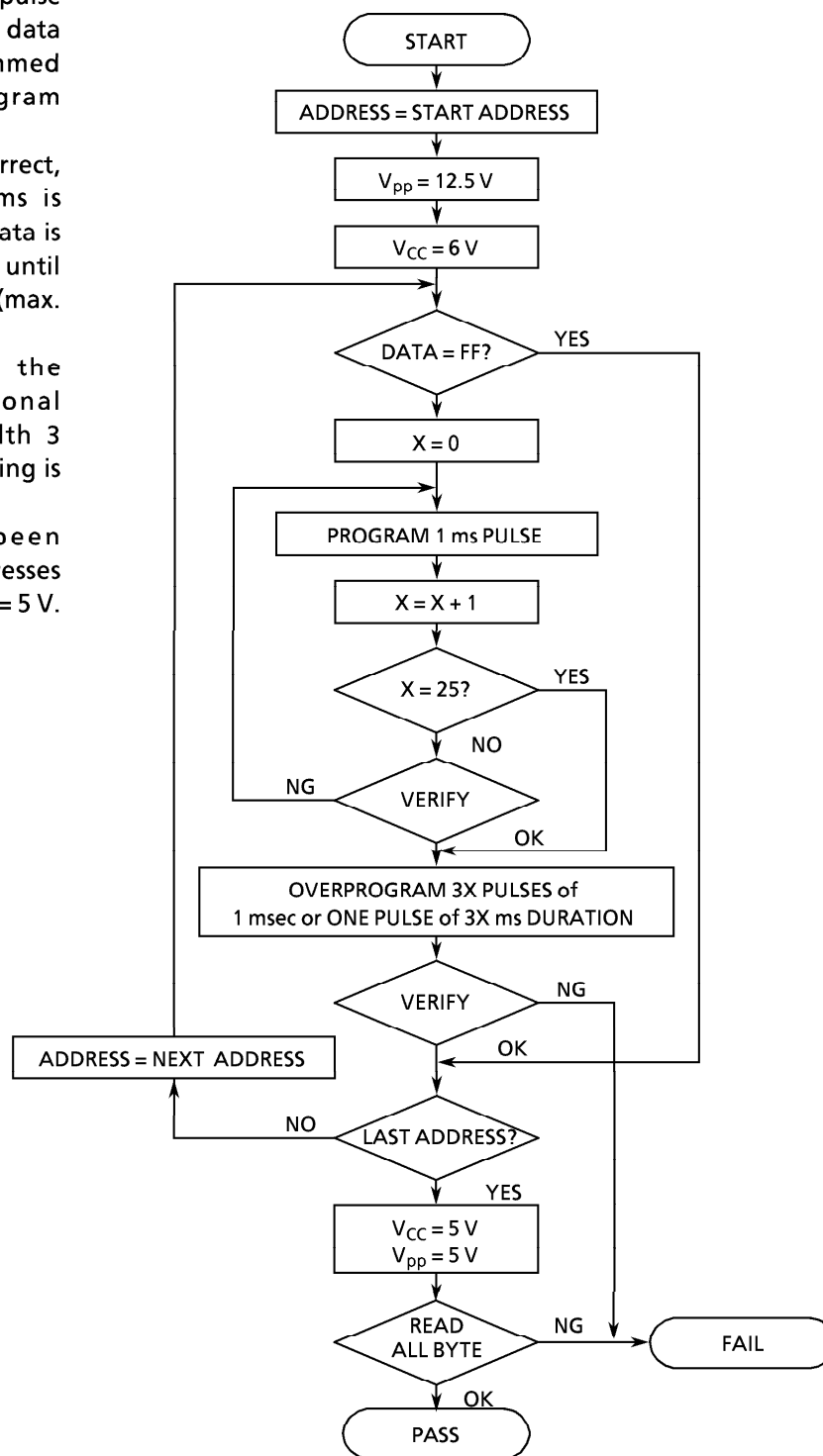


Figure 1-5. FLOW CHART

ELECTRICAL CHARACTERISTICS

ABSOLUTE MAXIMUM RATINGS

(V_{SS} = 0 V)

PARAMETER	SYMBOL	PINS	RATING	UNIT
Supply Voltage	V _{DD}		– 0.3 to 6.5	V
Program Voltage	V _{PP}	RESET / V _{PP} pin	– 0.3 to 13.0	V
Input Voltage	V _{IN}		– 0.3 to V _{DD} + 0.3	V
Output Voltage	V _{OUT1}	R7, R8, R9, XOUT	– 0.3 to V _{DD} + 0.3	V
	V _{OUT3}	P1, P2, P3, R4, R5, R6	V _{DD} – 38 to V _{DD} + 0.3	
Output Current (per 1 pin)	I _{OUT1}	R70	30	mA
	I _{OUT2}	R71 to R73, R80, R81, R9	3.2	
	I _{OUT3}	P1, P2, P3	– 12	
	I _{OUT4}	R4, R5, R6	– 25	
Output Currnent (Total)	ΣI _{OUT3}	P1, P2, P3	– 80	mA
	ΣI _{OUT4}	R4, R5, R6	– 100	
Power Dissipation [T _{opr} = 70 °C]	PD		600	mW
Soldering Temperature (time)	T _{sld}		260 (10 s)	°C
Storage Temperature	T _{stg}		– 55 to 125	°C
Operating Temperature	T _{opr}		– 30 to 70	°C

RECOMMENDED OPERATING CONDITIONS

(V_{SS} = 0 V, T_{opr} = – 30 to 70 °C)

PARAMETER	SYMBOL	PINS	CONDITIONS	Min.	Max.	UNIT
Supply Voltage	V _{DD}		In the Normal mode	4.5	5.5	V
			In the SLOW mode	2.7		
			In the HOLD mode	2.0		
Input - High Voltage	V _{IH1}	Except Hysteresis Input	V _{DD} ≥ 4.5 V	V _{DD} × 0.7	V _{DD}	V
	V _{IH2}	Hysteresis Input		V _{DD} × 0.75		
	V _{IH3}		V _{DD} < 4.5 V	V _{DD} × 0.9		
Input - Low Voltage	V _{IL1}	Except Hysteresis Input	V _{DD} ≥ 4.5 V	0	V _{DD} × 0.3	V
	V _{IL2}	Hysteresis Input			V _{DD} × 0.25	
	V _{IL3}		V _{DD} < 4.5 V		V _{DD} × 0.1	
Clock Frequency	f _c	XIN, XOUT		0.4	8.0	MHz
	f _s	XTIN, XTOUT		30.0	34.0	kHz

Note. Input voltage V_{IH3}, V_{IL3} : in the SLOW or HOLD mode

D.C. CHARACTERISTICS

(V_{SS} = 0 V, T_{opr} = – 30 to 70 °C)

PARAMETER	SYMBOL	PINS	CONDITIONS	Min.	Typ.	Max.	UNIT
Hysteresis Voltage	V _{HS}	Hysteresis Input		–	0.7	–	V
Input Current	I _{IN1}	K0, $\overline{\text{RESET}}$	V _{DD} = 5.5 V,	–	–	± 2	μA
	I _{IN2}	R ports (open drain)	V _{IN} = 5.5 V / 0 V				
Input Resistance	R _{IN1}	KO port with pull-up/pull-down		30	70	150	kΩ
	R _{IN2}	$\overline{\text{RESET}}$		100	220	450	
Pull-down resistance	R _K	source open drain	V _{DD} = 5.5 V, V _{KK} = –30 V	–	80	–	
Output Leakage Current	I _{LO1}	sink open drain	V _{DD} = 5.5 V, V _{IN} = 5.5 V	–	–	2	μA
	I _{LO2}	source open drain	V _{DD} = 5.5 V, V _{OUT} = –30 V	–	–	–2	
Output Level High Voltage	V _{OH}	P1, P2, P3	V _{DD} = 4.5 V, I _{OH} = –5 mA	2.4	–	–	V
Output Level Low Voltage	V _{OL}	R71 to R73, R80, R81, R9	V _{DD} = 4.5 V, I _{OL} = 1.6 mA	–	–	0.4	V
Output Level High Current	I _{OH}	R4, R5, R6	V _{DD} = 4.5 V, V _{OL} = 2.4 V	–	–15	–	mA
Output Level Low Current	I _{OL}	R70	V _{DD} = 4.5 V, V _{OL} = 1.0 V	–	15	–	mA
Supply Current (in the Normal mode)	I _{DD}		V _{DD} = 5.5 V, f _c = 4 MHz	–	3	6	mA
Supply Current (in the SLOW mode)	I _{DDS}		V _{DD} = 3.0 V, f _s = 32.768 kHz	–	30	60	μA
Supply Current (in the HOLD mode)	I _{DDH}		V _{DD} = 5.5 V	–	0.5	10	μA

Note 1. Typ. values show those when T_{opr} = 25 °C, V_{DD} = 5 V

Note 2. Input Current I_{IN1} ; The current through resistor is not included, when the input resistor (pull-up/pull-down) is contained.

Note 3. Supply Current I_{DD}, I_{DDH} ; V_{IN} = 5.3 V / 0.2 V
The K0 port is open when the input resistor is contained. The voltage applied to the R port is within the valid range.

I_{DDS} ; V_{IN} = 2.8 V / 0.2 V, low frequency clock is only oscillated (connecting XTIN, XTOUT).
at comparator input is disabled.

A/D conversion characteristics

(V_{SS} = 0 V, V_{DD} = 4.5 to 5.5 V, T_{opr} = – 30 to 70 °C)

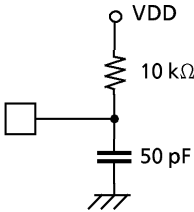
PARAMETER	SYMBOL	PINS	CONDITIONS	Min.	Typ.	Max.	UNIT
Analog Input Voltage	V _{AIN}	CIN3 to CIN0		V _{SS}	–	V _{DD}	V
A/D conversion error				–	–	± $\frac{1}{2}$	LSB

A.C. CHARACTERISTICS

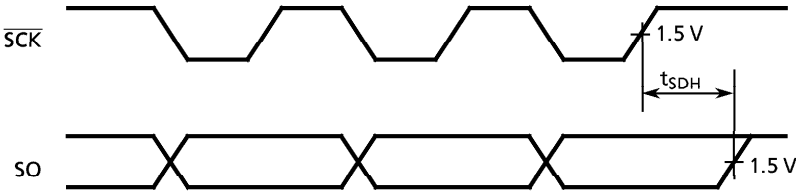
($V_{SS} = 0\text{ V}$, $V_{DD} = 4.5\text{ to }5.5\text{ V}$, $T_{opr} = -30\text{ to }70\text{ }^{\circ}\text{C}$)

PARAMETER	SYMBOL	CONDITIONS	Min.	Typ.	Max.	UNIT
Instruction Cycle Time	t_{cy}	In the Normal mode	1.0	—	20	μs
		In the SLOW mode	235	—	267	
High level clock pulse width	t_{WCH}	External clock mode	80	—	—	ns
Low level clock pulse width	t_{WCL}					
Shift Data Hold Time	t_{SDH}		$0.5 t_{cy} - 300$	—	—	ns

Note. External circuit for $\overline{\text{SCK}}$ Pin and SO pin



Serial port (completion of Transmission)



RECOMMENDED OSCILLATING CONDITIONS

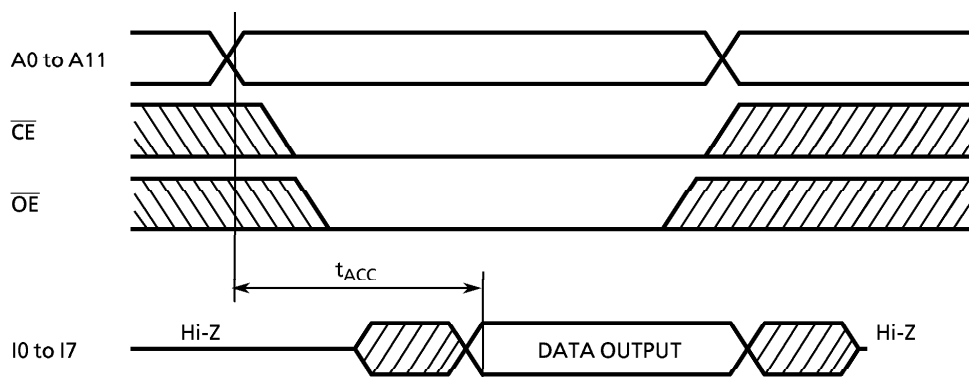
($V_{SS} = 0\text{ V}$, $V_{DD} = 4.5\text{ to }6.0\text{ V}$, $T_{opr} = -30\text{ to }70\text{ }^{\circ}\text{C}$)

Recommended oscillating conditions of the 47P415 are equal to the 47C415's.

DC/AC CHARACTERISTICS (V_{SS} = 0 V)

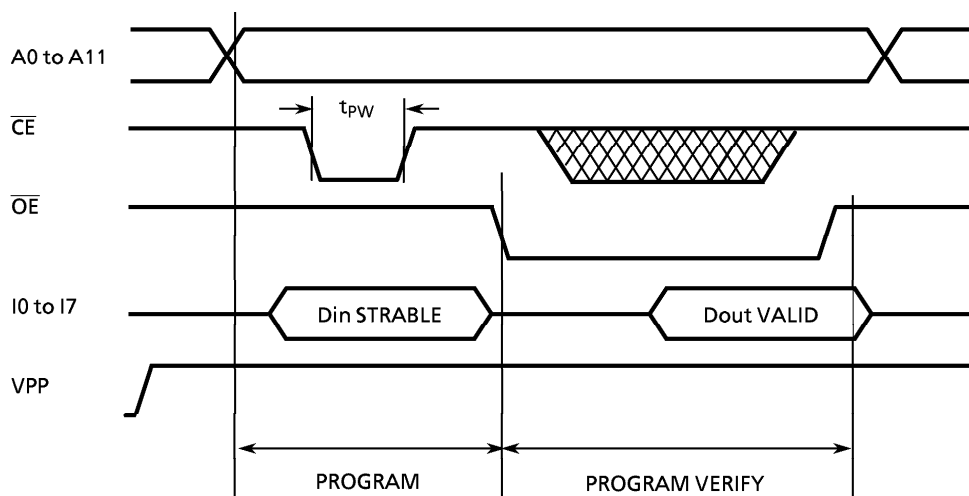
(1) Read Operation

PARAMETER	SYMBOL	CONDITION	Min.	Typ.	Max.	UNIT
Output Level High Voltage	V _{IH4}		V _{CC} × 0.7	–	V _{CC}	V
Output Level Low Voltage	V _{IL4}		0	–	V _{CC} × 0.3	V
Supply Voltage	V _{CC}		4.75	–	6.0	V
Programming Voltage	V _{PP}					
Address Access Time	t _{ACC}	V _{CC} = 5.0 ± 0.25 V	0	–	350	ns



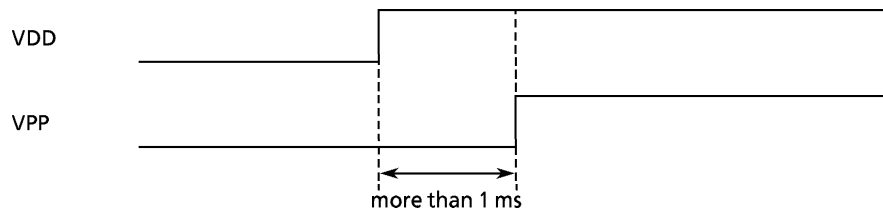
(2) High Speed Programming Operation

PARAMETER	SYMBOL	CONDITION	Min.	Typ.	Max.	UNIT
Input High Voltage	V _{IH4}		V _{CC} × 0.7	–	V _{CC}	V
Input Low Voltage	V _{IL4}		0	–	V _{CC} × 0.3	V
Supply Voltage	V _{CC}		4.75	–	6.0	V
V _{PP} Power Supply Voltage	V _{PP}		12.0	12.50	13.0	V
Programming Pulse Width	t _{PW}	V _{CC} = 6.0 ± 0.25 V	0.95	1.0	1.05	ms



Note. There are some PROM programmer types which cannot program OTP.

In TMP47P415V, VPP pin is also used as RESET pin. To set a mode, REST/VPP pin must be set to "low" during 1 ms and more after the rising of power-on and the rising of VDD electrical power.



Recommend EPROM programmer

TYPE	
R4945	(ADVANTEST)
UNISITE	(DATA I/O)
AF - 9706	(ANDO)
PECKER - 11	(AVAL DATA)