

TOSHIBA FIELD EFFECT TRANSISTOR SILICON P CHANNEL MOS TYPE (U-MOS II)

TPCS8101

LITHIUM ION BATTERY APPLICATIONS

NOTE BOOK PC, PORTABLE EQUIPMENTS APPLICATIONS

HIGH SPEED AND HIGH EFFICIENCY DC-DC CONVERTERS

- Low Drain-Source ON Resistance : $R_{DS(ON)} = 15 \text{ m}\Omega$ (Typ.)
- High Forward Transfer Admittance : $|Y_{fs}| = 12 \text{ S}$ (Typ.)
- Low Leakage Current : $I_{DSS} = -10 \mu\text{A}$ (Max) ($V_{DS} = -30 \text{ V}$)
- Enhancement-Mode : $V_{th} = -0.8 \sim -2.0 \text{ V}$
($V_{DS} = -10 \text{ V}$, $I_D = -1 \text{ mA}$)

MAXIMUM RATINGS ($T_a = 25^\circ\text{C}$)

CHARACTERISTIC		SYMBOL	RATING	UNIT
Drain-Source Voltage		V_{DS}	-30	V
Drain-Gate Voltage ($R_{GS} = 20 \text{ k}\Omega$)		V_{DGR}	-30	V
Gate-Source Voltage		V_{GSS}	± 20	V
Drain Current	DC	I_D	-6	A
	Pulse	I_{DP}	-24	A
Drain Power Dissipation ($T_a = 25^\circ\text{C}$)		P_D^{***}	1.5	W
Single Pulse Avalanche Energy*		E_{AS}	46.8	mJ
Avalanche Current		I_{AR}	-6	A
Repetitive Avalanche Energy**		E_{AR}	0.15	mJ
Channel Temperature		T_{ch}	150	$^\circ\text{C}$
Storage Temperature Range		T_{stg}	$-55 \sim 150$	$^\circ\text{C}$

THERMAL CHARACTERISTICS

CHARACTERISTIC	SYMBOL	MAX.	UNIT
Thermal Resistance, Channel to Ambient	$R_{th(ch-a)}^{***}$	83.3	$^\circ\text{C/W}$

Note ;

* $V_{DD} = -24 \text{ V}$, $T_{ch} = 25^\circ\text{C}$ (initial), $L = 1.0 \text{ mH}$,
 $I_{AR} = -6 \text{ A}$, $R_G = 25 \Omega$

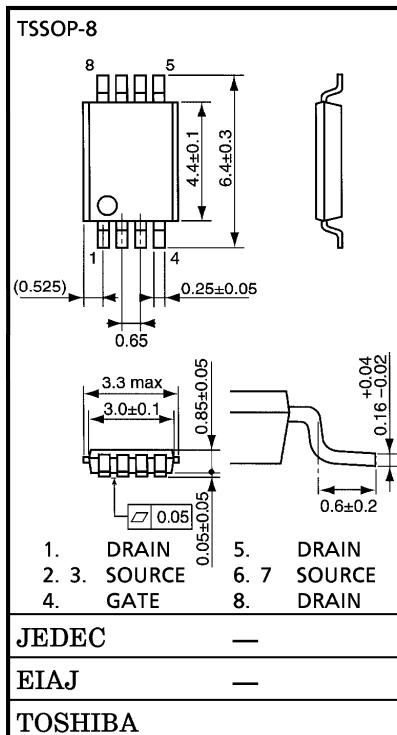
** Repetitive rating ; Pulse Width Limited by Max. Junction temperature.

*** Drive operation ; Mount on glass epoxy board [$1 \text{ inch}^2 \times 0.8 \text{ t}$]
($t = 10 \text{ s}$)

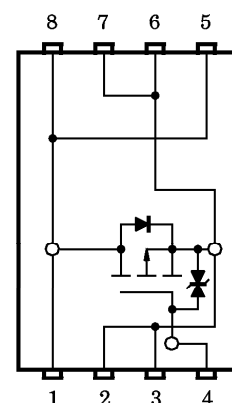
This transistor is an electrostatic sensitive device.
Please handle with caution.

INDUSTRIAL APPLICATIONS

Unit in mm



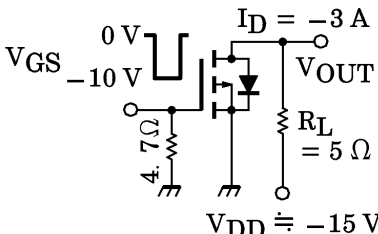
CIRCUIT CONFIGURATION



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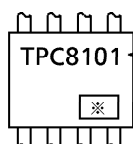
ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Gate Leakage Current		I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V	—	—	±10	μA
Drain Cut-Off Current		I _{DSS}	V _{DS} = −30 V, V _{GS} = 0 V	—	—	−10	μA
Drain-Source Breakdown Voltage		V _(BR) DSS	I _D = −10 mA, V _{GS} = 0 V	−30	—	—	V
		V _(BR) DSX	I _D = −10 mA, V _{GS} = 20 V	−15	—	—	
Gate Threshold Voltage		V _{th}	V _{DS} = −10 V, I _D = −1 mA	−0.8	—	−2.0	V
Drain-Source ON Resistance		R _{DS (ON)}	V _{GS} = −4 V, I _D = −3 A	—	32	40	mΩ
			V _{GS} = −10 V, I _D = −3 A	—	15	25	
Forward Transfer Admittance		Y _{fs}	V _{DS} = −10 V, I _D = −3 A	6	12	—	S
Input Capacitance		C _{iss}	V _{DS} = −10 V, V _{GS} = 0 V, f = 1 MHz	—	1810	—	pF
Reverse Transfer Capacitance		C _{rss}		—	350	—	
Output Capacitance		C _{oss}		—	610	—	
Switching Time	Rise Time	t _r	 V _{IN} : t _r , t _f < 5 ns Duty ≤ 1%, t _w = 10 μs	—	9	—	ns
	Turn-On Time	t _{on}		—	15	—	
	Fall Time	t _f		—	49	—	
	Turn-Off Time	t _{off}		—	135	—	
Total Gate Charge (Gate-Source Plus Gate-Drain)		Q _g	V _{DD} ≐ −24 V, V _{GS} = −10 V I _D = −6 A	—	37	—	nC
Gate-Source Charge		Q _{gs}		—	30	—	
Gate-Drain (“Miller”) Charge		Q _{gd}		—	7	—	

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Continuous Drain Reverse Current	I_{DR}	—	—	—	-6	A
Pulse Drain Reverse Current	I_{DRP}	—	—	—	-24	A
Diode Forward Voltage	V_{DSF}	$I_{DR} = -6 \text{ A}, V_{GS} = 0 \text{ V}$	—	—	1.2	V

MARKING



TYPE

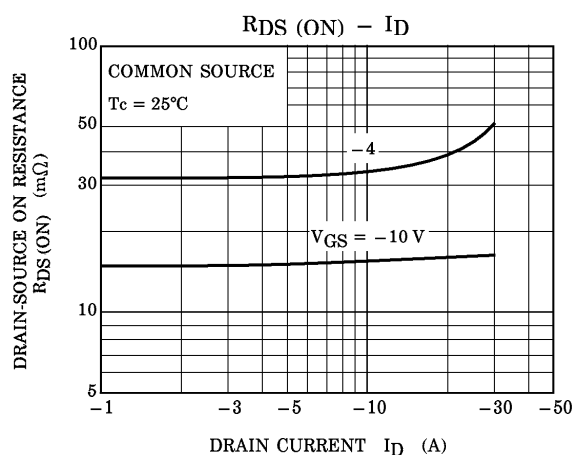
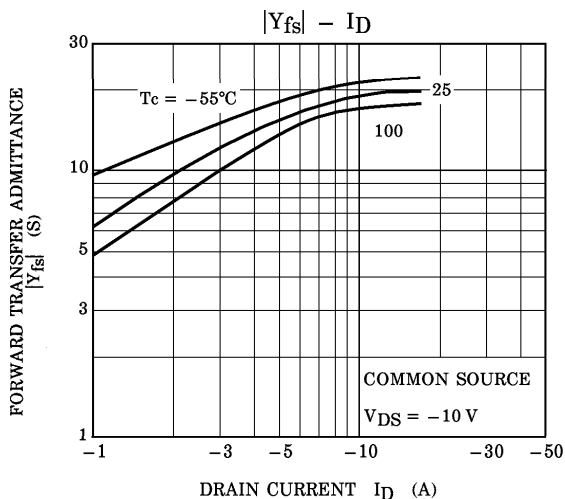
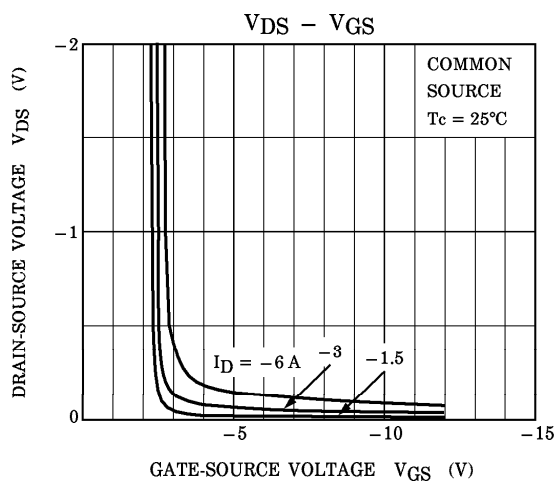
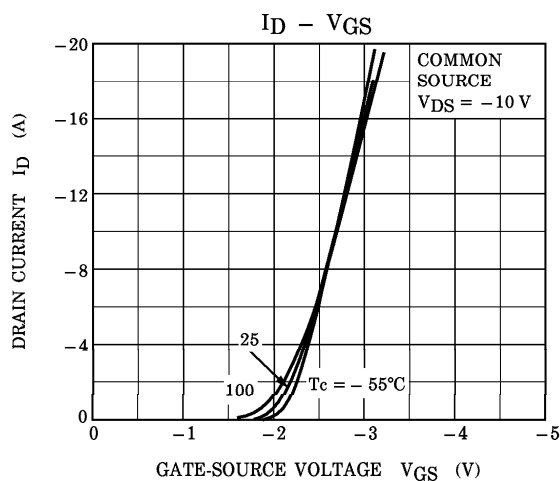
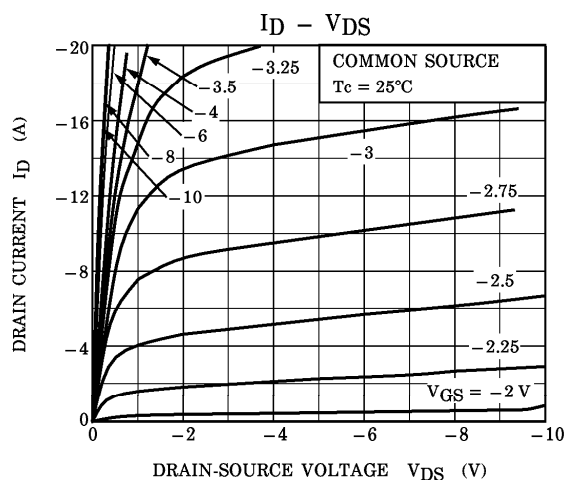
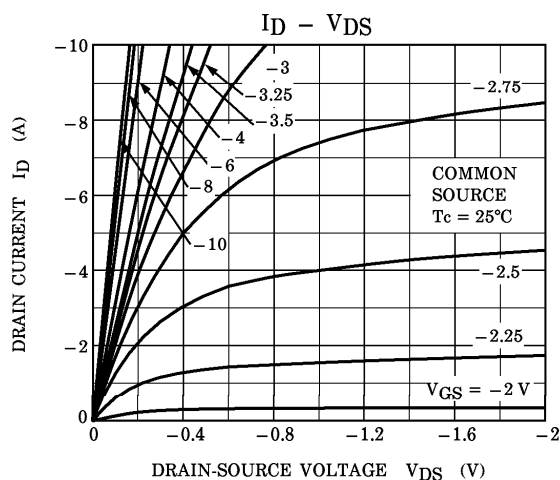
※ Lot Number

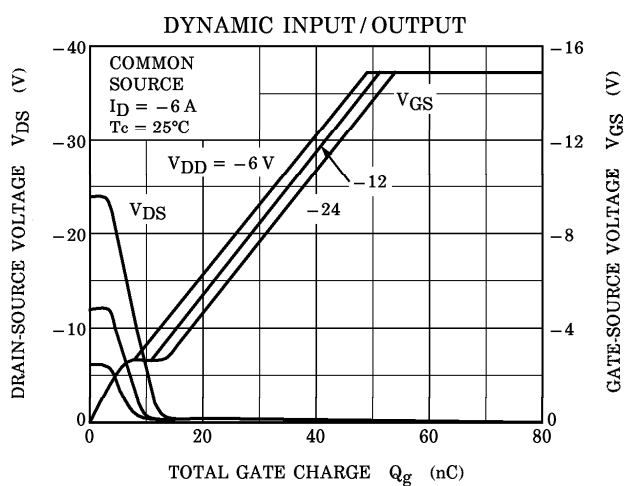
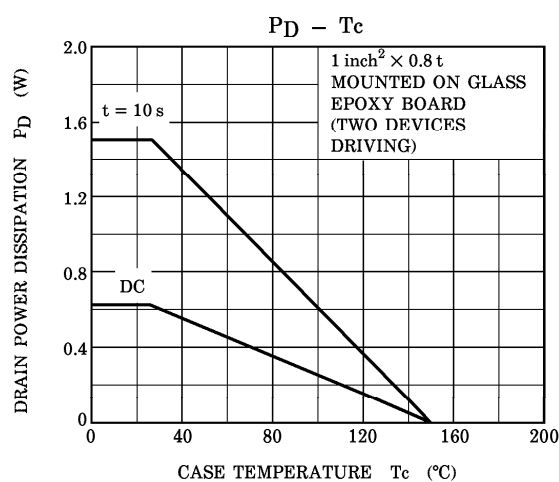
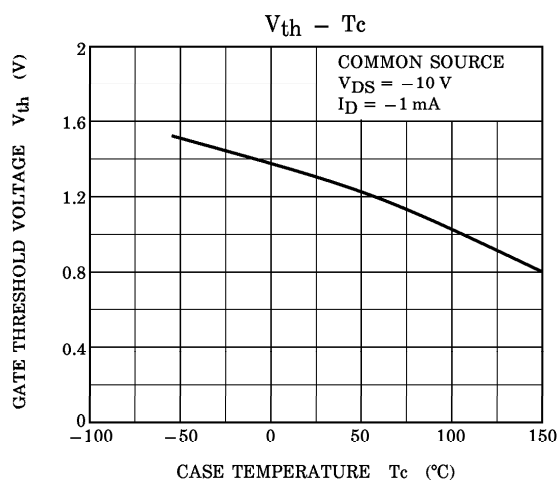
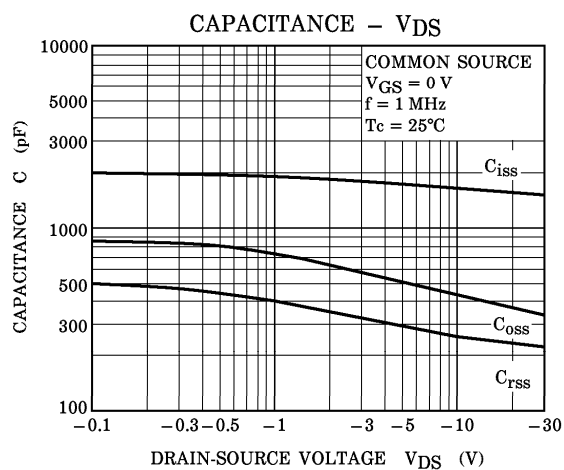
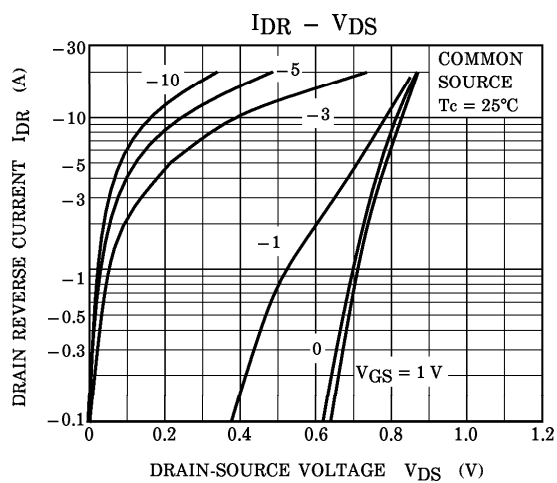
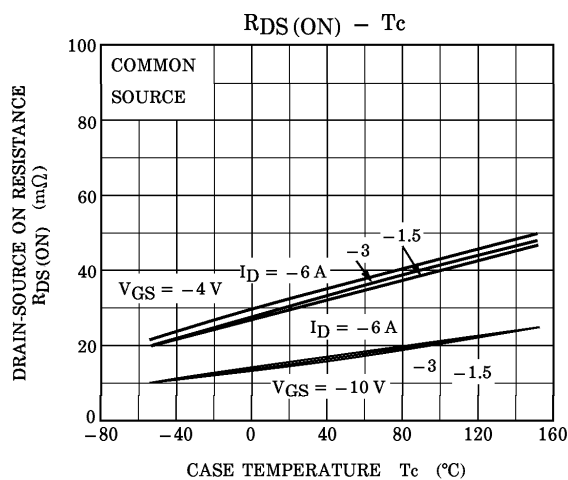


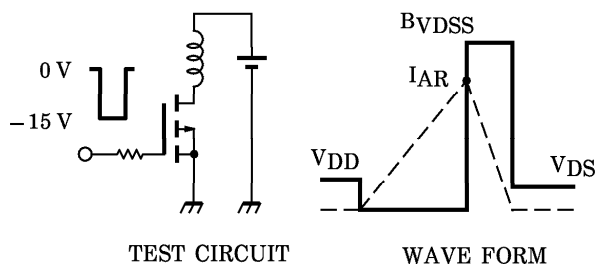
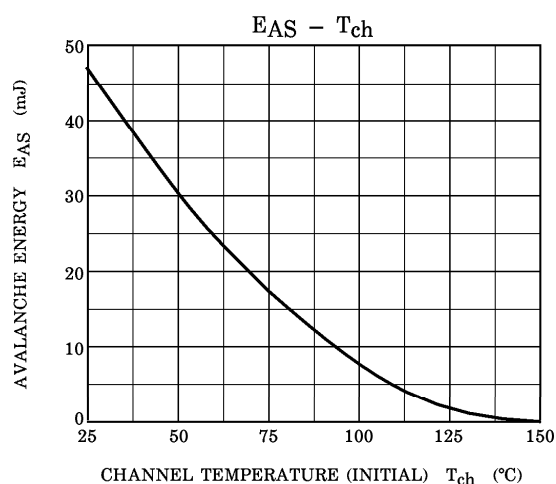
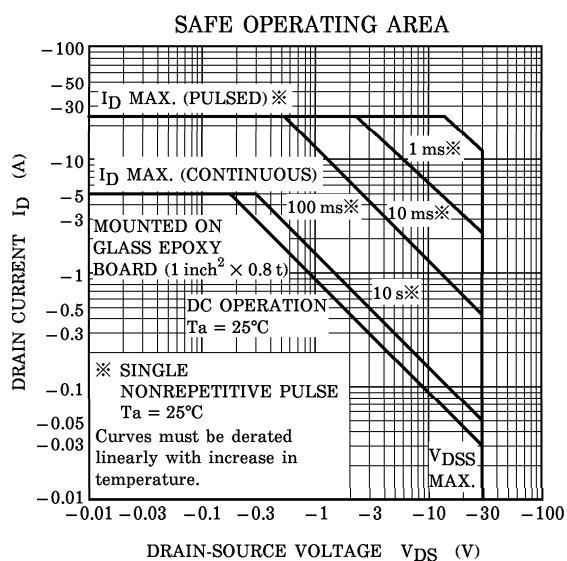
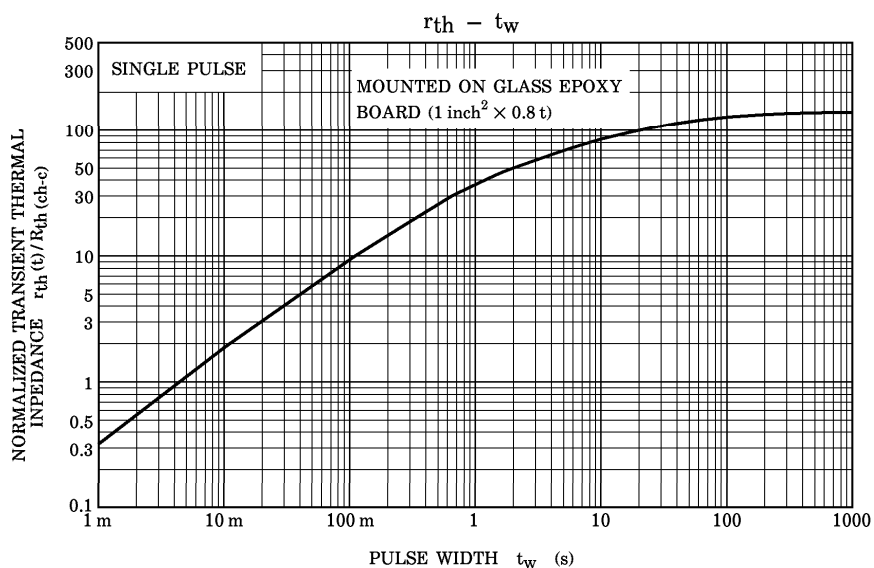
Month (Starting from Alphabet A)



Year (Last Number of the Christian Era)







$$\begin{aligned} \text{Peak } I_{AR} &= -6 \text{ A, } R_G = 25 \, \Omega \\ V_{DD} &= -16 \text{ V, } L = 1.0 \text{ mH} \end{aligned} \quad E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{B_{VDSS}}{B_{VDSS} - V_{DD}} \right)$$