**TOSHIBA** TC7SZ02F/FU

TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

# TC7SZ02F, TC7SZ02FU

# 2 INPUT NOR GATE

#### **FEATURES**

:  $\pm 24mA (Typ.) (V_{CC} = 3V)$ High Output Drive

Super High Speed Operation :  $t_{PD} = 2.4 \text{ns}$  (Typ.)

 $(V_{CC} = 5V, 50pF)$ 

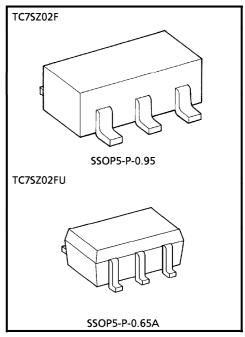
Operation Voltage Range :  $V_{CC (opr)} = 1.8 \sim 5.5 V$ 

**5V Toleratnt Function** 

Matches the Performance of TC74LCX Series when Operated at 3.3V V<sub>CC</sub>

#### **MAXIMUM RATINGS** ( $Ta = 25^{\circ}C$ )

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage Range	V <sub>C</sub> C	-0.5~6	V
DC Input Voltage	VIN	-0.5~6	V
DC Output Voltage	Vout	-0.5~6	V
Input Diode Current	ΙΚ	± 20	mA
Output Diode Current	<sup>I</sup> ОК	± 20	mA
DC Output Current	IOUT	± 50	mA
DC V <sub>CC</sub> / Ground Current	Icc	± 50	mA
Power Dissipation	PD	200	mW
Storage Temperature	T <sub>stg</sub>	<b>-65∼150</b>	°C
Lead Temperature (10s)	ΤL	260	°C



Weight

SSOP5-P-0.95 : 0.016g (Typ.) SSOP5-P-0.65A : 0.006g (Typ.)

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# DC ELECTRICAL CHARACTERISTICS

CHADACTERISTIC	CVMBOL	DL TEST CONDITION		Vcc Ta = 25°C		C	Ta = -40~85°C		UNIT	
CHARACTERISTIC	IC STIMBOL TEST CONDITION		ပ( (၃)	MIN.	TYP.	MAX.	MIN.	MAX.	UNIT	
High-Level				1.8	0.88× V <sub>C</sub> C	_	_	0.88 × V <sub>C</sub> C	_	٧
Input Voltage	V <sub>IH</sub>			2.3~ 5.5	0.75 × V <sub>C</sub> C	_	_	0.75 × V <sub>C</sub> C	_	٧
Low-Level	V			1.8	_	_	0.12 × V <sub>C</sub> C	_	0.12 x V <sub>CC</sub>	٧
Input Voltage	V <sub>IL</sub>			2.3~ 5.5	_	_	0.25 × V <sub>C</sub> C	_	0.25 × V <sub>CC</sub>	V
				1.8	1.7	1.8	_	1.7	_	
			100	2.3	2.2	2.3	_	2.2	_	V
		V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	$I_{OH} = -100\mu A$	3.0	2.9	3.0	_	2.9	_	
High-Level	V			4.5	4.4	4.5	_	4.4	_	
Output Voltage	VOH		I <sub>OH</sub> = -8mA	2.3	1.9	2.15	_	1.9	_	V
			I <sub>OH</sub> = - 16mA	3.0	2.4	2.8	_	2.4	_	
			I <sub>OH</sub> = - 24mA	3.0	2.3	2.68	_	2.3	_	
			$I_{OH} = -32mA$	4.5	3.8	4.2	_	3.8	_	
			Ι <sub>ΟΗ</sub> = 100μΑ	1.8	_	0	0.1	_	0.1	V
				2.3	_	0	0.1	_	0.1	
				3.0	_	0	0.1	_	0.1	
Low-Level	V	$V_{IN} = V_{IH}$		4.5	_	0	0.1	_	0.1	
Output Voltage	VOL	VOL Or VIL	I <sub>OH</sub> = 8mA	2.3	_	0.1	0.3	_	0.3	V
			I <sub>OH</sub> = 16mA	3.0	_	0.15	0.4	_	0.4	
			I <sub>OH</sub> = 24mA	3.0	_	0.22	0.55	_	0.55	
			I <sub>OH</sub> = 32mA	4.5	_	0.22	0.55	_	0.55	
Input Leakage Current	ΙΝ	V <sub>IN</sub> = 5.5V or GND		0~ 5.5	_	_	± 1	_	± 10	μΑ
Power Off Leakage Current	lOFF	V <sub>IN</sub> or V <sub>OUT</sub> = 5.5V		0.0	_	_	1	_	10	μΑ
Quiescent Supply Current	lcc	V <sub>IN</sub> = V <sub>CC</sub> o	or GND	5.5	_	_	2	_	20	μΑ

<b>AC ELECTRICAI</b>	. CHARACTERISTICS	(Input $t_r = t_f = 3ns$ )
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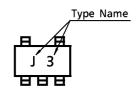
CHARACTERISTIC SYMBOL		TEST CONDITION	Vcc	Ta = 25°C			Ta = -40~85°C		UNIT
CHARACTERISTIC	STIMBOL	TEST CONDITION	(A)	MIN.	TYP.	MAX.	MIN.	MAX.	UNIT
Propagation tpLH Delay Time tpHL		$C_L = 15pF, R_L = 1M\Omega$	1.8	2.0	4.4	9.5	2.0	10.0	
			2.5 ± 0.2	0.8	2.9	6.5	0.8	7.0	
	tPLH		3.3 ± 0.3	0.5	2.3	4.5	0.5	4.7	
		5.0 ± 0.5	0.5	1.9	3.9	0.5	4.1	ns	
		$C_L = 50 pF, R_L = 500 \Omega$	3.3 ± 0.3	1.5	2.9	5.0	1.5	5.2	
			5.0 ± 0.5	0.8	2.4	4.3	0.8	4.5	
Input Capacitance	c <sub>IN</sub>		0~5.5		4	_	_	_	pF
Power Dissipation	C==	(Note 1)	3.3		23	_	_	_	n.E
Capacitance	C <sub>PD</sub>	(Note 1)	5.5	_	30	_	_	_	рF

(Note 1): C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

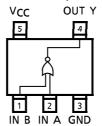
Average operating current can be obtained by the equation.

$$ICC (opr) = CPD \cdot VCC \cdot fIN + ICC$$

#### **MARKING**



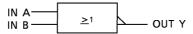
# PIN ASSIGNMENT (TOP VIEW)



### TRUTH TABLE

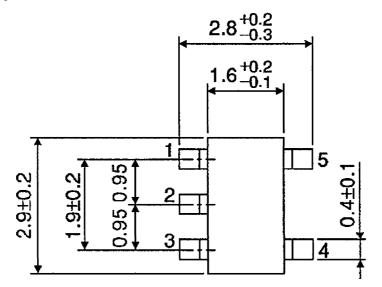
А	В	Y
L	L	Н
L	Ι	Ц
Н	L	L
Н	Н	L

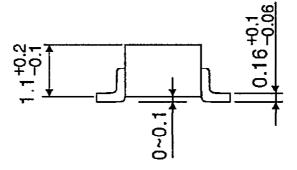
**LOGIC DIAGRAM** 



## **OUTLINE DRAWING** SSOP5-P-0.95

Unit: mm

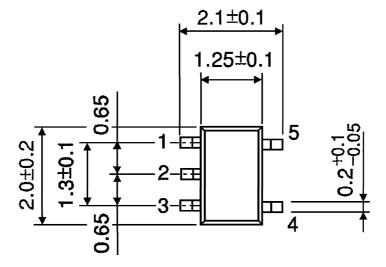


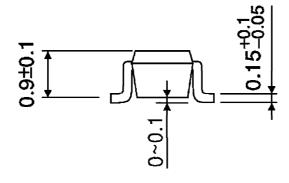


Weight: 0.016g (Typ.)

## OUTLINE DRAWING SSOP5-P-0.65A

Unit: mm





Weight: 0.006g (Typ.)