TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7WB66FK

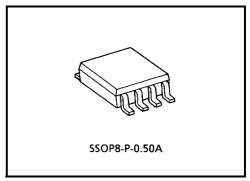
Dual Bus Switch

The TC7WB66FK is a low on-resistance, high-speed CMOS2-bit bus switch. This bus switch allows the connections or disconnections to be made with minimal propagation delay while maintaining Low power dissipation which is the feature of CMOS.

When output enable (OE) is at High level, the switch is on; when at Low level, the switch is off.

P-MOS and N-MOS channel block means the device is suitable for analog signal transmission.

All inputs are equipped with protector circuits to protect the device from static discharge.



Weight: 0.01 g (typ.)

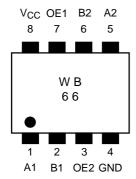
Features

- Operating voltage: $VCC = 2 \sim 5.5 \text{ V}$
- High speed operation: $t_{pd} = 0.25 \text{ ns (max)}$
- Ultra-low on resistance: $RON = 5 \Omega$ (typ.)
- Electro-static discharge (ESD) performance: ±200 V or more (JEITA)

±2000 V or more (MIL)

- High noise margin: VNIL = VNIH = 28% VCC (min)
- Power-down protection for inputs (control inputs only)
- Package: US8

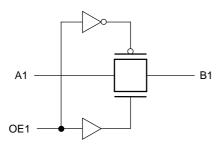
Pin Assignment (top view)

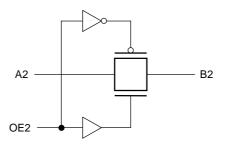


Truth Table

Inputs	Function
OE	Tancton
Н	A port = B port
L	Disconnect

System Diagram





Maximum Ratings

Chara	cteristics	Symbol	Rating	Unit	
Power supply volta	age	V _{CC}	-0.5~7.0	V	
Control pin input v	oltage	V _{IN}	-0.5~7.0	V	
Switch terminal I/O voltage		Vs	Vs -0.5~V _{CC} + 0.5		
Clump diode current	Control input pin	lık	-50	mA	
	Switch terminal	чк	±50		
Switch I/O current		I _S	128	mA	
Power dissipation		PD	200	mW	
DC V _{CC} /GND current		I _{CC} /I _{GND}	±100	mA	
Storage temperatu	ıre	T _{stg}	-65~150	°C	

Recommended Operating Conditions

Characteristics	Symbol	Rating	Unit
Power supply voltage	V _{CC}	2.0~5.5	V
Control pin input voltage	V _{IN}	0~5.5	V
Switch I/O voltage	Vs	0~V _{CC}	V
Operating temperature	T _{opr}	-40~85	°C
Control pin input rise/fall time	dt/dv	0~10	ns/V

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TC7WB66FK

Electrical Characteristics

DC Characteristics ($Ta = -40 \sim 85$ °C)

Characteristics		Symbol	Test Condition	.,	Min	Typ. (Note1)	Max	Unit
	T			V _{CC} (V)		(110101)		
Control pin input	"H" level	V_{IH}	_	2.0~5.5	$^{0.7\times}_{\text{CC}}$	_	_	V
voltage "L" level V _{IL}		V _{IL}	_	2.0~5.5			0.3 × V _{CC}	V
Control pin input l	eakage	I _{IN}	V _{IN} = 0~5.5 V	2.0~5.5	_	_	±1.0	μΑ
Off-state leakage (switch off)	current	I _{SZ}	A, B = 0~V _{CC} , OE = GND	2.0~5.5	_	_	±1.0	μА
		R _{ON}	$V_{IS} = 0 \text{ V}, I_{IS} = 30 \text{ mA}$	4.5	_	3	7	
			$V_{IS} = 4.5 \text{ V}, I_{IS} = 30 \text{ mA}$	4.5	_	5	15	
ON resistance (Note 2)	$V_{IS} = 2.4 \text{ V}, I_{IS} = 15 \text{ mA}$		4.5	_	6	12		
	V _{IS} = 0 V, I _{IS} = 24 mA		3.0	_	4	9	Ω	
	$V_{IS} = 3 \text{ V}, I_{IS} = 24 \text{ mA}$		3.0	_	7	20		
			V _{IS} = 0 V, I _{IS} = 8 mA	2.0		6	12	
			$V_{IS} = 2 \text{ V}, I_{IS} = 8 \text{ mA}$	2.0		10	30	
Quiescent supply	current	Icc	$V_{IN} = V_{CC}$ or GND, $I_{OUT} = 0$	5.5	_	_	10	μΑ

Note 1: The typical values are at Ta = 25°C.

Note 2: Apply the specified current to the switch, then measure the voltages on pins A and B. The on-resistance is the lower of the two.

AC Characteristics ($Ta = -40 \sim 85$ °C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
Propagation delay time	t _{pLH} Figu	Figure 1, Figure 2 (Note 3)	2.0 3.3 ± 0.3		0.5 0.35	ns
(bus to bus)			5.0 ± 0.5	_	0.25	
Output enable time	4	Figure 1, Figure 3	2.0	_	11.5	
			3.3 ± 0.3	_	6	ns
	чр∠Н		5.0 ± 0.5	_	4.5	
Output disable time	4		2.0	_	11.5	
	-	Figure 1, Figure 3	3.3 ± 0.3	_	6.5	ns
	чрНZ		5.0 ± 0.5	_	5	

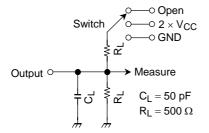
Note 3: The propagation delay time is calculated by the RC (on-resistance and load capacitance) time constant.

Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition		V _{CC} (V)	Тур.	Unit
Control pin input capacitance	C _{IN}		(Note 4)	5.0	3	pF
Switch terminal capacitance	C _{I/O}	OE = GND	(Note 4)	5.0	10	pF

Note 4: Guaranteed by design.

AC Test Circuit



Parameter	Switch
t _{pLH} , t _{pHL}	Open
t _{pLZ} , t _{pZL}	$2 \times V_{CC}$
t _{pHZ} , t _{pZH}	GND

Figure 1

AC Waveform

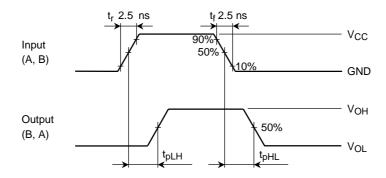


Figure 2 t_{pLH}, t_{pHL}

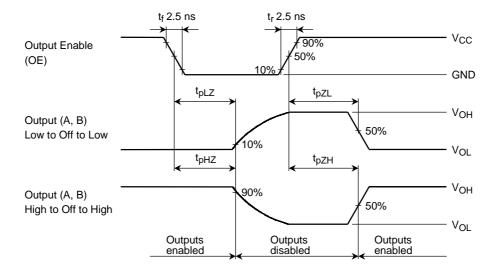
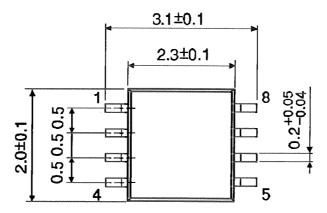


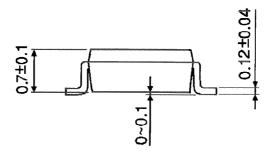
Figure 3 t_{pLZ} , t_{pHZ} , t_{pZL} , t_{pZH}

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Package Dimensions

SSOP8-P-0.50A Unit: mm





Weight: 0.01 g (typ.)

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