TOSHIBA TC7W02F/FU/FK

TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

# TC7W02F, TC7W02FU, TC7W02FK

#### **DUAL 2-INPUT NOR GATE**

The TC7W02 is a high speed C<sup>2</sup>MOS 2-INPUT NOR GATE fabricated with silicon gate C<sup>2</sup>MOS technology.

It achives the high speed operation similar to equivalent LSTTL while maintaining the C<sup>2</sup>MOS low power dissipation.

The internal circuit is composed of 3 stages including buffer output, which enables high noise immunity and stable output.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

#### **FEATURES**

• High Speed t <sub>pd</sub> =	= 6ns (Typ.)	at
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 $V_{CC} = 5V$ 

• Low Power Dissipation ............  $I_{CC} = 1\mu A$  (Max.) at

 $Ta = 25^{\circ}C$ 

• High Noise Immunity ...... V<sub>NIH</sub> = V<sub>NIL</sub>

= 28% V<sub>CC</sub> (Min.)

Output Drive Capability ...... 10 LSTTL Loads

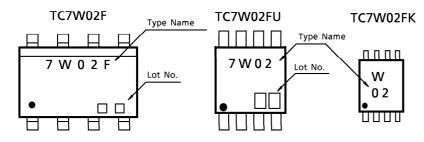
• Symmetrical Output Impedance ...  $|I_{OH}| = I_{OL} = 4mA$ 

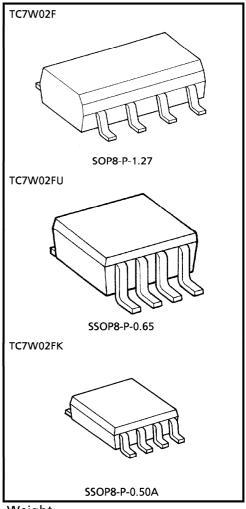
(Min.)

ullet Balanced Propagation Delays .....  $t_{pLH} \dot{=} t_{pHL}$ 

Wide Operating Voltage Range ... V<sub>CC (opr)</sub> = 2~6V

#### MARKING





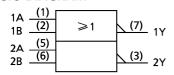
Weight

SOP8-P-1.27 : 0.05g (Typ.) SSOP8-P-0.65 : 0.02g (Typ.) SSOP8-P-0.50A : 0.01g (Typ.)

#### **MAXIMUM RATINGS** (Ta = $25^{\circ}$ C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage Range	V <sub>C</sub> C	-0.5~7	V
DC Input Voltage	VIN	-0.5~V <sub>CC</sub> +0.5	٧
DC Output Voltage	Vout	-0.5~V <sub>CC</sub> +0.5	٧
Input Diode Current	ΙΙΚ	± 20	mA
Output Diode Current	loк	± 20	mA
DC Output Current	IOUT	± 25	mA
DC V <sub>CC</sub> / Ground Current	lcc	± 25	mΑ
Power Dissipation	PD	300	mW
Storage Temperature	T <sub>stg</sub>	<b>-65∼150</b>	°C
Lead Temperature (10s)	TL	260	°C

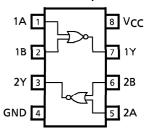
#### LOGIC DIAGRAM



#### TRUTH TABLE

Α	В	Y
L	L	Н
L	Н	L
Н	L	L
Н	Н	L

## PIN ASSIGNMENT (TOP VIEW)



### **RECOMMENDED OPERATING CONDITIONS**

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage Range	Vcc	2~6	V
Input Voltage	V <sub>IN</sub>	0~V <sub>CC</sub>	V
Output Voltage	Vout	0~V <sub>CC</sub>	V
Operating Temperature	T <sub>opr</sub>	- 40~85	°C
Input Rise and Fall Time	t <sub>r</sub> , t <sub>f</sub>	$0\sim1000 \text{ (V}_{CC}=2.0\text{V)}$ $0\sim500 \text{ (V}_{CC}=4.5\text{V)}$	ns
		$0 \sim 400 \ (V_{CC} = 6.0V)$	

#### DC ELECTRICAL CHARACTERISTICS

CHARACTERISTIC	CVMPOL	MIROLL TEST CONDITION			Т	a = 25°	C	Ta = -4	ŀ0~85°C	UNIT
CHARACTERISTIC	STIVIBUL			TYP.	MAX.	MIN.	MAX.	UNIT		
High Lovel				2.0	1.5	_	_	1.5	_	
High-Level Input Voltage	$V_{IH}$		_	4.5	3.15	_	—	3.15	_	V
input voitage				6.0	4.2	_	—	4.2	_	
Low-Level				2.0	_	_	0.5	_	0.5	
Input Voltage	V <sub>IL</sub>		_	4.5	—	_	1.35	_	1.35	V
input voitage				6.0	_	_	1.8	_	1.8	
				2.0	1.9	2.0	—	1.9	_	
High Lovel		V.s V	$I_{OH} = -20\mu A$	4.5	4.4	4.5	—	4.4	_	
High-Level	∨ <sub>OH</sub>	$V_{IN} = V_{IL}$	IL	6.0	5.9	6.0	—	5.9	_	V
Output Voltage			$I_{OH} = -4mA$	4.5	4.18	4.31	_	4.13	_	
			$I_{OH} = -5.2mA$	6.0	5.68	5.80	—	5.63	_	
				2.0	_	0.0	0.1	_	0.1	
l avy laval		Mari Mari	$I_{OL} = 20 \mu A$	4.5	—	0.0	0.1	_	0.1	
Low-Level	V <sub>OL</sub>	$V_{IN} = V_{IH}$		6.0	_	0.0	0.1	_	0.1	V
Output Voltage		or V <sub>IL</sub>	$I_{OL} = 4mA$	4.5	_	0.17	0.26	_	0.33	
			$I_{OL} = 5.2 \text{mA}$	6.0	_	0.18	0.26	_	0.33	
Input Leakage Current	IN	V <sub>IN</sub> = V <sub>CC</sub> or GND		6.0	_	_	± 0.1	_	± 1.0	
Quiescent Supply Current	lcc	$V_{IN} = V_{CC}$	or GND	6.0	_	_	1.0	_	10.0	μΑ

## AC ELECTRICAL CHARACTERISTICS ( $C_L = 15pF$ , $V_{CC} = 5V$ , Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	Т	UNIT		
CHARACTERISTIC	STIVIBOL	TEST CONDITION		TYP.	MAX.	ONIT
Output Transition	tTLH	_		4	8	ns
Time	t <sub>THL</sub>			-		113
Propagation Delay	t <sub>pLH</sub>			6	12	ns
Time	t <sub>pHL</sub>	_			12	113

#### AC ELECTRICAL CHARACTERISTICS ( $C_L = 50 \text{ pF}$ , Input $t_r = t_f = 6 \text{ ns}$ )

		· • · · · · · · · · · · · · · · · · · ·							
CHARACTERISTIC	SYMBOL	TEST CONDITION		Т	a = 25°	C.	Ta = -4	l0∼85°C	UNIT
CHARACTERISTIC	3 I WIBOL	TEST CONDITION	Vcc	MIN.	TYP.	MAX.	MIN.	MAX.	UNIT
Output Transition	<b>+</b>		2.0	_	25	75	_	95	
•	t <sub>TLH</sub>	_	4.5	l —	7	15	_	19	ns
Time t <sub>THL</sub>	THL		6.0	—	6	13	_	16	
B	4		2.0	_	27	75	_	95	
Propagation Delay Time	t <sub>pLH</sub>	_	4.5	l —	9	15	_	19	ns
rime	t <sub>pHL</sub>		6.0	—	8	13	_	16	
Input Capacitance	CIN	_		_	5	10	_	10	
Power Dissipation Capacitance	C <sub>PD</sub>	(Note 1)		_	21	_	_	_	pF

Note 1 : CpD is defined as the value of internal equivalent capacitance of IC which is calculated from the operating current consumption without load (refer to Test Circuit).

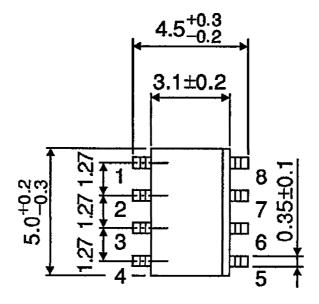
Average operating current can be obtained by the equation hereunder.

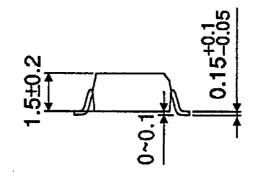
ICC (opr) = CpD·VcC·flN + Icc / 2 (per gate)

# **PACKAGE DIMENSIONS**

SOP8-P-1.27

Unit: mm

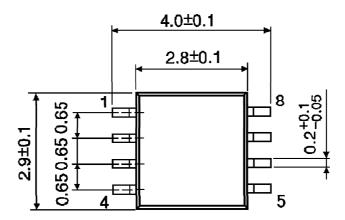


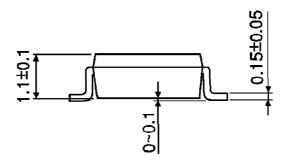


Weight: 0.05g (Typ.)

# PACKAGE DIMENSIONS

SSOP8-P-0.65 Unit: mm

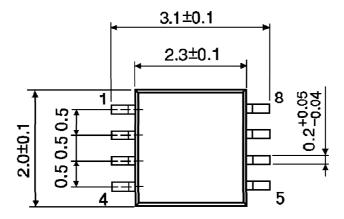


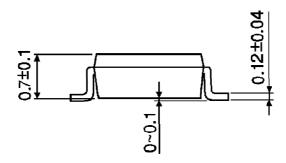


Weight: 0.02g (Typ.)

# PACKAGE DIMENSIONS

SSOP8-P-0.50A Unit: mm





Weight: 0.01g (Typ.)

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