TENTATIVE

TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

TC7WH14FU, TC7WH14FK

(UNDER DEVELOPMENT)

TRIPLE SCHMITT INVERTER

The TC7WH14 is an advanced high speed CMOS SCHMITT INVERTER fabricated with silicon gate CMOS technology. It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation. Pin configuration and function are the same as the TC7SH14 but the inputs have hysteresis and with its schmitt trigger function, the TC7SH14 can be used as a line receivers which will receive slow input signals. An input protection circuit ensures that 0 to 7V can be applied to the input pins without regard to the supply voltage. This device can be used to interface 5V to 3V systems and two supply systems such as battery back up. This circuit prevents device destruction due to mismatched supply and input voltages.

FEATURES

• High Speed $\cdots t_{pd} = 5.5 \text{ns}$ (Typ.) at

 $\dot{V}_{CC} = 5V$

• Low Power Dissipation $\cdots I_{CC} = 2\mu A$ (Max.) at

 $Ta = 25^{\circ}C$

High Noise Immunity VNIH = VNIL = 28% VCC

Min.)

Power Down Protection is provided on all inputs.

Balanced Propagation Delays ······ t_{pLH}≒t_{pHL}

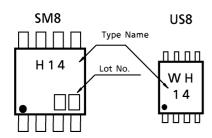
• Wide Operating Voltage Range ··· V_{CC} (opr) = 2~5.5V

TC7WH14FU SSOP8-P-0.65 TC7WH14FK SSOP8-P-0.50A

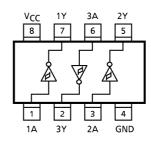
Weight

SSOP8-P-0.65 : 0.02g (Typ.) SSOP8-P-0.50A : 0.01g (Typ.)

MARKING



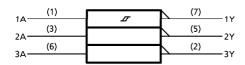
PIN ASSIGNMENT (TOP VIEW)



MAXIMUM RATINGS (Ta = 25° C)

CHARACTERISTIC	SYMBOL	RATING	UNIT	
Supply Voltage Range	Vcc	- 0.5~7.0	V	
DC Input Voltage	V _{IN}	- 0.5~7.0	V	
DC Output Voltage	Vout	-0.5~V _{CC} + 0.5	V	
Input Diode Current	ΙΚ	- 20	mA	
Output Diode Current	^I ок	± 20	mA	
DC Output Current	lout	± 25	mA	
DC V _{CC} / Ground Current	lcc	± 50	mA	
Payer Dissination	D-	300 (SM8)	mW	
Power Dissipation	PD	200 (US8)	11100	
Storage Temperature	T _{stg}	-65∼150	°C	
Lead Temperature (10 s)	TL	260	°C	

LOGIC DIAGRAM



TRUTH TABLE

Α	Υ
L	Н
Н	L

RECOMMENDED OPERATING CONDITIONS

CHARACTERISTIC	SYMBOL	RATING	UNIT	
Supply Voltage	Vcc	2.0~5.5	V	
Input Voltage	VIN	0~5.5	V	
Output Voltage	Vout	0~V _{CC}	V	
Operating Temperature	T _{opr}	- 40∼85	°C	
Input Rise and Fall Time	dt / dv	$0 \sim 100 \text{ (V}_{CC} = 3.3 \pm 0.3 \text{V)}$	nc /\/	
input Rise and Fail Time	ut/uv	$0\sim20 \ (V_{CC} = 5 \pm 0.5V)$	ns/V	

DC ELECTRICAL CHARACTERISTICS

CHARACTERISTIC SYMBOI		TEST CONDITION		Vсс (V)	Ta = 25°C			$Ta = -40 \sim 85^{\circ}C$		UNIT
CHARACTERISTIC	STIVIBUL	IESI C	TEST CONDITION		MIN.	TYP.	MAX.	MIN.	MAX.	UNIT
Positive				3.0		_	2.20	_	2.20	
Threshold	VP			4.5		_	3.15		3.15	V
Voltage				5.5	1	_	3.85	_	3.85	
Negative				3.0	0.90	_	_	0.90	_	
Threshold	٧N			4.5	1.35	_	_	1.35	_	V
Voltage				6.0	1.65	_		1.65	_	
Hysteresis				3.0	0.30	_	1.20	0.30	1.20	
Voltage	VΗ			4.5	0.40	_	1.40	0.40	1.40	V
Voltage				0.50	_	1.60	0.50	1.60		
	V _{OH}	V _{IN} = V _{IL}	ΙΟΗ = - 50μΑ	2.0	1.9	2.0		1.9	_	v
High-Level				3.0	2.9	3.0		2.9	_	
Output Voltage				4.5	4.4	4.5		4.4	_	
Catput Voltage			$I_{OH} = -4mA$	3.0	2.58	_		2.48	_	
			$I_{OH} = -8mA$	4.5	3.94	_	_	3.80	_	
		V _{IN} = V _{IH}	I _{OL} = 50μA	2.0	_	0.0	0.1	_	0.1	V
Low-Level				3.0	_	0.0	0.1	_	0.1	
Output Voltage	VOL			4.5	_	0.0	0.1	_	0.1	
Catput Voltage			I _{OL} = 4mA	3.0	_	_	0.36	_	0.44	
		I _{OL} = 8mA	4.5	_	_	0.36	_	0.44		
Input Leakage Current	IIN	V _{IN} = 5.5V or GND		0~ 5.5	I	_	± 0.1	_	± 1.0	μ A
Quiescent Supply Current	lcc	V _{IN} = V _{CC} or GND		5.5		_	2.0	_	20.0	μ A

AC ELECTRICAL	CHARACTERISTICS	(Input $t_r = t_f = 3ns$)
AC LLLCINICAL	CHANACIENISTICS	(III) Dut tr — tr — 3113/

CHARACTERISTIC SYMBOL		TEST CONDITION		Ta = 25°C			Ta = -40~85°C			
			V _{CC} (V)	C _L (pF)	MIN.	TYP.	MAX.	MIN.	MAX.	UNIT
Propagation Delay t _{pLH} Time t _{pHL}		3.3 ± 0.3	15	_	8.3	12.8	1.0	15.0		
		3.3 ± 0.3	50	_	10.8	16.3	1.0	18.5		
		_	5.0 ± 0.5	15	_	5.5	8.6	1.0	10.0	ns
		3.0 ± 0.3	50	_	7.0	10.6	1.0	12.0		
Input Capacitance	C _{IN}		_		_	4	10	_	10	pF
Power Dissipation Capacitance	C _{PD}	(Note 1)			_	21	_	_		pF

(Note 1): C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

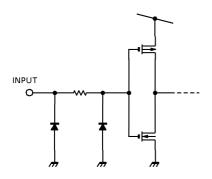
Average operating current can be obtained by the equation:

$$I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

NOISE CHARACTERISTICS (Ta = 25°C, Input $t_r = t_f = 3ns$)

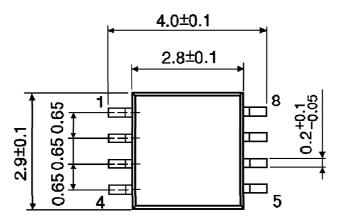
CHARACTERISTIC	SYMBOL	TEST CONDITION	V _{CC} (V)	TYP.	LIMIT	UNIT
Quiet Output Maximum Dynamic V _{OL}	V _{OLP}	C _L = 50pF	5.0	0.3	0.8	٧
Quiet Output Minimum Dynamic V _{OL}	V _{OLV}	C _L = 50pF	5.0	-0.3	-0.8	V
Minimum High Level Dynamic Input Voltage	V _{IHD}	C _L = 50pF	5.0	_	3.5	V
Maximum Low Level Dynamic Input Voltage	V _{ILD}	C _L = 50pF	5.0	l	1.5	V

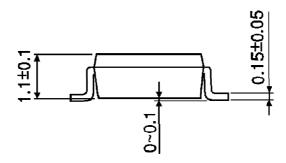
INPUT EQUIVALENT CIRCUIT



PACKAGE DIMENSIONS

SSOP8-P-0.65 Unit: mm

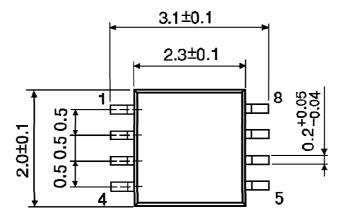


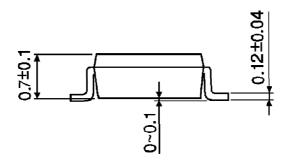


Weight: 0.02g (Typ.)

PACKAGE DIMENSIONS

SSOP8-P-0.50A Unit: mm





Weight: 0.01g (Typ.)

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