Unit in mm

TOSHIBA AC SWITCH OPTICALLY ISOLATED AC SWITCH

SA2100G, 'SA2100J

R.M.S. On-State Current $: I_{T(RMS)} = 0.1 \sim 2A$: $V_{DRM} = 400, 600V$ Repetitive Peak Off-State Voltage

Isolation Voltage between Input to Output: 3000VAC (t=1min.)

Thickness of Inner Insulation Material : 0.8mm (Min.)

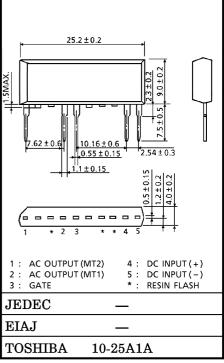
Creepage Distances, Clearances for Insulation

between Input and Output Side : 6mm (Min.)

TTL drive is Available

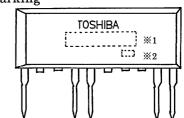
MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC			SYMBOL	RATING	UNIT	
INPUT	Control Input Curre	I _{F (IN)}	50	mA		
	Forward Current De (Ta≥53°C)	ΔI _F /°C	-0.7	mA/°C		
	Peak Forward Curre (100 µs pulse, 100 pp	I_{FP}	1	A		
	Reverse Voltage	v_{R}	5	V		
OUTPUT	Repetitive Peak	TSA2100G	Vapas	400	V	
	Off-State Voltage	TSA2100J	$V_{ m DRM}$	600		
	Nominal AC Line	TSA2100G	V_{AC}	80~125	V	
	Voltage (Note 1)	TSA2100J		80~250		
	R.M.S On-State Cur (Sine Waveform, R.I	I _T (RMS)	0.1~2	A		
	Peak One Cycle Surge On-State		Imaze	20 (50Hz)	Α	
	Current (Non-Repeti	ITSM	22 (60Hz)	A		
	I ² t Limit Value	${ m I}^2{ m t}$	2	A^2s		
Operating Frequency Range			f	45~65	Hz	
Ope	Operating Temperature Range			-40~100	$^{\circ}\mathrm{C}$	
Sto	Storage Temperature Range			-40~100	$^{\circ}\mathrm{C}$	
	ation Voltage out to Output) Note 2	BV_{S}	3000	V		

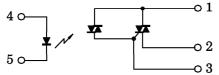


Weight: 2g

Marking



EQUIVALENT CIRCUIT



1 : AC OUTPUT (MT2) 2 : AC OUTPUT (MT1) 4 : DC INPUT (+) 5 : DC INPUT (-)

3:GATE

NUMBER SYMBOL MARK **TSA2100G** TSA21000 **%**1 TYPE TYPE TSA2100J TSA2100J Lot Number Example 3A : January 1993 ×2 3B: February 1993 _Month Staring from 3L: December 1993 Alphabet A Last Number of the Christian era

(The cutted pins near by Pin No.1 & No.3 is connecting in electrically with output terminal)

Note 1: When the voltage larger than applied AC voltage is applied to the device such as 2 phase motor and others, please derating for this maximum rating value. TEST CONDITION...AC, t=60s, $RH \le 60\%$

Note 3: Soldering of printed wiring board should be used under 260°C and 10 seconds.

ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT	
INPUT	Forward Vo	oltage	$V_{\mathbf{F}}$	$I_{ m F} = 10 { m mA}$	1.0	1.15	1.3	V
	Reverse Current		$I_{\mathbf{R}}$	$V_R = 5V$	1	_	10	μ A
	Capacitance		$\mathrm{c_{T}}$	V_T =0 V , f=1 M Hz	1	20	_	pF
OUTPUT	Peak Off-St	ate Current	$I_{ m DRM}$	$V_{ m DRM}$ = Rated	ı	_	10	μ A
	Peak On-St	ate Voltage	$ m V_{TM}$	$I_{TM} = 3.0A$	1	_	1.5	V
	Holding Cu	rrent	$I_{\mathbf{H}}$	V _D =6V, Beginning Current=1A		_	25	mA
	Critical Rat Off-State V	te of Rise of oltage	dv/dt	$V_{ m DRM} = { m Rated}$	_	2000	_	V/μs
	Critical Rat	te of Rise of ng Voltage	(dv / dt) c	$V_D = 400V, -di/dt = 20A/ms$	1	20	_	V/μs
	Thermal	Junction to Lead	$R_{ ext{th}}$ $_{(j-\ell)}$	AC	_	_	22	°C/W
	Resistance	Junction to Ambient	R _{th (j-a)}	AC	_	_	90	°C/W

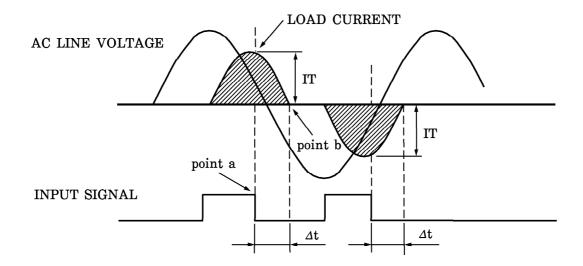
COUPLED ELECTRICAL CHARACTERISTICS (Ta = 25°C)

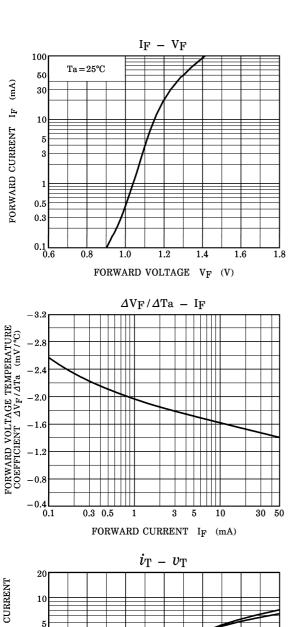
CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Trigger LED Current	I_{FT}	$V_D=6V, R_L=20\Omega$	_		10	mA
Capacitance (Input to output)	c_{S}	$V_S=0V, f=1MHz$	1	0.5	_	pF
Isolation Resistance	$R_{\mathbf{S}}$	V=500V, RH≦60%	10^{9}	_	_	Ω
Turn-off Time	${ m t_{off}}$	OUTPUT : Sine Waveform		_	3/4	cycle

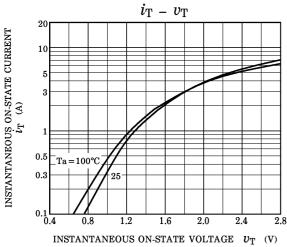
<REMARK>

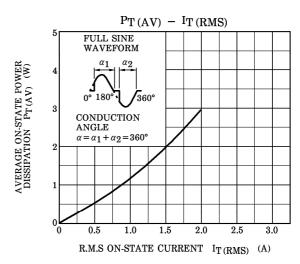
PHASE CONTROL APPLICATION

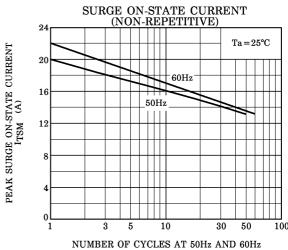
In case of using in phase control application. Δt must be at least 1ms (Δt : The time starting from the end of INPUT SIGNAL "point a" to the point at which load current become ZERO "point b"). And, Load current "IT" at "point a" must be at least double the maximum Holding Current (IH) specification in each operating temperature.

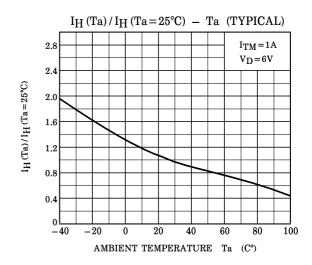


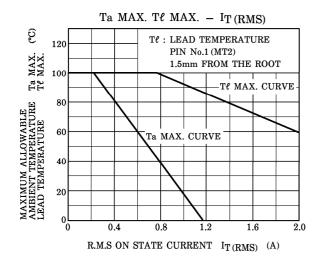


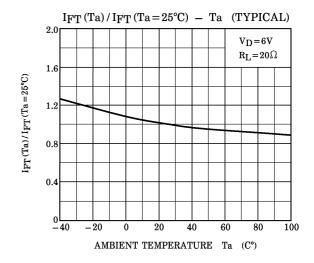


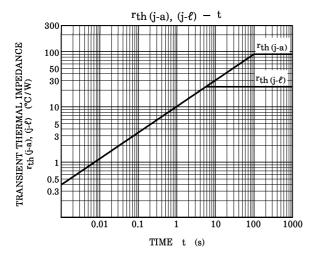


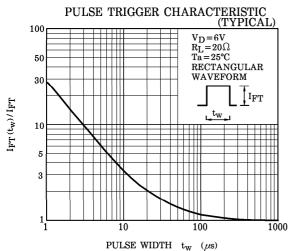












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