**TOSHIBA TA8045BF** 

TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

## **TA8045BF**

### 5V VOLTAGE REGULATOR WITH WATCHDOG TIMER

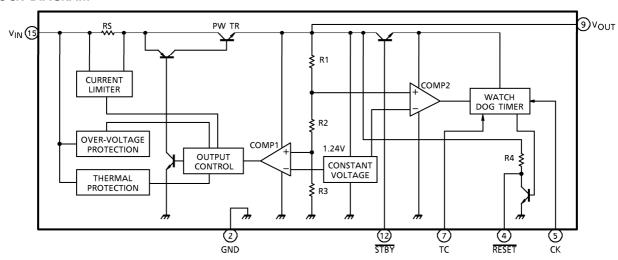
This IC is designed especially for microcomputer system in automobiles and is capable of providing output voltage 5 ±0.15V without any adjustment.

This IC incorporates the system reset function, that is, low voltage reset and watchdog timer which can selfdiagnose the microcomputer system so that program runaway can be prevented.

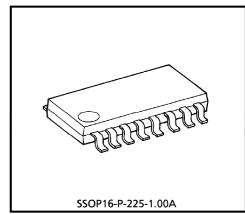
This IC also has an over-voltage protection, current limiter and thermal protection to protect the IC from destruction caused of overstress and load accident. In addition, this IC has a low standby current function, so it can be connected directly to an automotive battery.

- Operating DC Supply Voltage Range: 6~30V
- Transient Supply Voltage: 60V (Max.)
- Extremely low Quiescient Current in Standby Mode  $400\mu A$  (Max.)
- High Accurate Output Voltage: 5 ± 0.15V (3%)
- Output Current Capability: 50mA (Max.)
- Dropout Voltage between VIN and Vout: 1V (lout = 50mA)
- On-Chip System reset function
- : Watchdog Timer, power On Reset Timer, Low Voltage Reset.
- On-Chip Protective function
  - : Current limiter (100mA Typ.), Over-Voltage Protection (28V Typ.), Thermal Protection (150°C
- Wide Operating temperature : -40~105°C (125°C)
- Compact Package: Plastic SSOP 16 Pin Package.

### **BLOCK DIAGRAM**



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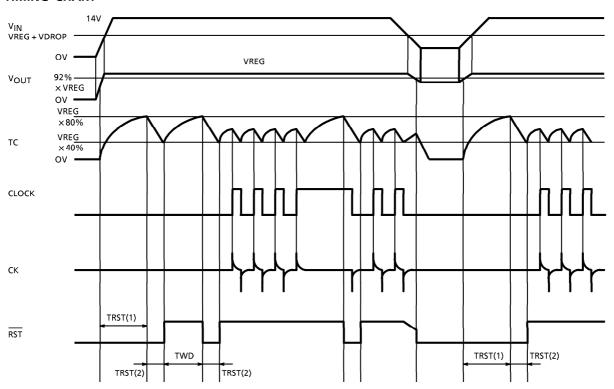


Weight: 0.14g (Typ.)

### PIN DESCRIPTION

PIN No.	SYMBOL	DESCRIPTION			
1	N.C	Not connected.			
2	GND	Grounded.			
3	N.C	Not connected.			
4	RESET	<ul> <li>Watchdog timer reset pin.</li> <li>Generates a reset signal which is determined by the CR combination of the TC pin.</li> <li>Intermittently generates reset pulses if no clock is supplied to the CK pin.</li> <li>The RESET signal is the output from the collector of an NPN transistor with a pull-up resistor.</li> </ul>			
5	СК	Clock input pin for the watchdog timer. If it is used for a Power-on reset timer only, it is pulled up to $\overline{\text{RESET}}$ pin.			
6	N.C	Not connected.			
7	тс	Pin for setting a time for the reset timer and watchdog timer. It connects to a resistor $R_T$ which leads to $V_{CC}$ and a capacitor $C_T$ which is grounded.			
8	N.C	Not connected.			
9	Vout	Output pin for 5V constant-voltage power supply. It has a current capacity of up to 50mA (Max.).  This pin is also a power supply pin for the reset timer.			
10, 11	N.C	Not connected.			
12	ST	Standby mode setup pin. When the signal is low, the system is in standby mode in which the reset timer is off and the power current is limited to 0.4mA or less. When the signal is high, the system is in active mode in which the power supply and reset timer are active.			
13, 14	N.C	Not connected.			
15	V <sub>IN</sub>	Power supply input pin.			
16	N.C	Not connected.			

### **TIMING CHART**



### **ABSOLUTE MAXIMUM RATINGS** (Ta = 25°C)

CHARACTERISTIC	SYMBOL	PIN	RATING	UNIT	
Supply Voltage	V	Vice	30 (DC)	V	
Supply Voltage	V <sub>IN</sub>	V <sub>IN</sub>	60 (<1s)	] <b>'</b>	
Input Voltage	V/15.1	CK	<b>-</b> 5∼5	V	
Imput voitage	V <sub>IN</sub>	RESET, STBY, TC	-0.3~5	]	
Input Current	IIN	CK, STBY, TC	<b>-</b> 5∼5	mA	
Output Current	10	Vout	50	mA	
Output Current	OUT	RESET	2	""A	
Output Voltage	Vout	V <sub>OUT</sub> , RESET	16	٧	
Operation Temperature	TOPR		<b>- 40∼105</b>	°C	
Junction Temperature	Tj		<b>-40∼150</b>	°C	
Storage Temperature	T <sub>stg</sub>		<b>-40∼150</b>	°C	
Lead Temperature Time	T <sub>sol</sub>		260 (1s)	°C	

### THERMAL DATA

CHARACTERISTIC	SYMBOL	RATING	UNIT
Power Dissipation	PD	0.6	W
Thermal Resistance Junction-ambient	$R_{ heta J-A}$	200	°C/W
Thermal Shutdown Junction-Temperature	T <sub>JSD</sub>	150	°C

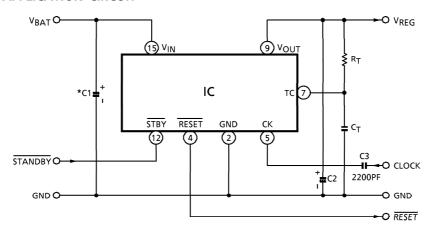
### **AC CHARACTERISTICS**

CHARACTERISTIC	SYMBOL	PIN	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Watchdog Timer	TWD	RESET		0.9 × C <sub>T</sub> ·R <sub>T</sub>	1.1 x C <sub>T</sub> ·R <sub>T</sub>	1.3 x C <sub>T</sub> ·R <sub>T</sub>	
	T <sub>RST</sub> (1)			1.3 x C <sub>T</sub> ·R <sub>T</sub>	1.6 x C <sub>T</sub> ·R <sub>T</sub>	1.9 × C <sub>T</sub> ·R <sub>T</sub>	
	T <sub>RST</sub> (2)			300 x C <sub>T</sub>	700 × C <sub>T</sub>	1500 × C <sub>T</sub>	
Clock Pulse Width	TW	CK		3	_	_	μs
Transfer Delay Time	TDHL	RESET	C1 = $0\mu$ F RESET "High" to "Low"	_	3	10	μs

# ELECTRICAL CHARACTERISTICS $V_{IN} = 6 \sim 18V$ , $T_{a} = -40 \sim 105^{\circ}C$ , $I_{LOAD} = 10 \text{mA}$ unless otherwise specified

	/ 41111	000 0 01101	wise specified	/				
CHARACTERISTIC	SYMBOL	PIN	TEST CONDITION	MIN.	TYP.	MAX.	UNIT	
Output Voltage	V <sub>REG</sub>	VOUT	I <sub>LOAD</sub> = 1~50mA Ta = -40~85°C	4.85	5	5.15	V	
Line Regulation	V <sub>LINE</sub>	Vout	V <sub>IN</sub> = 6~25V	_	0.1	0.5	%	
Load Regulation	V <sub>LOAD</sub>	VOUT	Ta = 25°C, I <sub>LOAD</sub> = 1~50mA	_	0.1	0.5	%	
Temperature Coefficient	$\Delta V_{REG}$ / $\Delta_{OUT}$	VOUT	_	_	0.01	_	% /°C	
Dropout Voltage	V <sub>DROP</sub>	V <sub>OUT</sub>	I <sub>LOAD</sub> = 50mA Ta = 25°C	_	0.85	1.0	٧	
Current Limiter Detection	ILIMIT	V <sub>OUT</sub>	Ta = 25°C	_	100	_	mA	
Reset Detect Voltage	V <sub>TH</sub>	VOUT	_	V <sub>REG</sub> × 89%	V <sub>REG</sub> × 92%	V <sub>REG</sub> × 95%	V	
Output Voltage	VOL	RESET	I <sub>OL</sub> = 1mA		0.3	0.5	\ \	
Output Leakage Current	ILEAK	RESET	V <sub>OUT</sub> = 5V	-5	0	5	μΑ	
Input Current	ΙΝ	TC	$V_{IN} = 0 \sim 3.5 V$	-3	0	3	μΑ	
Threshold Voltage	V <sub>TH-H</sub>	тс	RESET "High" to "Low"	_	V <sub>REG</sub> × 80%	_	\ \ \	
	V <sub>TH-L</sub>		RESET "Low" to "High"	_	V <sub>REG</sub> × 40%	_		
Input Current	IN	CK	V <sub>IN</sub> = 5V	_	0.3	0.7	mA	
Input Detect Voltage	V <sub>IH</sub>	CK		2			<sub>v</sub>	
	V <sub>IL</sub>	STBY		<u> </u>	_	0.5		
Input Current	IN	STBY	V <sub>IN</sub> = 5V	<u> </u>	40	80	$\mu$ A	
Standby Current	I <sub>ST</sub>	V <sub>IN</sub>	STBY = "L", V <sub>IN</sub> = 14V		0.18	0.4	mA	
Current Consumption	lcc	VIN	<u>STBY</u> = "H", V <sub>IN</sub> = 14V	_	0.9	1.8	mA	
Over-Voltage Detection	V <sub>SD</sub>	V <sub>IN</sub>		25	28	31	٧	

### **EXAMPLE OF APPLICATION CIRCUIT**



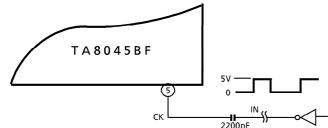
- \* Cautions for wiring
  - 1. C1 and C2 are for absorbing disturbance, noise, etc. Connect them as close to the IC as possible.
  - 2. Use capacitor C1 and C2 having less temperature coefficient (tantalum capacitor, etc).

### **RECOMENDED CONDITIONS**

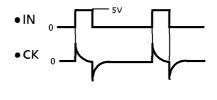
PART NAME	MIN.	MAX.	UNIT
C <sub>T</sub>	0.01	100	$\mu$ F
R <sub>T</sub>	5	100	kΩ

### **CK INPUT APPLICATION CIRCUIT**





**Timing Chart** 



The capacitor coupling allows reset pulses to be supplied intermittently from the  $\overline{\text{RESET}}$  pin whether the input level (IN) is high or low.

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# OUTLINE DRAWING SSOP16-P-225-1.00A Unit:mm 0.6TYP 8.7MAX 8.2±0.2 7.0±5-1.00A Unit:mm 0.525±0.2

Weight: 0.14g (Typ.)