

Preliminary

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

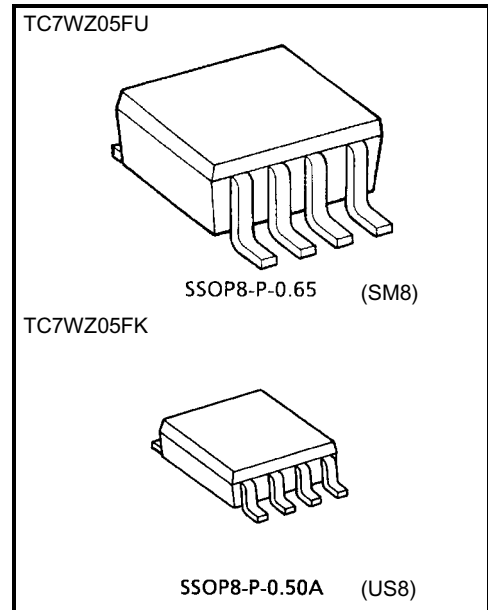
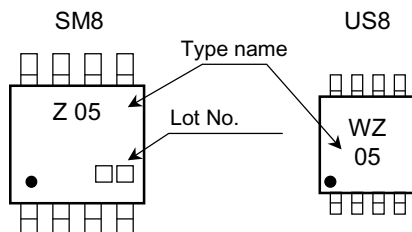
TC7WZ05FU, TC7WZ05FK

Triple Inverter (open drain)

Features

- High output drive: 24 mA (min) @V_{CC} = 3 V
- Super high speed operation: t_{pZL} 2.3 ns (typ.) @V_{CC} = 5 V, 50 pF
- Operation voltage range: V_{CC (opr)} = 1.65~5.5 V
- Latch-up performance: ±500 mA or more
- ESD performance: ±200 V or more (EIAJ)
±2000 V or more (MIL)
- Power down protection is provided on all inputs and outputs.
- Matches the performance of TC74LCX series when operated at 3.3 V V_{CC}.

Marking



Weight

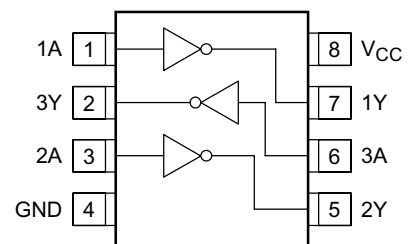
SSOP8-P-0.65 : 0.02 g (typ.)

SSOP8-P-0.50A : 0.01 g (typ.)

Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Power supply voltage	V _{CC}	-0.5~6	V
DC input voltage	V _{IN}	-0.5~6	V
DC output voltage	V _{OUT}	-0.5~6	V
Input diode current	I _{IK}	-20	mA
Output diode current	I _{OK}	-20	mA
DC output current	I _{OUT}	50	mA
DC V _{CC} /ground current	I _{CC}	±50	mA
Power dissipation	P _D	300 (SM8) 200 (US8)	mW
Storage temperature	T _{stg}	-65~150	°C
Lead temperature (10s)	T _L	260	°C

Pin Assignment (top view)



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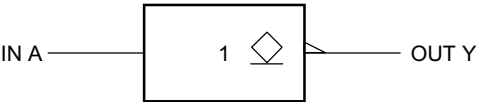
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Truth Table

A	Y
L	*H
H	L

*: High impedance

Logic Diagram



Recommended Operating Conditions

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	1.65~5.5	V
		1.5~5.5 (Note 1)	
Input voltage	V _{IN}	0~5.5	V
Output voltage	V _{OUT}	0~5.5 (Note 2)	V
		0~V _{CC} (Note 3)	
Operating temperature	T _{opr}	-40~85	°C
Input rise and fall time	d _t /d _v	0~20 (V _{CC} = 1.8 V ± 0.15 V, 2.5 V ± 0.2 V)	ns/V
		0~10 (V _{CC} = 3.3 V ± 0.3 V)	
		0~5 (V _{CC} = 5.5 V ± 0.5 V)	

Note 1: Data retention only

Note 2: V_{CC} = 0 V

Note 3: Low state

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Electrical Characteristics

DC Characteristics

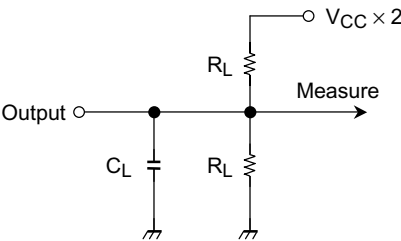
Characteristics		Symbol	Test Condition		Ta = 25°C				Ta = -40~85°C		Unit
					V _{CC} (V)	Min	Typ.	Max	Min	Max	
Input voltage	High level	V _{IH}	—		1.65~1.95	0.75 × V _{CC}	—	—	0.75 × V _{CC}	—	V
					2.3~5.5	0.7 × V _{CC}	—	—	0.7 × V _{CC}	—	
	Low level	V _{IL}	—		1.65~1.95	—	—	0.25 × V _{CC}	—	0.25 × V _{CC}	
					2.3~5.5	—	—	0.3 × V _{CC}	—	0.3 × V _{CC}	
Output voltage	Low level	V _{OL}	V _{IN} = V _{IH}	I _{OL} = 100 μA	1.65	—	0	0.1	—	0.1	V
					2.3	—	0	0.1	—	0.1	
					3.0	—	0	0.1		0.1	
					4.5	—	0	0.1	—	0.1	
				I _{OL} = 4 mA	1.65	—	0.08	0.24	—	0.24	
				I _{OL} = 8 mA	2.3	—	0.1	0.3		0.3	
				I _{OL} = 16 mA	3.0	—	0.15	0.4		0.4	
				I _{OL} = 24 mA	3.0	—	0.22	0.55		0.55	
				I _{OL} = 32 mA	4.5	—	0.22	0.55	—	0.55	
				Input leakage current		I _{IN}	V _{IN} = 5.5 V or GND		0~5.5	—	
Off-state current		I _{OZ}	V _{IN} = V _{IL} , V _{OUT} = V _{CC} or GND		5.5	—	—	±5	—	±10	μA
Power off leakage current		I _{OFF}	V _{IN} or V _{OUT} = 5.5 V		0.0	—	—	1	—	10	μA
Quiescent supply current		I _{CC}	V _{IN} = 5.5 V or GND		1.65~5.5	—	—	1	—	10	μA

AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3\text{ ns}$)

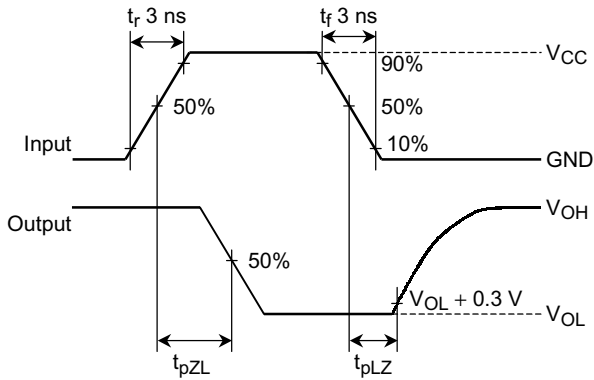
Characteristics	Symbol	Test Condition	Ta = 25°C				Ta = -40~85°C		Unit
			V _{CC} (V)	Min	Typ.	Max	Min	Max	
Propagation delay time	t _{pZL}	C _L = 50 pF, R _L = 500 Ω	1.8 ± 0.15	1.8	5.5	9.5	1.8	10.5	ns
			2.5 ± 0.2	1.2	3.7	5.8	1.2	6.4	
			3.3 ± 0.3	0.8	2.9	4.4	0.8	4.8	
			5.0 ± 0.5	0.5	2.3	3.5	0.5	3.9	
	t _{pLZ}	C _L = 50 pF, R _L = 500 Ω	1.8 ± 0.15	1.8	4.3	9.5	1.8	10.5	
			2.5 ± 0.2	1.2	2.8	5.8	1.2	6.4	
			3.3 ± 0.3	0.8	2.1	4.4	0.8	4.8	
			5.0 ± 0.5	0.5	1.4	3.5	0.5	3.9	
Input capacitance	C _{IN}	—	0~5.5	—	3.0	—	—	pF	
Output capacitance	C _{OUT}	—	0~5.5	—	3.0	—	—	pF	
Power dissipation capacitance	C _{PD}	(Note)	3.3	—	5.2	—	—	pF	
			5.5	—	8.5	—	—		

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.
 Average operating current can be obtained by the equation:
 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/3$

Test Circuit



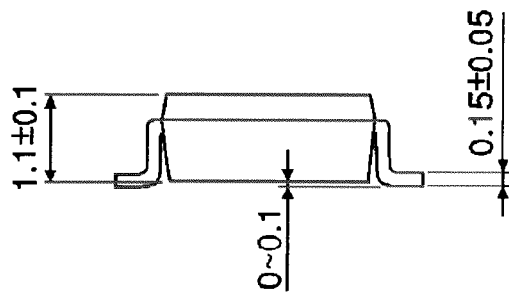
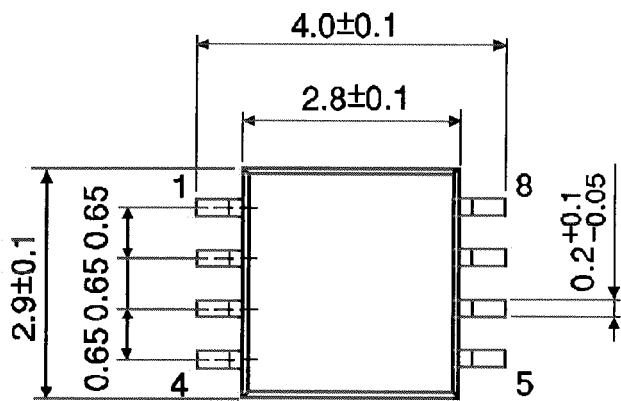
AC Waveform



Package Dimensions

SSOP8-P-0.65

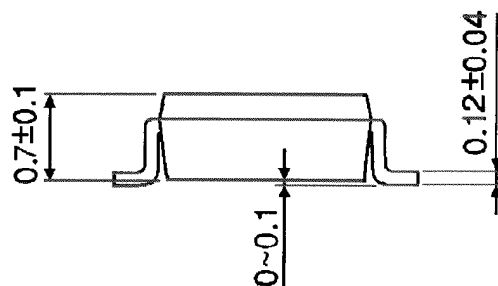
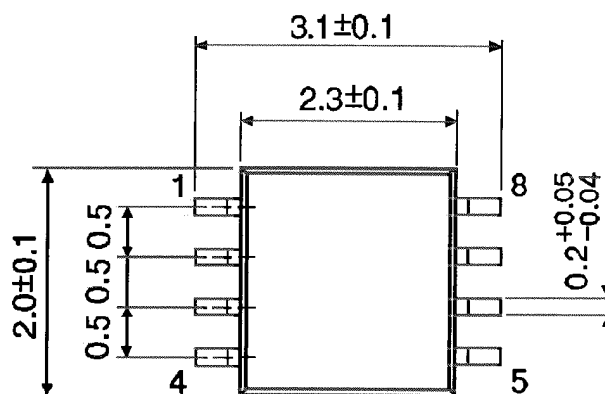
Unit : mm



Weight: 0.02 g (typ.)

SSOP8-P-0.50A

Unit : mm



Weight: 0.01 g (typ.)