

TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

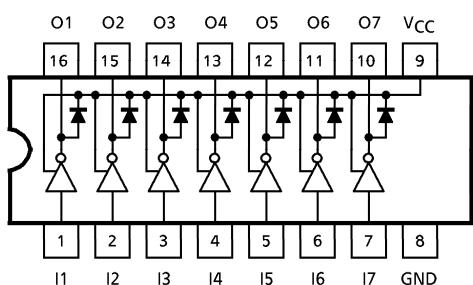
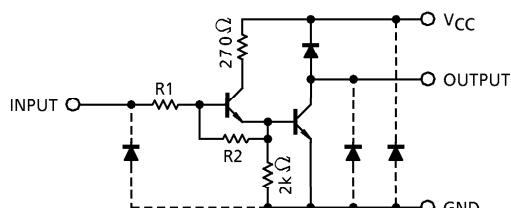
TD62301P, TD62301F, TD62302P, TD62302F**7CH LOW SATURATION SINK DRIVER**

The TD62301P/F and TD62302P/F are comprised of seven NPN low saturation drivers.

All units feature integral clamp diodes for switching inductive loads. Applications include relay, hammer, lamp and LED drive in low voltage system.

FEATURES

- Low saturation output $V_{CE}(\text{sat}) = 0.7V$ (Max.)
- Output rating (single output) 15V (Min.) / 200mA (Max.)
- High DC transfer ratio 1000 (Min.)
- Output clamp diodes
- Input register : TD62301P/F $R_1 = 2k\Omega$, $R_2 = 20k\Omega$
: TD62302P/F $R_1 = 8.4k\Omega$, $R_2 = 15k\Omega$
- Inputs compatible with TTL and 3~6V CMOS
- Package type-P : DIP-16 pin
- Package type-F : SOP-16 pin

PIN CONNECTION (TOP VIEW)**SCHEMATICS (EACH DRIVER)**

TD62301P : $R_1 = 2k\Omega$, $R_2 = 20k\Omega$
TD62302P : $R_1 = 8.4k\Omega$, $R_2 = 15k\Omega$

(Note) The input and output parasitic diodes cannot be used as clamp diodes.

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MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT
Supply Voltage		V _{CC}	- 0.5 ~ 15	V
Output Sustaining Voltage		V _{CE} (SUS)	- 0.5 ~ V _{CC} + 0.5	V
Output Current		I _{OUT}	200	mA / ch
Input Voltage		V _{IN}	- 0.5 ~ 15	V
Input Current		I _{IN}	15	mA
Clamp Diode Reverse Voltage		V _R	15	V
Clamp Diode Forward Current		I _F	200	mA
Power Dissipation	P	P _D	1.0	W
	F		0.625 (Note)	
Operating Temperature	P	T _{opr}	- 30 ~ 75	°C
	F		- 40 ~ 85	
Storage Temperature		T _{stg}	- 55 ~ 150	°C

(Note) On Glass Epoxy PCB (30 × 30 × 1.6mm Cu 50%)

RECOMMENDED OPERATING CONDITIONS (Ta = - 40 ~ 85°C and Ta = - 30 ~ 75°C for Type-P)

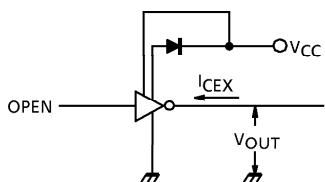
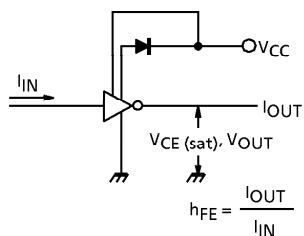
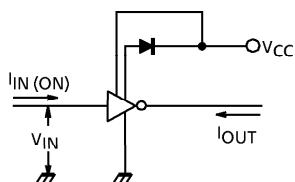
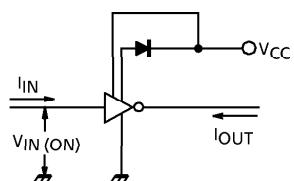
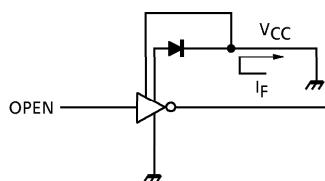
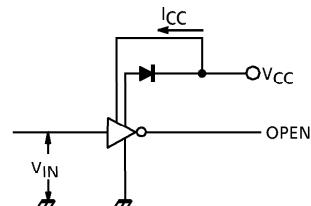
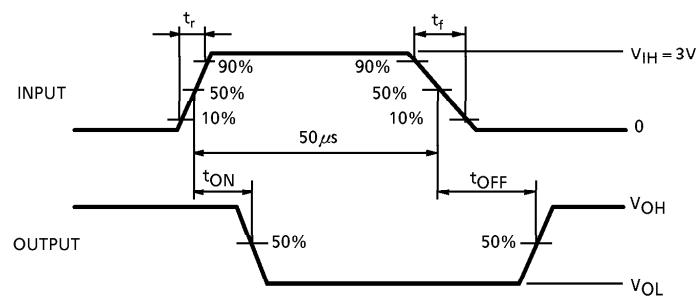
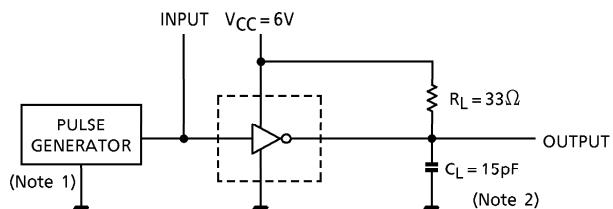
CHARACTERISTIC		SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	
Supply Voltage		V _{CC}		3	—	6	V	
Output Current	P	I _{OUT}	DC 1 Circuit	0	—	180	mA	
			T _{pw} = 25ms, Duty = 50%, 4 Circuits	0	—	150		
Input Voltage		V _{IN}		—	—	V _{CC}	V	
Clamp Diode Reverse Voltage		V _R		—	—	V _{CC}	V	
Clamp Diode Forward Current		I _F		—	—	180	mA	
Power Dissipation	P	P _D		—	—	0.44	W	
	F		(Note)	—	—	0.325		

(Note) On Glass Epoxy PCB (30 × 30 × 1.6mm Cu 50%)

ELECTRICAL CHARACTERISTICS ($T_a = 25^\circ\text{C}$)

CHARACTERISTIC		SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Leakage Current	I_{CEX}	1	$V_{CC} = 6V, V_{OUT} = 6V$ $T_a = 50^\circ\text{C}$	—	—	7	μA	
			$V_{CC} = 6V, V_{OUT} = 6V$ $T_a = 25^\circ\text{C}$	—	—	1		
Output Saturation Voltage	V_{CE} (sat)	2	$V_{CC} = 5V, I_{IN} = 0.14\text{mA}$ $I_{OUT} = 70\text{mA}$	—	—	0.5	V	
			$V_{CC} = 5V, I_{IN} = 0.3\text{mA}$ $I_{OUT} = 150\text{mA}$	—	—	0.7		
DC Current Transfer Ratio	h_{FE}	2	$V_{CC} = 5V, V_{OUT} = 2V$ $I_{OUT} = 120\text{mA}$	1000	2000	—	—	
Input Current On	TD62301P / F TD62302P / F	I_{IN} (ON)	$V_{CC} = 5V, V_{IN} = 2.4V$ $I_{OUT} = 120\text{mA}$	—	—	0.60	mA	
Input Voltage On	TD62301P / F TD62302P / F		$V_{CC} = 5V, I_{IN} = 0.2\text{mA}$ $I_{OUT} = 120\text{mA}$	—	—	0.14		
Clamp Diode Forward Voltage	V_F	5	$I_F = 120\text{mA}$	—	—	2.0	V	
Supply Current	I_{CC}	6	$I_F = 120\text{mA}$	—	15	22	mA / Gate	
Input Capacitance	C_{IN}	—	$V_{IN} = 0, f = 1\text{MHz}$	—	15	—	pF	
Turn-On Delay	t_{ON}	7	$V_{CC} = 6V, R_L = 33\Omega$ $C_L = 15\text{pF}$	—	0.1	—	μs	
Turn-Off Delay	t_{OFF}			—	0.2	—	μs	

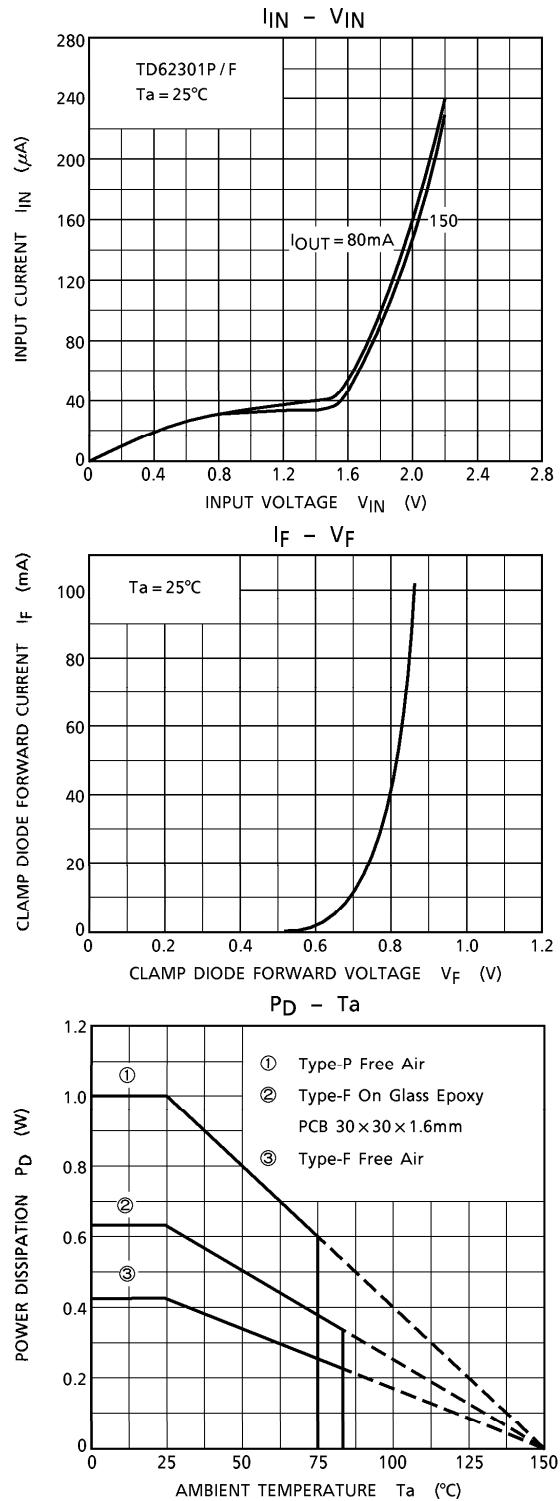
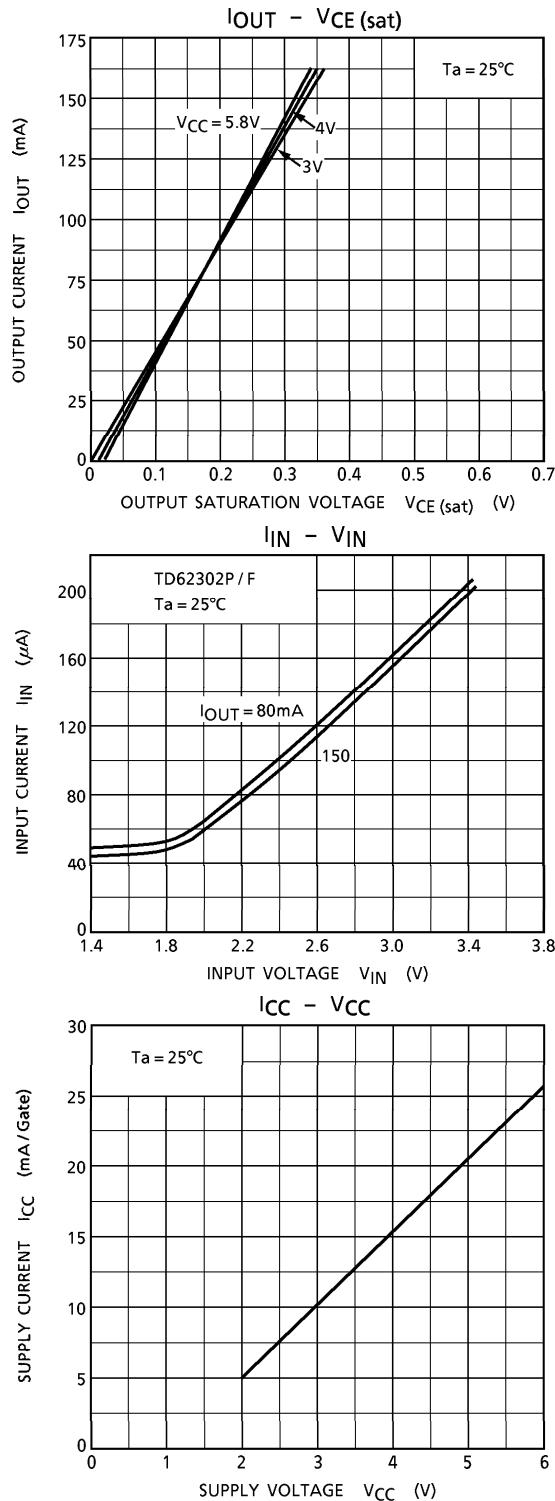
TEST CIRCUIT

1. I_{CEX} 2. h_{FE} , $V_{CE}(\text{sat})$ 3. $I_{IN}(\text{ON})$ 4. $V_{IN}(\text{ON})$ 5. V_F 6. I_{CC} 7. t_{ON} , t_{OFF} 

(Note 1) Pulse Width 50μs
Duty Cycle 10%
Output Impedance 50Ω
 $t_r \leq 5\text{ns}$, $t_f \leq 10\text{ns}$
(Note 2) C_L includes probe and jig capacitance.

PRECAUTIONS for USING

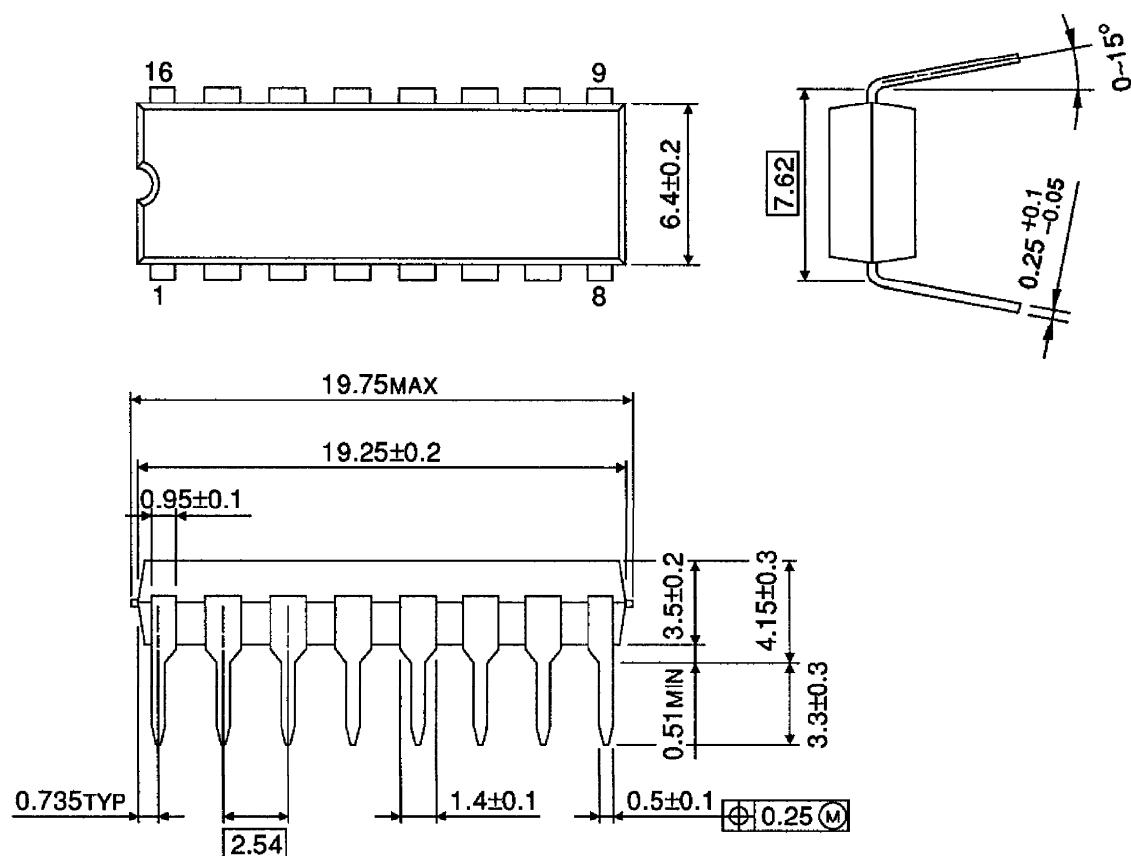
Utmost care is necessary in the design of the output line, V_{CC} and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.



OUTLINE DRAWING

DIP16-P-300-2.54A

Unit : mm

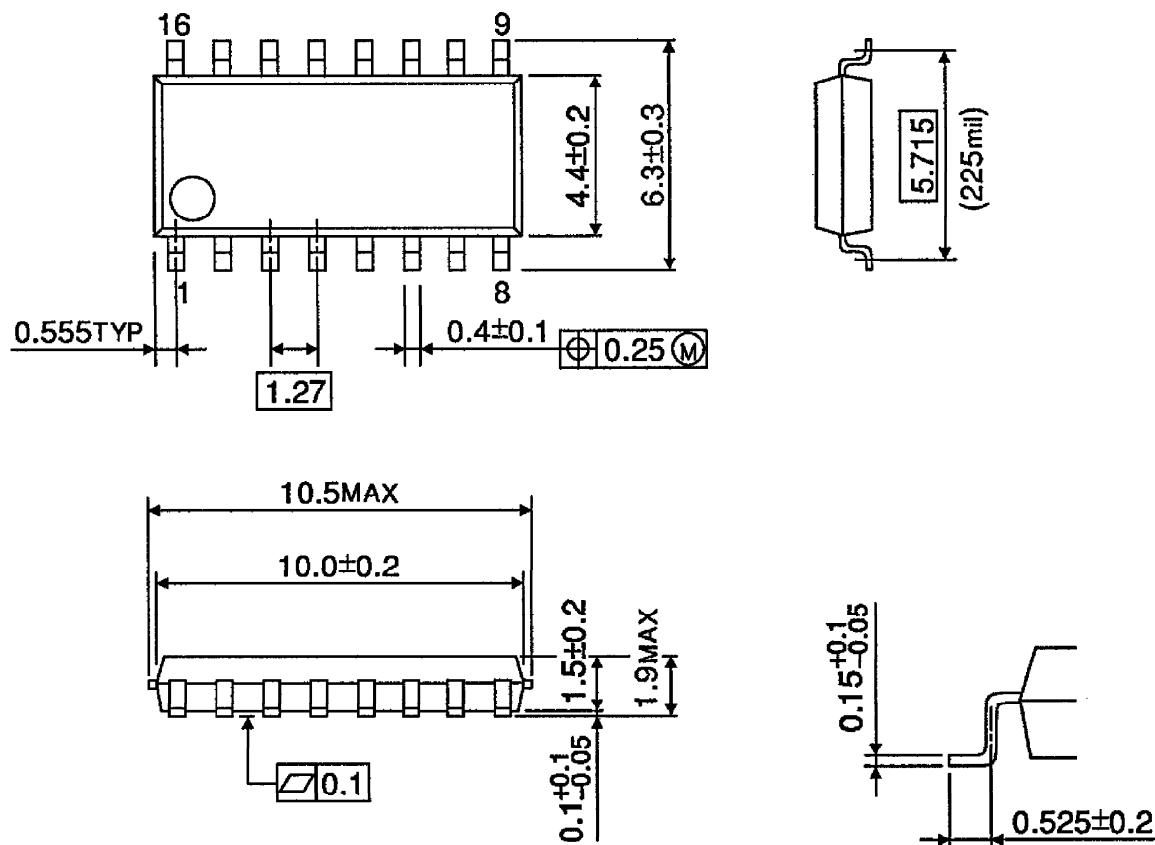


Weight : 1.11g (Typ.)

OUTLINE DRAWING

SOP16-P-225-1.27

Unit : mm



Weight : 0.16g (Typ.)