TOSHIBA

Low Power Consumption Photo-IC

TPS842

Outline

The TPS842 is a digital-output Si photo-IC which incorporates a photodiode, an amplifier circuit and a schmitt trigger circuit in a single chip. The TPS842 is a low-voltage drive device which has been designed for 3.3-V systems. The device allows construction of low-voltage systems which thus consume less power. In addition, the sensitivity of the device is superior to that of a phototransistor.

Features



Compact side-view epoxy resin package (black package impermeable to visible light)



Digital-output Si photo-IC which incorporates a photodiode, amplifier circuit and schmitt trigger circuit in a single chip



Low-voltage drive: operating power supply voltage: Vcc = 2.7 V ~ 7 V



High-speed response: t_{pLH} = 15 μs



Threshold radiant incidence: EHL= 0.3 mW/cm² (max) @Ta = 25°C



Digital output: low-level output when optical input signal is received (open-collector)



Direct connection to TTL and CMOS devices possible



Maximum Rating (Ta = 25°C)

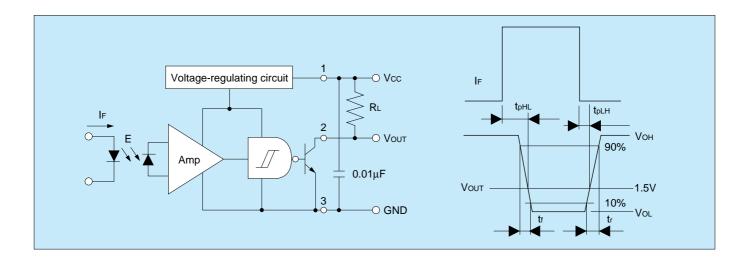
Characteristic	Symbol	Rating	Unit
Supply Voltage	Vcc	7	V
Output Voltage	Vo	15	V
Low-Level Output Current(Ta=Topr)	lou	8	mA
Operating Temperature	Topr	-25 ~ 85	°C
Storage Temperature	Tstg	-40 ~ 100	°C
Soldering Temperature(5s) (Note1)	Tsol	260	°C

Note 1: Soldering is carried out 1.3 mm from the resin which comprises the underside of the package.

Optical and Electrical Characteristics (unless otherwise specified: Ta = 25° ~ 85°C and Vcc = 2.7 V ~ 7 V)

Characte	eristic	Symbol	Test Condition	Min	Тур.	Max	Unit	
Supply Voltage		Vcc	_	2.7	-	7	V	
High-Level Supply Ccurrent		Іссн	E=0	-	0.01	1.2	mA	
Low-Lev	rel Supply Current	Iccl	E=2mW/cm ^{2(Note2)}	-	_	2.0	mA	
High-Lev	vel Output Current	Іон	E=0,Vo=15V	-	-	6.3	μΑ	
Low-Lev	rel Output Voltage	VoL	E=2mW/cm ^{2(Note2)} Iон=8mA	-	-	0.4	V	
Threshold Radiant Incident(H→L)		Ень	Ta=25°C	-	0.1	0.3	mW/cm ²	
			_	-	-	0.6	mvv/cm-	
Hysteres	sis	EHL/ELH	Ta=25°C	1.1	1.5	2	_	
Peak Wa	avelength	λρ	_	-	900	-	nm	
Switching Time	Propagation delay time(L→H)	t pLH	Ta=25°C	-	-	15		
	Propagation delay time(H→L)	t pHL	Vcc=3.3V E=2mW/cm ^{2(Note2)} RL=10k Ω ^(Note3)	_	_	9	μs	
	Rise time	tr		_	0.8	-		
	Fall time	t f		_	0.05	_		

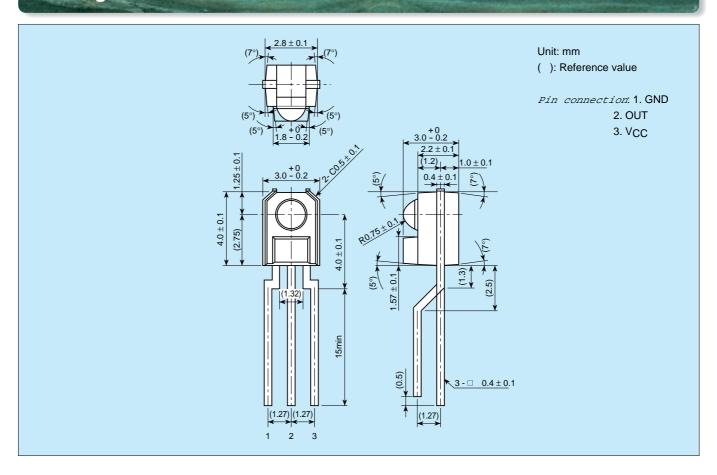
Note2: The light used is a CIE standard A light source (standard tungsten bulb and color temperature = 2870 °K) Note3: Test conditions when measuring rise and fall time



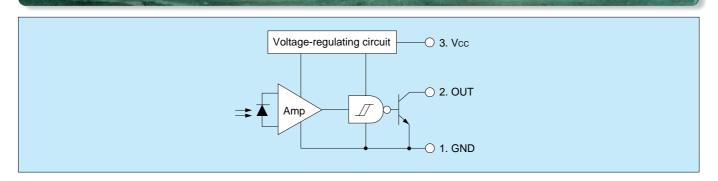
Recommended Operating Condition

Characteristics	Symbol	Min	Тур.	Max.	Unit
Supply Voltage	Vcc	2.7	_	5.5	V
Output Voltage	Vo	_	_	13.2	V
Low-Level Output Current	loL	_	_	8	mA
Operating Temperature	Topr	0	-	70	°C

Package Dimensions



Internal Circuit Diagram



Applications

- Photo-electric switches
- Office equipment: (copying machines, printers and fax machines)
 Drum rotation detection, paper detection and paper feed detection
- Vending machines, ticket machines and ATMs
- Handheld terminals



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