ADVANCE INFORMATION

SaRonix

Voltage Controlled Crystal Oscillator

3.3V, PECL

Technical Data

S1568 Series

ACTUAL SIZE

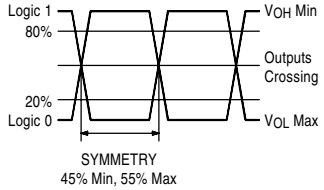
Description

A 3.3 Volt voltage controlled PECL crystal oscillator designed primarily for use in phase locked loops, Sonet, ATM, SDH and network/switch applications. Output is Motorola 10KH compatible. Device is packaged in a small, low profile 6-pin ceramic SMD package.

Applications & Features

- Positive supply voltage 3.3V ECL (PECL)
- Output enable/disable feature
- Wide frequency range from 77.76 MHz to 155.52 MHz using SaRonix fundamental crystals for exceptional jitter performance
- Covers a wide range of telecommunication applications such as Sonet, ATM and SDH
- ±50ppm minimum APR*

Output Waveforms



Frequency Range:	77.76 MHz to 155.52 MHz
Frequency Stability:	±20, ±25, or ±50ppm over all conditions: operating temperature, supply voltage change, load change, calibration tolerance, aging (5 years @ +40°C average ambient operating temp range), shock and vibration.
Temperature Range:	
Operating:	0 to +70°C or -40 to +85°C
Storage:	-55°C to +105°C
Supply Voltage:	3.3V ±10%
Supply Current:	65mA typ, 100mA max
Output Drive:	
Symmetry:	45/55% max @ 50% waveform
Rise & Fall Times:	550ps max @ 20 to 80% waveform
Logic 0:	V_{CC} -1.620 max
Logic 1:	V_{CC} -1.025 min
Load:	50Ω to V_{CC} -2V (output requires termination)
Period Jitter RMS:	3.5ps max
Pull Characteristics:	
Input Impedence (Pin 1):	50KΩ min
Frequency Response (-3dB):	20kHz min
Pullability:	±50ppm min APR*
Control Voltage:	0.3V to 3.0V
Transfer Function:	Frequency increases when control voltage increases
Monotonic Linearity:	10%
Center Control Voltage:	1.65V
Mechanical:	
Shock:	MIL-STD-883, Method 213, Condition F
Solderability:	MIL-STD-883, Method 2003
Terminal Strength:	MIL-STD-883, Method 2004, Conditions B2
Vibration:	MIL-STD-883, Method 2007, Condition A
Solvent Resistance:	MIL-STD-202, Method 215
Resistance to Soldering Heat:	MIL-STD-202, Method 210, Condition I or J
Environmental:	
Gross Leak Test:	MIL-STD-883, Method 1014, Condition C
Fine Leak Test:	MIL-STD-883, Method 1014, Condition A2
Thermal Shock:	MIL-STD-883, Method 1011, Condition A
Moisture Resistance:	MIL-STD-883, Method 1004

*APR = (VCXO Pull relative to specified Output Frequency) – (VCXO Freq. Stability)

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www.saronix.com

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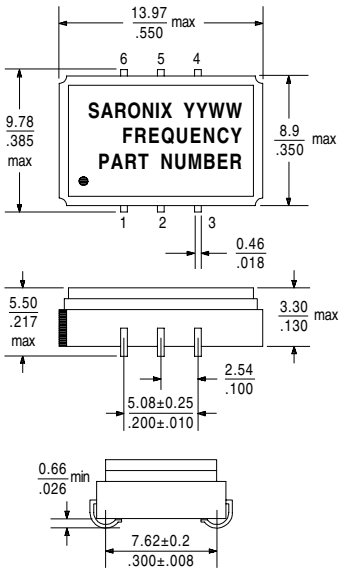
Package Details

Marking Format **
Includes Date Code, Frequency & Model



Pin Functions:

Pin 1: Control Voltage	Pin 4: Q Output
Pin 2: Enable	Pin 5: $\overline{Q}$ Output
Pin 3: GND / Case	Pin 6: Supply Voltage

Scale: None (Dimensions in $\frac{mm}{inches}$)

**Exact location of items may vary

Part Numbering Guide

S 1568 B A B - 80.0000 (T)

SaRonix

Model
3.3V PECL VCXO, Ceramic SMD
Complementary Output with Enable

Stability Tolerance
AA = ±20ppm, 0 to +70°C
A = ±25ppm, 0 to +70°C
B = ±50 ppm, 0 to +70°C
E = ±50 ppm, -40 to +85°C

Pullability (minimum APR)**
A = ±50 ppm

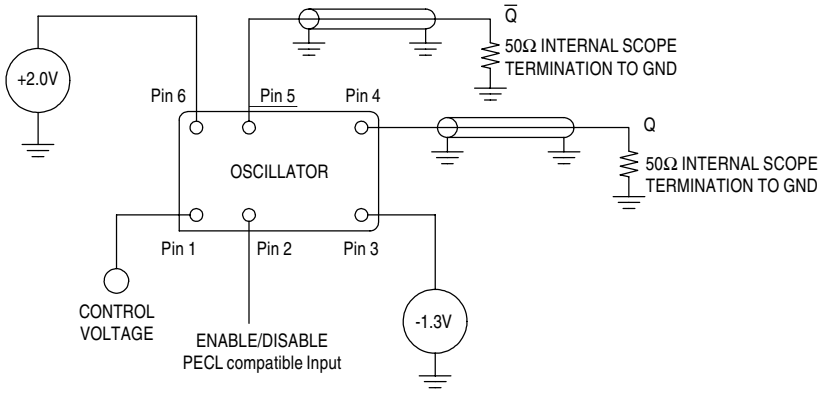
Linearity
B = 10%

Frequency (MHz)

(T) = Tape & Reel
full reel increments only

**APR = (VCXO Pull relative to specified Output Frequency) – (VCXO Freq. Stability)

Test Circuit



All specifications are subject to change without notice.

DS-202 REV A

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