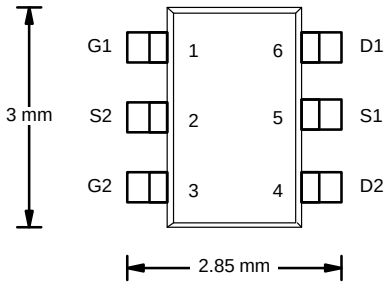
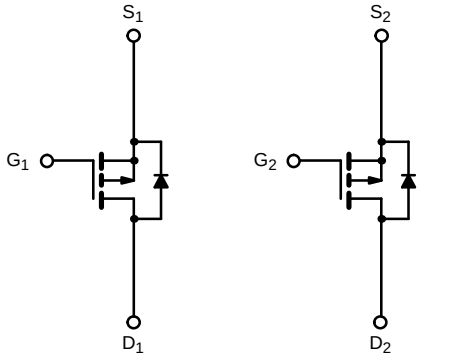
Dual P-Channel 20-V (D-S) MOSFET

PRODUCT SUMMARY		
V_{DS} (V)	$r_{DS(on)}$ (Ω)	I_D (A)
–20	0.200 @ V_{GS} = –4.5 V	±1.8
	0.235 @ V_{GS} = –3.6 V	±1.6
	0.340 @ V_{GS} = –2.5 V	±1.3

TrenchFET® Power MOSFETs
2.5-V Rated

TSOP-6
Top View





P-Channel MOSFET P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T_A = 25°C UNLESS OTHERWISE NOTED)				
Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V_{DS}	–20	V
Gate-Source Voltage		V_{GS}	±12	
Continuous Drain Current (T_J = 150°C)[a, b]	T_A = 25°C	I_D	±1.8	A
	T_A = 70°C		±1.2	
Pulsed Drain Current		I_{DM}	±7	
Continuous Diode Current (Diode Conduction)[a, b]		I_S	–1.05	
Maximum Power Dissipation[a, b]	T_A = 25°C	P_D	1.15	W
	T_A = 70°C		0.73	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	–55 to 150	°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient[a]	t ≤ 5 sec	R_{thJA}	93	110	°C/W
	Steady State		130	150	
Maximum Junction-to-Lead	Steady State	R_{thJL}	75	90	

Notes
a. Surface Mounted on FR4 Board.
b. t ≤ 5 sec

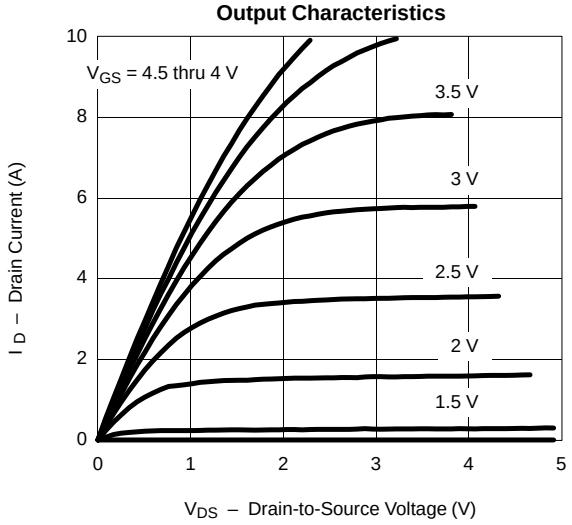
SPECIFICATIONS (T_J = 25°C UNLESS OTHERWISE NOTED)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static						
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = –250 μA	–0.5			V
Gate-Body Leakage	I_{GSS}	V_{DS} = 0 V, V_{GS} = ±12 V			±100	nA
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = –16 V, V_{GS} = 0 V			–1	μA
		V_{DS} = –16 V, V_{GS} = 0 V, T_J = 55°C			–5	
On-State Drain Current[a]	$I_{D(on)}$	V_{DS} ≤ –5 V, V_{GS} = –4.5 V	–5			A
Drain-Source On-State Resistance[a]	$r_{DS(on)}$	V_{GS} = –4.5 V, I_D = –1.8 A		0.160	0.200	Ω
		V_{GS} = –3.6 V, I_D = –1.6 A		0.190	0.235	
		V_{GS} = –2.5 V, I_D = –1 A		0.280	0.340	
Forward Transconductance[a]	g_{fs}	V_{DS} = –10 V, I_D = –1.8 A		3.6		S
Diode Forward Voltage[a]	V_{SD}	I_S = –1.05 A, V_{GS} = 0 V		–0.83	–1.1	V
Dynamic[b]						
Total Gate Charge	Q_g	V_{DS} = –10 V, V_{GS} = –4.5 V, I_D = –1.8 A		2.7	4.0	nC
Gate-Source Charge	Q_{gs}			0.4		
Gate-Drain Charge	Q_{gd}			0.6		
Turn-On Delay Time	$t_{d(on)}$	V_{DD} = –10 V, R_L = 10 Ω I_D ≅ –1 A, V_{GEN} = –4.5 V, R_G = 6 Ω		11	17	ns
Rise Time	t_r			34	50	
Turn-Off Delay Time	$t_{d(off)}$			19	30	
Fall Time	t_f			24	36	
Source-Drain Reverse Recovery Time	t_{rr}	I_F = –1.05 A, di/dt = 100 A/μs		20	40	ns

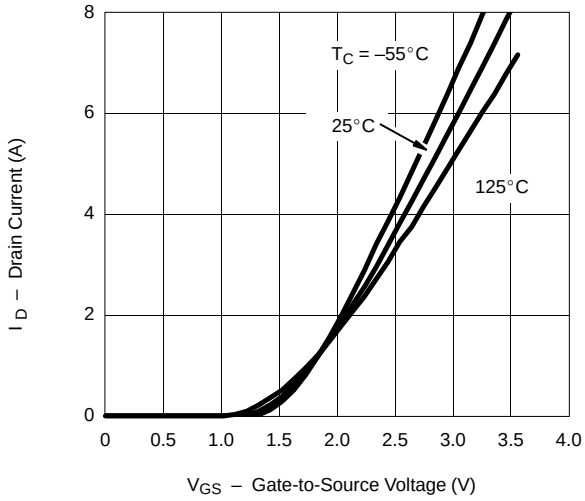
Notes
a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.
b. Guaranteed by design, not subject to production testing.

TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

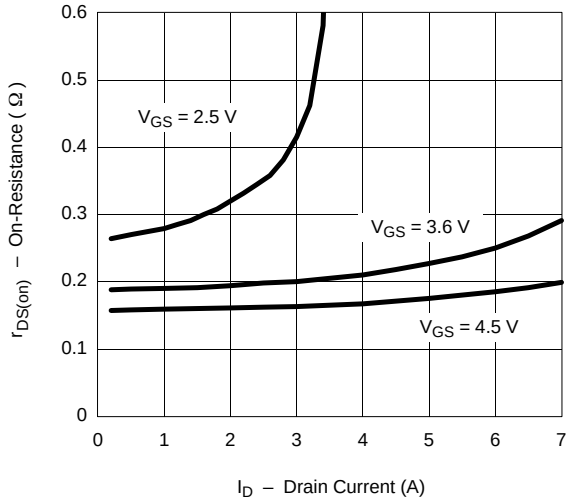
Output Characteristics



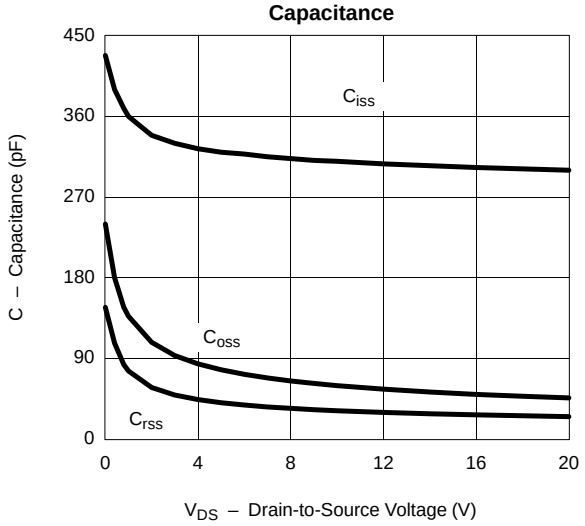
Transfer Characteristics



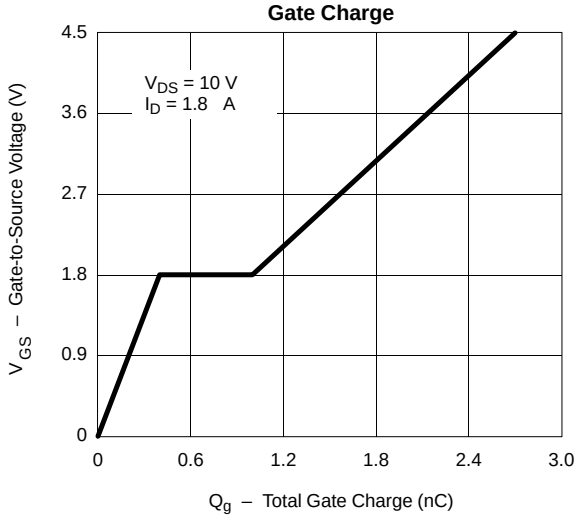
On-Resistance vs. Drain Current



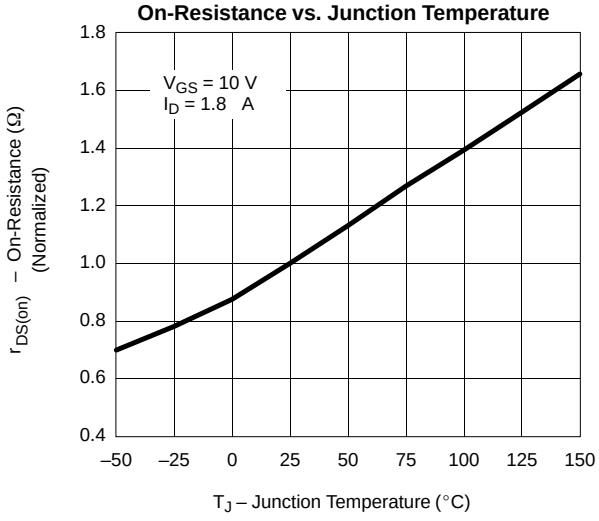
Capacitance



Gate Charge

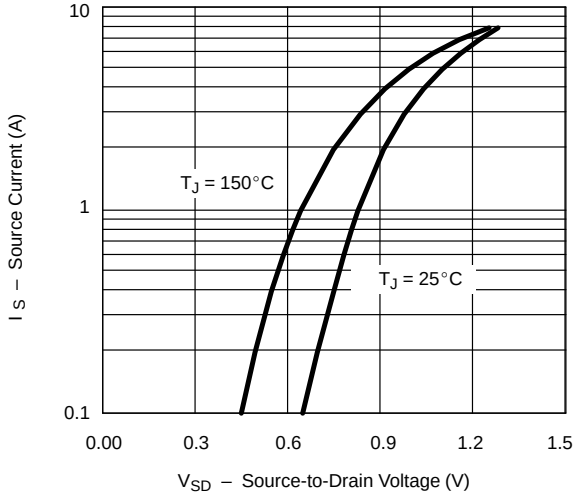


On-Resistance vs. Junction Temperature

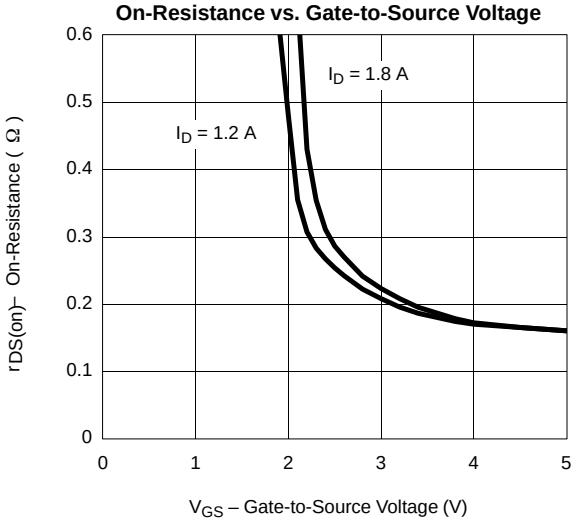


TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

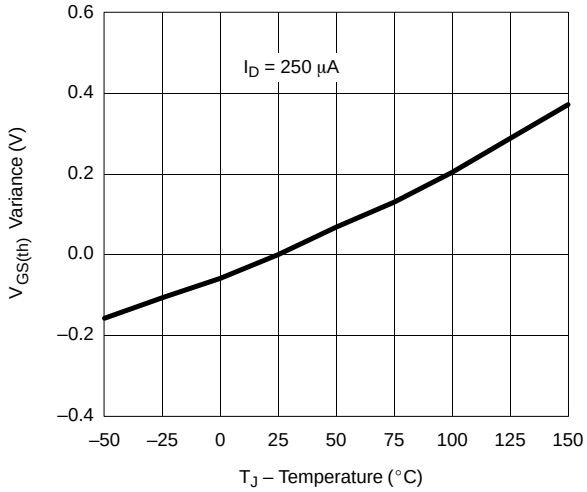
Source-Drain Diode Forward Voltage



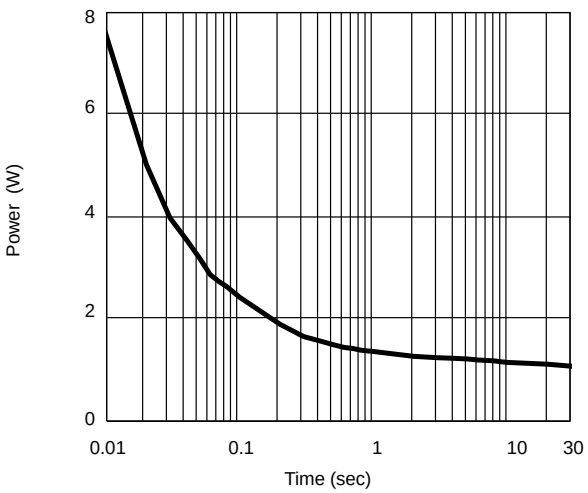
On-Resistance vs. Gate-to-Source Voltage



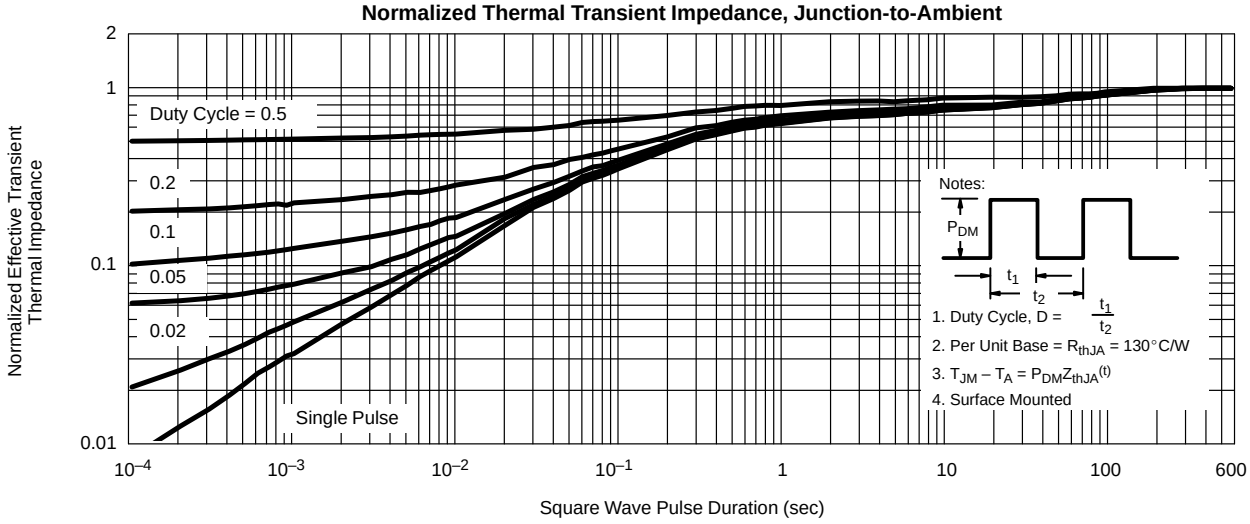
Threshold Voltage



Single Pulse Power, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Ambient



TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)