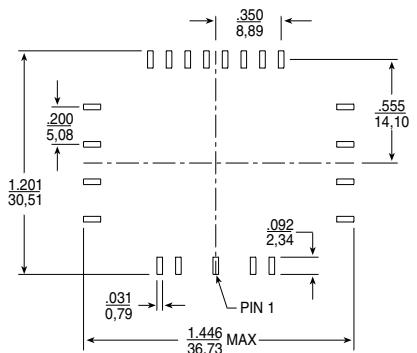
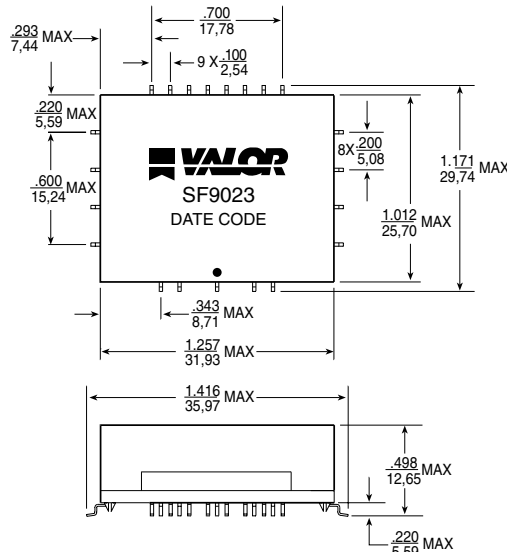


SF9023 — Electrical Specifications @ 25°C

Pin diagram of the AD8339 integrated circuit. The chip is a square package with pins numbered 1 through 21. Pin 1 is labeled 'RF IN'. Pins 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20, and 21 are all connected to ground (GND). The diagram shows the chip with pins 1 through 21, with labels for each pin and its connection to GND or RF IN.

SF9023



SUGGESTED PAD LAYOUT

Weight8.5 grams

Tray56/tray

Dimensions: Inches

mm
Unless otherwise specified, all tolerances are $\pm \frac{.010}{0.25}$

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