# **SiberCAM**

# Ultra-18M



#### Key Features

- 18,874,368 bits true ternary Content Addressable Memory (CAM) storage
- Supports entry widths of 36, 72,144, 288, 360, 432 and 576-bits
- 3-port interface for independent search and table management (2-port interface option)
- Up to 100 million look-ups per second throughput
- Glueless cascade support for up to 16 devices allows scaling up to two million IP packet classification entries
- Compatible with all SiberCAM family members

## **SCT1842**

Able to support up to 256k 72-bit entries, the SCT1842 Packet Forwarding Engine (PFE) is targeted at applications requiring large look-up tables. Based on ternary Content Addressable Memory (CAM) technology, the SCT1842 is fully backward compatible with the other members of the SiberCAM family (SCT2000, SCT9000, SCT9020 & SCT9022).

SiberCore's proprietary advanced ternary CAM architecture allows the SCT1842 to improve the speed and efficiency of table look-up functions for classification and routing in networking equipment with performance of up to 100 million multi-layer protocol look-ups per second. The SCT1842 is capable of maintaining wire speed while performing two look-ups at OC-192 (10 Gbit/s) or eleven lookups at OC-48 (2.5 Gbits/s) per minimum sized packet.

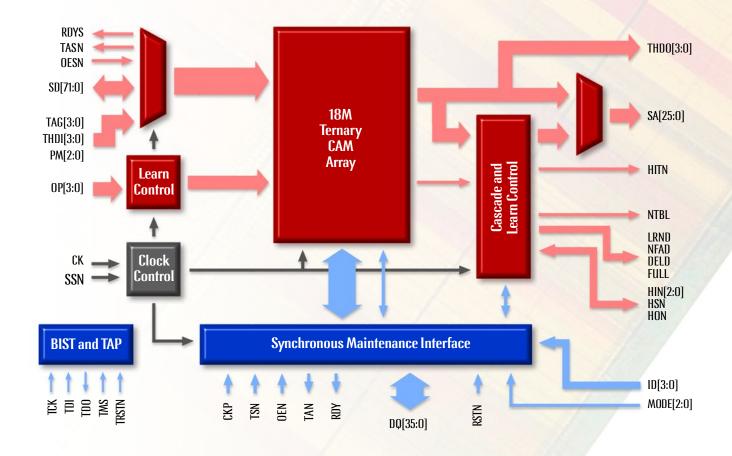
The SCT1842 supports 72 and 144-bit look-ups at full speed while 36 and 288 bit look-ups are supported at up to 50 Msps. Entry widths of 360 bits (33 Msps), 432 bits (33 Msps) and 576 bits (25 Msps) address the emerging requirements driven by IPv6 classification. Up to 16 SCT1842 devices can be cascaded to support over 2 million 144-bit classification entries. Cascading the SCT1842 to create larger tables does not introduce any search throughput performance penalty.

The SCT1842 is the only CAM-based PFE to support fully configurable multi-layer forwarding through a unique memory architecture that accommodates varying entry widths. Entries from 72 to 576 bits can be dynamically and individually configured, enabling vendors to support both IPv4 and IPv6 classification in a single chip.

Offering the lowest power consumption in its class, the SCT1842 also supports a complex power management feature based on a software programmable, patent pending block select technique. Power management is deterministic in nature, not statistical, thus guaranteeing lower power for worst-case scenarios. If the power management features are fully utilized, a cascade of four SCT1842s can be configured to consume up to an order of magnitude less power than competing solutions.

As with other SiberCAM devices, the SCT1842 provides a unique third-port that uses a non-intrusive interleaving technique allowing table updates to proceed without interrupting or suspending the search path. For applications where pin count is paramount, a pin efficient 2-port mode is also available.





#### Part Number

· SCT1842

#### **Features**

- · 18,874,368 bits of ternary CAM storage
- · Support for 36-bit, 72-bit, 144-bit, 288-bit, 360-bit, 432-bit and 576-bit entries
- Dynamic variable width architecture allows entry width to be defined on an entry by entry basis
- · High-speed synchronous search port 18-bit, 36-bit and 72-bit bus support
- · Search input can be configured for Single Data Rate (SDR) or Double Data Rate (DDR) signaling
- · 100 Million look-ups per second sustained search throughput
- · Independent maintenance port for non-intrusive table update operations
- · 16 programmable global search masks
- · 4 programmable entry mask registers
- · Automatic learning entry learned, next free address, entry deleted and full flags
- · Aging entries can be tagged as invalid, valid and has been hit, valid and has not been hit
- Flexible 4-bit programmable thread pipeline for search context identification and glueless connection to context memories
- · Glueless cascade support for up to 16 chips
- · Simple synchronous maintenance port 36-bit and 18-bit bus support
- · Built-in burst move operation
- · IEEE 1149.1 JTAG TAP with BIST
- · 1.5 V core supply (VDD); 3.3 or 2.5 V I/O (VCC)
- · 40mm BGA package (1.27mm ball pitch)
- Backwards compatible with the SCT2000, SCT9000, SCT9020 & SCT9022

### **Applications**

- · WAN & Internet Core
  - · Multi-Gigabit/Terabit Routers
  - · Edge Routers
  - · ATM, POS, IP
- MAN/LAN/Enterprise
- · Backbone Switch/Routers
- · Aggregation Routers
- · 10Mb/100Mb/1Gb/10Gb Ethernet
- · Layer 4 Flow Classification and Filtering
- · Classless Inter-Domain Routing (CIDR)
- Longest Prefix Match (LPM)
- · Network Address Translation (NAT)
- · Class of Service (CoS)
- · Quality of Service (QoS)
- · Virtual Private Networks (VPN)
- · Policy-based Networking
- · Server Load Balancing

## www.sibercore.com

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