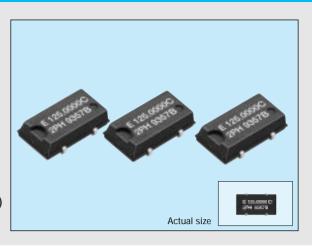
PROGRAMMABLE HIGH-FREQUENCY CRYSTAL OSCILLATOR

SG-8002JC series

- Wide frequency output by PLL technology.
- Quick delivery of samples and short lead mass production time.
- Excellent shock resistance and environmental capability.
- Output enable function (OE) and stand-by function (ST) can be used for low current consumption applications.
- Package and pin compatible with SG-636.

8002 PROM Writer available to purchase.(Type:PRW-8000A3-M01) Please contact EPSON or local sales representative.



Specifications (characteristics)

Item		Symbol	PT/ST	PH/SH	PC/SC	Remarks	
		Symbol	Specifications			Kemarks	
Output frequency range		fo		1.0000 MHz to 125.0000 MHz			
Power source voltage	Max. supply voltage	VDD-GND		-0.5V to +7.0V			
	Operating voltage	V _{DD}	$5.0V \pm 0.5V$ $3.3 \pm 0.3V$		$3.0V \pm 0.3V$: $f_0 \le 66.7MHz(PC/SC)$		
Temperature range	Storage temperature	Tstg		-55°C to +100°C			
	Operating temperature	Topr		-20°C to +70°C		Refer to page 4."Frequency range"	
Soldering condition		Tsol	Twice at under 260°C within 10 sec. or under 230°C within 3 min.				
Frequency stability		∆f/fo	B: ±50ppm C: ± 100ppm		-20°C to +70°C		
Current consumption		lop	45mA max. 28mA max.		No load condition, Max. frequency range		
Output disable current		loe	30mA max. 16mA max		16mA max.	OE=GND(PT, PH, PC)	
Standby current		lsт		50μA max.		ST=GND(ST, SH, SC)	
Duty		tw/t	_	40% to 60%		C-MOS load: 1/2VDD level	
			40% to 60%		_	TTL load: 1.4V level	
High output voltage		Vон		V _{DD} -0.4V min.		I _{он=-} 16mA(PT/ST,PH/SH),-8mA(PC/SC)	
Low output voltage		Vol		0.4V max.		IoL= 16mA(PT/ST,PH/SH), 8mA(PC/SC)	
Output load condition (fan out)	TTL	N	5TTL max.		_	Max. frequency and max. operating voltage range	
	C-MOS	CL		15pF max.			
Output enable/disable input voltage		VIH	2.0V	min.	$0.7 \times V_{DD}$ min.	ST. OE terminal	
		Vıl	V8.0	max. 0.2 × V _{DD} max.		ST, OE terminal	
Output rise time	C-MOS level			4ns max.		C-MOS load: 20%→80% VDD	
	TTL level	tтьн	4ns max.		_	TTL load: 0.4V→2.4V	
Output fall time	C-MOS level		_	4ns max.		C-MOS load: 80%→20% VDD	
	TTL level	tтн∟	4ns max.		_	TTL load: 2.4V→0.4V	
Oscillation start up time		tosc		10ms max.		Time at minimum operating voltage to be 0 sec.	
Aging		fa		±5ppm/year max.		Ta= 25°C, V _{DD} = 5.0V/3.3V(PC/SC)	
Shock resistance		S.R.		±20ppm max.		Three drops on a hard board from 75 cm of excitation test with 3000G x 0.3ms x 1/2sin wave in 3 directions	

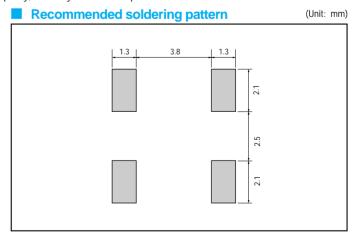
Note:

• Please contact us for inquiries about usable frequencies, duty and output load conditions.

Checking possible by the Frequency Checking Program. http://www.epson.co.jp/CRYSTAL/

Metal may be exposed on the top of this product. This won't affect any quality, reliability or electrical spec.

External dimensions (Unit: mm) NO. Pin terminal 10.5 max. 1 OE or ST #3 GND OUT E 125.0000 C VDD 2PH 9357B 5.8max. (1.0) (1.0) 5.08 (Wired-or connection & Jitter specification, please refer to page 13.)



THE CRYSTALMASTER

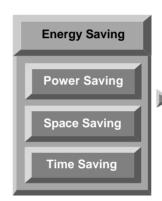


EPSON offers effective savings to its customers through a wide range of electronic devices, such as semiconductors, liquid crystal display (LCD) modules, and crystal devices. These savings are achieved through a sophisticated melding of three different efficiency technologies.

Power saving technology provides low power consumption at low voltages.

Space saving technology provides further reductions in product size and weight through super-precise processing and high-density assembly technology.

Time saving technology shortens the time required for design and development on the customer side and shortens delivery times.



Our concept of Energy Saving technology conserves resources by blending the essence of these three efficiency technologies. The essence of these technologies is represented in each of the products that we provide to our cus-

In the industrial sector, leading priorities include measures to counter the greenhouse effect by reducing CO2,

measures to preserve the global environ-

ment, and the development of energyefficient products. Environmental problems are of global concern, and although the contribution of energysaving technology developed by EPSON may appear insignificant, we seek to contribute to the develop-

ment of energy-saving products by our

customers through the utilization of our electronic devices. EPSON is committed to the conservation of energy, both for the sake of people and of the planet on which we live.





Resource

Saving



SEIKO EPSON CORP. QUARTZ DEVICE DIVISION acquired ISO9001 and ISO14001 certification by B.V.Q.I. (Bureau Veritas Quality International) .

> ISO9001 in October, 1992. ISO14001 in November, 1997.

NOTICE

No part of this material may be reproduced or duplicated in any form or by any means without the written permission of Seiko Epson. Seiko Epson reserves the right to make changes to this material without notice. Seiko Epson does not assume any liability of any kind arising out of any inaccuracies contained in this material or due to its application or use in any product or circuit and, further, there is no representation that this material is applicable to products requiring high level reliability, such as, medical products. Moreover, no license to any intellectual property rights is granted by implication or otherwise, and there is no representation or warranty that anything made in accordance with this material will be free from any patent or copyright infringement of a third party. This material of portions there may contain technology or the subject relating to strategic products under the control of the Foreign Exchange and Foreign Trade Control Law of Japan and may require an export license from the Ministry of International Trade and Industry or other approval from another government agency.