

EPSON S1C63654 Spec

Core CPU.....	4-bit core CPU S1C63
OSC1 oscillation circuit.....	Crystal oscillation circuit: 32 kHz
OSC3 oscillation circuit.....	CR or ceramic oscillation circuit: 1.1 MHz or 4 MHz
Instruction set	411 types, with 8 addressing modes
Minimum instruction execution time	0.5 μ sec / 4 MHz (2 clocks)
Built-in ROM	4k words x 13-bit / 1k x 4-bit program ROM / data ROM
Built-in RAM	512 words x 4-bit / work RAM 48 x 4-bit / display memory
Ports	8-bit input /4-bit output /8-bit I/O
Serial interface	1 channel (synchronous type clock)
Timers	Programmable 16-bit x 1ch, or programmable 8-bit x 2 channel
	PWM output available
	Timekeeping timer: 1 channel
	1/1000 sec. stopwatch timer: 1 channel
R/F converter	2 channels, CR oscillation, supports temperature/humidity sensor
LCD driver	32 segments x 6/5/4/3 commons
Sound generator	Built-in, with envelope and one-shot output functions
Watchdog timer	Built-in
Supply voltage detector circuit	Built-in
Supply voltage	1.8 – 3.6 V (32k); 2.4 – 3.6 V (4M)
Package	Chip 100-pin QFP15 (14 x 14 x 1.4 mm body)