

Micro MINI S1C60N03

4-bit Single Chip Microcomputer



- S1C6200B Core CPU
- Low Voltage and Low Power
- Built-in LCD Driver
- Low Cost Performance

■ DESCRIPTION

The S1C60N03 Series single-chip microcomputer features an S1C6200B CMOS 4-bit CPU as the core. It contains a 768 (words) × 12 (bits) ROM, 64 (words) × 4 (bits) RAM, LCD driver , 4-bit input port (K00–K03), 4-bit output port (R00–R03) and a timer.

The S1C60N03 Series is configured as follows, depending on the supply voltage.

S1C60N03: 3.0 V (1.8 to 3.6 V)

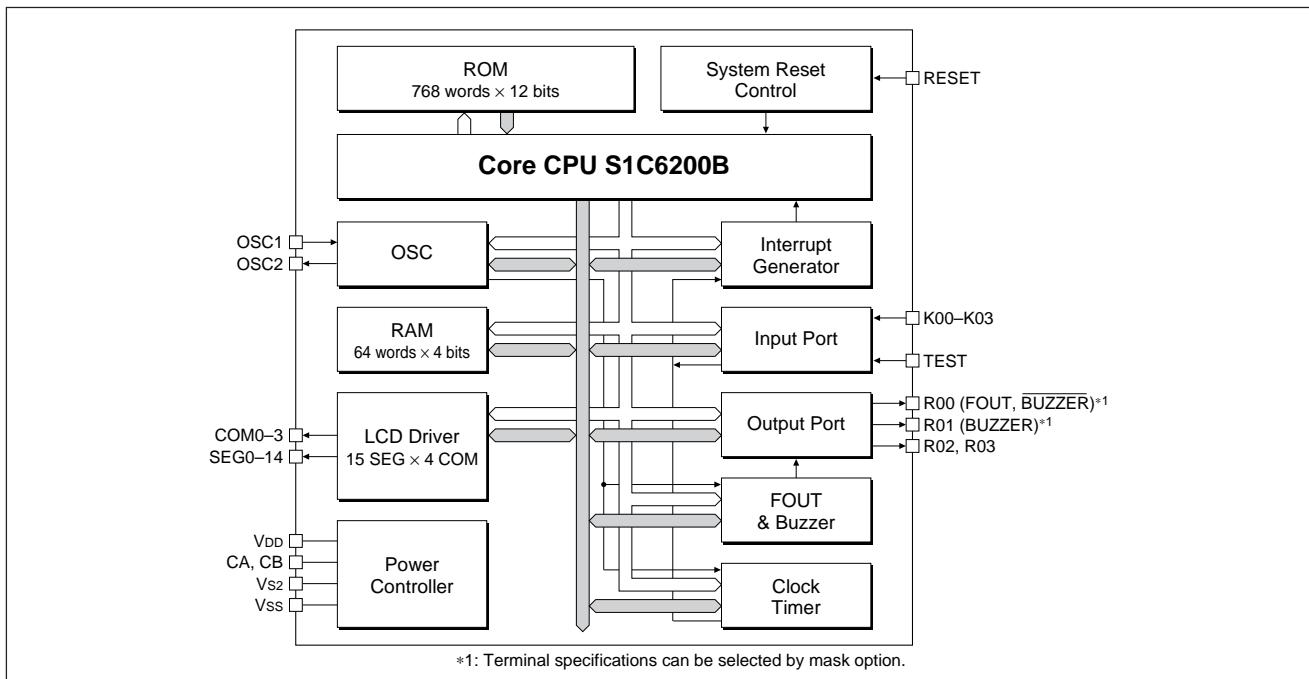
S1C60L03: 1.5 V (1.2 to 2.0 V)

■ FEATURES

- Core CPU S1C6200B
- Built-in oscillation circuit Crystal 32.768 kHz (Typ.) or CR oscillation circuit 65 kHz (Typ.)
- Instruction set 100 instructions
- ROM capacity 768 words × 12 bits
- RAM capacity 64 words × 4 bits
- Input port 4 bits (pull-down resistors are available by mask option)
- Output ports 4 bits (clock and buzzer outputs are possible by mask option)
- LCD driver 15 segments × 4, 3 or 2 commons (1/4, 1/3 or 1/2 duty are selectable by mask option)
- Timer 1 system (clock timer) built-in
- Interrupt External: Input port interrupt 1 system
Internal: Timer interrupt 1 system
- Supply voltage 1.5 V (1.2 to 2.0 V) S1C60L03
3.0 V (1.8 to 3.6 V) S1C60N03
- Current consumption (Typ.) During HALT: 1.0 µA (32 kHz crystal, with power divider OFF)
During execution: 2.5 µA (32 kHz crystal, with power divider OFF)
15 µA (32 kHz crystal, with power divider ON)
- Supply form Chip

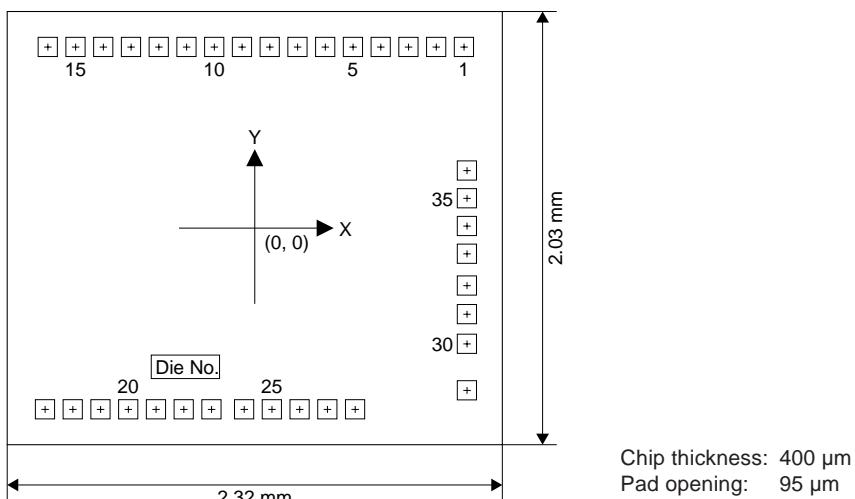
S1C60N03

■ BLOCK DIAGRAM



■ PAD LAYOUT

Pad Layout Diagram



Pad Coordinates (unit: μm)

No.	Pad name	X	Y	No.	Pad name	X	Y	No.	Pad name	X	Y
1	TEST	980	849	13	SEG3	-580	849	25	OSC2	80	-849
2	SEG14	850	849	14	SEG2	-710	849	26	OSC1	210	-849
3	SEG13	720	849	15	SEG1	-840	849	27	VDD	340	-849
4	SEG12	590	849	16	SEG0	-970	849	28	RESET	470	-849
5	SEG11	460	849	17	COM0	-983	-849	29	R00	994	-760
6	SEG10	330	849	18	COM1	-853	-849	30	R01	994	-542
7	SEG9	200	849	19	COM2	-723	-849	31	R02	994	-403
8	SEG8	70	849	20	COM3	-593	-849	32	R03	994	-269
9	SEG7	-60	849	21	CA	-463	-849	33	K00	994	-120
10	SEG6	-190	849	22	CB	-333	-849	34	K01	994	10
11	SEG5	-320	849	23	Vs2	-203	-849	35	K02	994	140
12	SEG4	-450	849	24	Vss	-50	-849	36	K03	994	270

■ PAD DESCRIPTION

Pad name	Pad No.	I/O	Function
VDD	27	(I)	Power supply terminal (+)
Vss	24	(I)	Power supply terminal (-)
Vs2	23	O	LCD system voltage doubler (2·Vss)/halver (Vss/2) output
CA, CB	21, 22	-	Booster capacitor connecting terminal
OSC1	26	I	Crystal or CR oscillation input terminal *
OSC2	25	O	Crystal or CR oscillation output terminal *
K00–03	33–36	I	Input port terminal
R00	29	O	Output port terminal, BUZZER or FOUT output terminal *
R01	30	O	Output port terminal or BUZZER output terminal *
R02, R03	31, 32	O	Output port terminal
SEG0–14	2–16	O	LCD segment output or DC output terminal *
COM0–3	17–20	O	LCD common output terminal (1/4, 1/3 or 1/2 duty are selectable *)
RESET	28	I	Initial reset input terminal
TEST	1	I	Test input terminal

* Can be selected by mask option

■ OPTION LIST

- | | |
|----------------------------------|---|
| 1. DEVICE TYPE | <input type="checkbox"/> 1. EOC6003 (Normal Type <S1C60N03>)
<input type="checkbox"/> 2. EOC60L03 (Low Power Type <S1C60L03>) |
| 2. LCD SPECIFICATION | |
| • BIAS SELECTION | <input type="checkbox"/> 1. 1/3 Bias By Voltage Divider
<input type="checkbox"/> 2. 1/2 Bias By Voltage Divider
<input type="checkbox"/> 3. 1/2 Bias By Doubler/Halver |
| • DUTY SELECTION | <input type="checkbox"/> 1. 1/4 Duty
<input type="checkbox"/> 2. 1/3 Duty
<input type="checkbox"/> 3. 1/2 Duty |
| 3. OSC1 SYSTEM CLOCK | <input type="checkbox"/> 1. Crystal
<input type="checkbox"/> 2. CR |
| 4. MULTIPLE KEY ENTRY RESET | |
| • COMBINATION | <input type="checkbox"/> 1. Not Use
<input type="checkbox"/> 2. Use K00, K01
<input type="checkbox"/> 3. Use K00, K01, K02
<input type="checkbox"/> 4. Use ALL K00–K03 |
| 5. INTERRUPT NOISE REJECTOR | |
| • K00–K03 | <input type="checkbox"/> 1. Use
<input type="checkbox"/> 2. Not Use |
| 6. TIMER INTERRUPT FREQUENCY | |
| • INTERRUPT FREQUENCY | <input type="checkbox"/> 1. 32/16/2 Hz Interrupt
<input type="checkbox"/> 2. 64/16/2 Hz Interrupt |
| 7. INPUT PORT PULL DOWN RESISTOR | |
| • K00 | <input type="checkbox"/> 1. With Resistor <input type="checkbox"/> 2. Gate Direct |
| • K01 | <input type="checkbox"/> 1. With Resistor <input type="checkbox"/> 2. Gate Direct |
| • K02 | <input type="checkbox"/> 1. With Resistor <input type="checkbox"/> 2. Gate Direct |
| • K03 | <input type="checkbox"/> 1. With Resistor <input type="checkbox"/> 2. Gate Direct |
| 8. R00 SPECIFICATION | |
| • OUTPUT TYPE | <input type="checkbox"/> 1. DC Output
<input type="checkbox"/> 2. Buzzer Inverted Output (R00 Control)
<input type="checkbox"/> 3. FOUT Output |

S1C60N03

• FOUT OUTPUT SPACIFICATION	
F1	<input type="checkbox"/> 1. 256[Hz] <input type="checkbox"/> 2. 512[Hz] <input type="checkbox"/> 3. 1,024[Hz] <input type="checkbox"/> 4. 2,048[Hz] <input type="checkbox"/> 5. 4,096[Hz]
F2	<input type="checkbox"/> 1. 512[Hz] <input type="checkbox"/> 2. 1,024[Hz] <input type="checkbox"/> 3. 2,048[Hz] <input type="checkbox"/> 4. 4,096[Hz] <input type="checkbox"/> 5. 8,192[Hz]
F3	<input type="checkbox"/> 1. 1,024[Hz] <input type="checkbox"/> 2. 2,048[Hz] <input type="checkbox"/> 3. 4,096[Hz] <input type="checkbox"/> 4. 8,192[Hz] <input type="checkbox"/> 5. 16,384[Hz]
F4	<input type="checkbox"/> 1. 2,048[Hz] <input type="checkbox"/> 2. 4,096[Hz] <input type="checkbox"/> 3. 8,192[Hz] <input type="checkbox"/> 4. 16,384[Hz] <input type="checkbox"/> 5. 32,768[Hz]
• OUTPUT SPECIFICATION	<input type="checkbox"/> 1. Complementary <input type="checkbox"/> 2. Pch-Open Drain
9. R01 SPECIFICATION	<input type="checkbox"/> 1. DC Output <input type="checkbox"/> 2. Buzzer Output <input type="checkbox"/> 2. Pch-Open Drain
• OUTPUT TYPE	<input type="checkbox"/> 1. DC Output
• OUTPUT SPECIFICATION	<input type="checkbox"/> 1. Complementary
10. R02, R03 SPECIFICATION	<input type="checkbox"/> 1. Complementary <input type="checkbox"/> 2. Pch-Open Drain <input type="checkbox"/> 2. Pch-Open Drain
• R02 OUTPUT SPECIFICATION ...	<input type="checkbox"/> 1. Complementary
• R03 OUTPUT SPECIFICATION ...	<input type="checkbox"/> 1. Complementary

■ ELECTRICAL CHARACTERISTICS

● Absolute Maximum Ratings

Rating	Symbol	Value	(VDD=0V)
Supply voltage	Vss	-5.0 to 0.5	V
Input voltage (1)	Vi	Vss - 0.3 to 0.5	V
Input voltage (2)	Viosc	Vss - 0.3 to 0.5	V
Permissible total output current *1	ΣI_{VSS}	10	mA
Operating temperature	Topr	-20 to 70	°C
Storage temperature	Tstg	-65 to 150	°C
Soldering temperature / time	Tsol	260°C, 10sec (lead section)	—
Permissible dissipation	Pd	250	mW

*1: The permissible total output current is the sum total of the current (average current) that simultaneously flows from the output pin (or is drawn in).

● Recommended Operating Conditions

S1C60N03

Condition	Symbol	Remark	Min.	Typ.	Max.	Unit
Supply voltage	Vss	VDD=0V	-3.6	-3.0	-1.8	V
Oscillation frequency	fosc	Crystal oscillation CR oscillation, RCR=470kΩ		32.768		kHz
Booster capacitor	C1		50	65	80	μF
Capacitor between VDD and Vs2	C2		0.1			μF

S1C60L03

Condition	Symbol	Remark	Min.	Typ.	Max.	Unit
Supply voltage	Vss	VDD=0V	-0.2	-1.5	-1.2	V
Oscillation frequency	fosc	Crystal oscillation CR oscillation, RCR=470kΩ		32.768		kHz
Booster capacitor	C1		50	65	80	kHz
Capacitor between VDD and Vs2	C2		0.1			μF

● DC Characteristics

S1C60N03

(Unless otherwise specified: VDD=0V, Vss=-3.0V, fosc=32.768kHz, Ta=25°C, Vs2 is internal voltage, C1=C2=0.1μF)

Characteristic	Symbol	Condition	Min.	Typ.	Max.	Unit
High level input voltage (1)	VIH1	K00-K03	0.2•Vss		0	V
High level input voltage (2)	VIH2	RESET	0.15•Vss		0	V
Low level input voltage (1)	VIL1	K00-K03	Vss		0.8•Vss	V
Low level input voltage (2)	VIL2	RESET	Vss		0.85•Vss	V
High level input current (1)	IIH1	VIH1=0V, No pull down resistor	K00-K03	0	0.5	μA
High level input current (2)	IIH2	VIH2=0V, With pull down resistor	K00-K03	10	40	μA
High level input current (3)	IIH3	VIH3=0V, With pull down resistor	RESET	30	100	μA
Low level input current	IIL	VIL=VSS	K00-K03 RESET, TEST	-0.5	0	μA
High level output current (1)	IOH1	VOH1=0.1•Vss	R02, R03		-1.0	mA
High level output current (2)	IOH2	VOH2=0.1•Vss (built-in protection resistance)	R00, R01		-1.0	mA
Low level output current (1)	IOL1	VOL1=0.9•Vss	R02, R03	3.0		mA
Low level output current (2)	IOL2	VOL2=0.9•Vss (built-in protection resistance)	R00, R01	3.0		mA
Common output current	IOH3	VOH3=-0.05V	COM0-COM3		-3	μA
	IOL3	VOL3=VL3+0.05V		3		μA
Segment output current (during LCD output)	IOH4	VOH4=-0.05V	SEG0-SEG14		-3	μA
	IOL4	VL4=VL3+0.05V		3		μA
Segment output current (during DC output)	IOH5	VOH5=0.1•Vss	SEG0-SEG14		-300	μA
	IOL5	VOL5=0.9•Vss		300		μA

S1C60L03

(Unless otherwise specified: VDD=0V, Vss=-1.5V, fosc=32.768kHz, Ta=25°C, Vs2 is internal voltage, C1=C2=0.1μF)

Characteristic	Symbol	Condition	Min.	Typ.	Max.	Unit
High level input voltage (1)	VIH1	K00-K03	0.2•Vss		0	V
High level input voltage (2)	VIH2	RESET	0.15•Vss		0	V
Low level input voltage (1)	VIL1	K00-K03	Vss		0.8•Vss	V
Low level input voltage (2)	VIL2	RESET	Vss		0.85•Vss	V
High level input current (1)	IIH1	VIH1=0V, No pull down resistor	K00-K03	0	0.5	μA
High level input current (2)	IIH2	VIH2=0V, With pull down resistor	K00-K03	5.0	20	μA
High level input current (3)	IIH3	VIH3=0V, With pull down resistor	RESET	9.0	100	μA
Low level input current	IIL	VIL=VSS	K00-K03 RESET, TEST	-0.5	0	μA
High level output current (1)	IOH1	VOH1=0.1•Vss	R02, R03		-200	μA
High level output current (2)	IOH2	VOH2=0.1•Vss (built-in protection resistance)	R00, R01		-200	μA
Low level output current (1)	IOL1	VOL1=0.9•Vss	R02, R03	700		μA
Low level output current (2)	IOL2	VOL2=0.9•Vss (built-in protection resistance)	R00, R01	700		μA
Common output current	IOH3	VOH3=-0.05V	COM0-COM3		-3	μA
	IOL3	VOL3=VL3+0.05V		3		μA
Segment output current (during LCD output)	IOH4	VOH4=-0.05V	SEG0-SEG14		-3	μA
	IOL4	VL4=VL3+0.05V		3		μA
Segment output current (during DC output)	IOH5	VOH5=0.1•Vss	SEG0-SEG14		-100	μA
	IOL5	VOL5=0.9•Vss		130		μA

● Analog Circuit Characteristics and Current Consumption

S1C60N03 (Crystal Oscillation)

(Unless otherwise specified: VDD=0V, Vss=-3.0V, fosc=32.768kHz, Ta=25°C, CG=25pF, Vs2 is internal voltage, C1=C2=0.1μF)

Characteristic	Symbol	Condition	Min.	Typ.	Max.	Unit
LCD drive voltage	VL1	Connect 1 MΩ load resistor between VDD and segment driver (SEG0-SEG14) when segment driver's level is VL1	1/3•Vss - 0.1	1/3•Vss	1/3•Vss ×0.9	V
	VL2	Connect 1 MΩ load resistor between VDD and segment driver (SEG0-SEG14) when segment driver's level is VL2	2/3•Vss - 0.1	2/3•Vss	2/3•Vss ×0.9	V
	VL3	Connect 1 MΩ load resistor between VDD and segment driver (SEG0-SEG14) when segment driver's level is VL3		Vss		V
Current consumption	IHLT	During HALT with LCD OFF		1.0	2.5	μA
	IEXE1	During operation with LCD OFF	No panel load	2.0	5.0	μA
	IEXE2	During operation with power divider ON		15	20	μA

S1C60N03

S1C60L03 (Crystal Oscillation)

(Unless otherwise specified: VDD=0V, Vss=-1.5V, fosc=32.768kHz, Ta=25°C, Cg=25pF, Vs2 is internal voltage, C1=C2=0.1μF)

Characteristic	Symbol	Condition	Min.	Typ.	Max.	Unit
LCD drive voltage	V _{L1}	Connect 1 MΩ load resistor between VDD and segment driver (SEG0–SEG14) when segment driver's level is V _{L1}	1/3·Vs ₂ - 0.1	1/3·Vs ₂	1/3·Vs ₂ ×0.9	V
	V _{L2}	Connect 1 MΩ load resistor between VDD and segment driver (SEG0–SEG14) when segment driver's level is V _{L2}	2/3·Vs ₂ - 0.1	2/3·Vs ₂	2/3·Vs ₂ ×0.9	V
	V _{L3}	Connect 1 MΩ load resistor between VDD and segment driver (SEG0–SEG14) when segment driver's level is V _{L3}	Vs ₂ - 0.1	Vs ₂	Vs ₂ ×0.9	V
Current consumption	I _{HLT}	During HALT with LCD OFF	No panel load	1.0	2.5	μA
	I _{EXE1}	During operation with LCD OFF		2.0	5.0	μA
	I _{EXE2}	During operation with power divider ON		15	20	μA

S1C60N03 (CR Oscillation)

(Unless otherwise specified: VDD=0V, Vss=-3.0V, fosc=65kHz, Ta=25°C, Rcr=470kΩ, Vs2 is internal voltage, C1=C2=0.1μF)

Characteristic	Symbol	Condition	Min.	Typ.	Max.	Unit
LCD drive voltage	V _{L1}	Connect 1 MΩ load resistor between VDD and segment driver (SEG0–SEG14) when segment driver's level is V _{L1}	1/3·Vss - 0.1	1/3·Vss	1/3·Vss ×0.9	V
	V _{L2}	Connect 1 MΩ load resistor between VDD and segment driver (SEG0–SEG14) when segment driver's level is V _{L2}	2/3·Vss - 0.1	2/3·Vss	2/3·Vss ×0.9	V
	V _{L3}	Connect 1 MΩ load resistor between VDD and segment driver (SEG0–SEG14) when segment driver's level is V _{L3}	Vss			V
Current consumption	I _{HLT}	During HALT with LCD OFF	No panel load	8	15	μA
	I _{EXE1}	During operation with LCD OFF		15	20	μA
	I _{EXE2}	During operation with power divider ON		25	30	μA

S1C60L03 (CR Oscillation)

(Unless otherwise specified: VDD=0V, Vss=-1.5V, fosc=65kHz, Ta=25°C, Rcr=470kΩ, Vs2 is internal voltage, C1=C2=0.1μF)

Characteristic	Symbol	Condition	Min.	Typ.	Max.	Unit
LCD drive voltage	V _{L1}	Connect 1 MΩ load resistor between VDD and segment driver (SEG0–SEG14) when segment driver's level is V _{L1}	1/3·Vs ₂ - 0.1	1/3·Vs ₂	1/3·Vs ₂ ×0.9	V
	V _{L2}	Connect 1 MΩ load resistor between VDD and segment driver (SEG0–SEG14) when segment driver's level is V _{L2}	2/3·Vs ₂ - 0.1	2/3·Vs ₂	2/3·Vs ₂ ×0.9	V
	V _{L3}	Connect 1 MΩ load resistor between VDD and segment driver (SEG0–SEG14) when segment driver's level is V _{L3}	Vs ₂ - 0.1	Vs ₂	Vs ₂ ×0.9	V
Current consumption	I _{HLT}	During HALT with LCD OFF	No panel load	8	15	μA
	I _{EXE1}	During operation with LCD OFF		15	20	μA
	I _{EXE2}	During operation with power divider ON		25	30	μA

● Oscillation Characteristics

Oscillation characteristics will vary according to different conditions (elements used, board pattern). Use the following characteristics as reference values.

S1C60N03 Crystal Oscillation

(Unless otherwise specified: VDD=0V, Vss=-3.0V, fosc=32.768kHz, Crystal: Q13MC146, Cg=25pF, Cd=built-in, Ta=25°C)

Characteristic	Symbol	Condition	Min.	Typ.	Max.	Unit
Oscillation start voltage	V _{sta}	t _{sta} ≤5sec (Vss)	-1.8			V
Oscillation stop voltage	V _{stp}	t _{stp} ≤10sec (Vss)	-1.8			V
Built-in capacitance (drain)	C _D	Including the parasitic capacitance inside the IC (in chip)		20		pF
Frequency/voltage deviation	Δf/ΔV	Vss=-1.8 to -3.6V			5	ppm
Frequency/IC deviation	Δf/ΔIC		-10		10	ppm
Frequency adjustment range	Δf/ΔC _G	C _G =5 to 25pF	40			ppm
Harmonic oscillation start voltage	V _{hho}	C _G =5pF (Vss)			-3.6	V
Permitted leak resistance	R _{leak}	Between OSC1 and VDD	200			MΩ

S1C60L03 Crystal Oscillation

(Unless otherwise specified: VDD=0V, Vss=-1.5V, fosc=32.768kHz, Crystal: Q13MC146, CG=25pF, CD=built-in, Ta=25°C)

Characteristic	Symbol	Condition	Min.	Typ.	Max.	Unit
Oscillation start voltage	Vsta	tsta≤5sec (Vss)	-1.2			V
Oscillation stop voltage	Vstp	tstp≤10sec (Vss)	-1.2			V
Built-in capacitance (drain)	CD	Including the parasitic capacitance inside the IC (in chip)		20		pF
Frequency/voltage deviation	Δf/ΔV	Vss=-1.2 to -2.0V			5	ppm
Frequency/IC deviation	Δf/ΔIC		-10		10	ppm
Frequency adjustment range	Δf/ΔCG	CG=5 to 25pF	40			ppm
Harmonic oscillation start voltage	Vhho	CG=5pF (Vss)			-2.0	V
Permitted leak resistance	Rleak	Between OSC1 and VDD	200			MΩ

S1C60N03 CR Oscillation

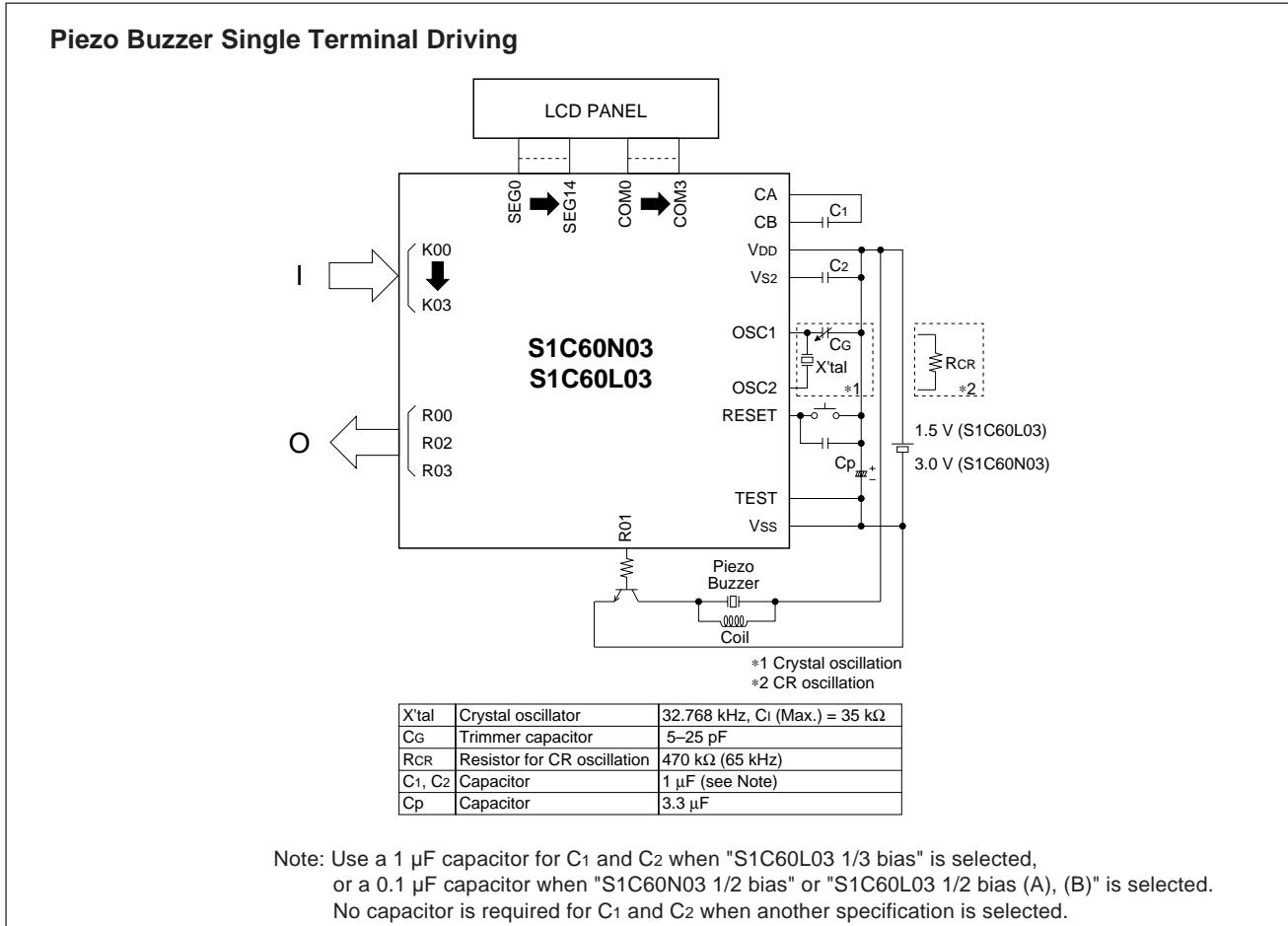
(Unless otherwise specified: VDD=0V, Vss=-3.0V, RCR=470kΩ, Ta=25°C)

Characteristic	Symbol	Condition	Min.	Typ.	Max.	Unit
Oscillation frequency dispersion	fosc		-20	65kHz	20	%
Oscillation start voltage	Vsta	(Vss)	-1.8			V
Oscillation start time	tsta	Vss=-1.8 to -3.6V		3		mS
Oscillation stop voltage	Vstp	(Vss)	-1.8			V

S1C60L03 CR Oscillation

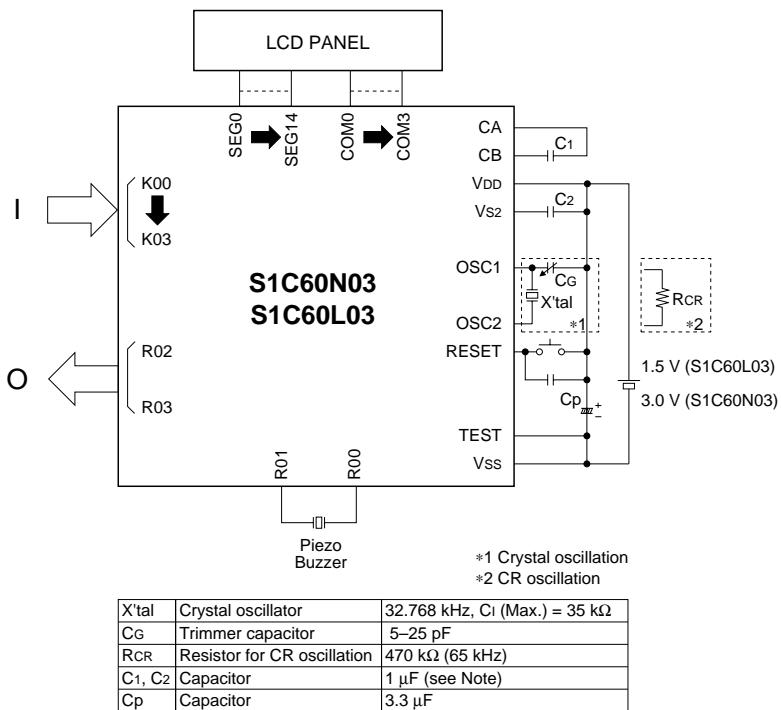
(Unless otherwise specified: VDD=0V, Vss=-1.5V, RCR=470kΩ, Ta=25°C)

Characteristic	Symbol	Condition	Min.	Typ.	Max.	Unit
Oscillation frequency dispersion	fosc		-20	65kHz	20	%
Oscillation start voltage	Vsta	(Vss)	-1.2			V
Oscillation start time	tsta	Vss=-1.2 to -2.0V		3		mS
Oscillation stop voltage	Vstp	(Vss)	-1.2			V

■ BASIC EXTERNAL CONNECTION DIAGRAM

S1C60N03

Piezo Buzzer Direct Driving



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