

*IGBT* 

# SGH20N120RUF

### Short Circuit Rated IGBT

### **General Description**

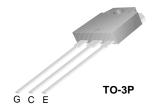
Fairchild's RUF series of Insulated Gate Bipolar Transistors (IGBTs) RUF series provides low conduction and switching losses as well as short circuit ruggedness. The RUF series is designed for the applications such as motor control, uninterrupted power supplies (UPS) and general inverters where short circuit ruggedness is a required feature.

#### **Features**

- Short circuit rated 10 $\mu$ s @ T<sub>C</sub> = 100°C, V<sub>GE</sub> = 15V
- High speed switching
- Low saturation voltage: V<sub>CE(sat)</sub> = 2.3 V @ I<sub>C</sub> = 20A
- High input impedance

## **Applications**

AC & DC motor controls, general purpose inverters, robotics, and servo controls.





# Absolute Maximum Ratings T<sub>C</sub> = 25°C unless otherwise noted

Symbol	Description		SGH20N120RUF	Units
V <sub>CES</sub>	Collector-Emitter Voltage		1200	V
V <sub>GES</sub>	Gate-Emitter Voltage		± 25	V
	Collector Current	@ T <sub>C</sub> = 25°C	32	Α
I <sub>C</sub>	Collector Current	@ T <sub>C</sub> = 100°C	20	Α
I <sub>CM (1)</sub>	Pulsed Collector Current		60	Α
	Short Circuit Withstand Time	@ T <sub>C</sub> = 100°C	10	μs
P <sub>D</sub>	Maximum Power Dissipation	@ $T_C = 25^{\circ}C$	230	W
	Maximum Power Dissipation	@ T <sub>C</sub> = 100°C	92	W
T <sub>J</sub>	Operating Junction Temperature		-55 to +150	°C
T <sub>stg</sub>	Storage Temperature Range		-55 to +150	°C
T <sub>L</sub>	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 seconds		300	°C

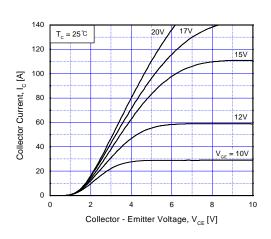
#### Notes:

(1) Repetitive rating : Pulse width limited by max. junction temperature

### **Thermal Characteristics**

Symbol	Parameter	Тур.	Max.	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case		0.54	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient		40	°C/W

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
Off Cha	racteristics					
BV <sub>CES</sub>	Collector-Emitter Breakdown Voltage	$V_{GE} = 0V, I_{C} = 1mA$	1200			V
$\Delta B_{VCES}/$ $\Delta T_J$	Temperature Coefficient of Breakdown Voltage	V <sub>GE</sub> = 0V, I <sub>C</sub> = 1mA		0.6		V/°C
I <sub>CES</sub>	Collector Cut-Off Current	$V_{CE} = V_{CES}, V_{GE} = 0V$			1	mΑ
$I_{GES}$	G-E Leakage Current	$V_{GE} = V_{GES}, V_{CE} = 0V$			± 100	nA
On Char	racteristics					
V <sub>GE(th)</sub>	G-E Threshold Voltage	$I_C = 20$ mA, $V_{CE} = V_{GE}$	3.5	5.5	7.5	V
•	Collector to Emitter	$I_C = 20A$ , $V_{GE} = 15V$		2.3	3.0	V
$V_{CE(sat)}$	Saturation Voltage	$I_C = 32A$ , $V_{GE} = 15V$		2.8		
Dvnami	c Characteristics	, OL	1			
C <sub>ies</sub>	Input Capacitance			2000		pF
C <sub>oes</sub>	Output Capacitance	$V_{CE} = 30V, V_{GE} = 0V,$		170		pF
C <sub>res</sub>	Reverse Transfer Capacitance	f = 1MHz		60		pF
t <sub>d(on)</sub>	Turn-On Delay Time			30		ns
t <sub>d(on)</sub>	•					ns
t <sub>r</sub>	Rise Time			60		ns
t <sub>d(off)</sub>	Turn-Off Delay Time	$V_{CC} = 600 \text{ V}, I_{C} = 20\text{A},$		70	130	ns
t <sub>f</sub>	Fall Time	$R_G = 15\Omega, V_{GE} = 15V,$		150	300	ns
E <sub>on</sub>	Turn-On Switching Loss	Inductive Load, T <sub>C</sub> = 25°C		1.30		mJ
E <sub>off</sub>	Turn-Off Switching Loss			1.30		mJ
E <sub>ts</sub>	Total Switching Loss			2.60	3.65	mJ
t <sub>d(on)</sub>	Turn-On Delay Time			30		ns
t <sub>r</sub>	Rise Time			70		ns
t <sub>d(off)</sub>	Turn-Off Delay Time	$V_{CC} = 600 \text{ V}, I_{C} = 20 \text{A},$		90	165	ns
t <sub>f</sub>	Fall Time	$R_G = 15\Omega$ , $V_{GE} = 15V$ ,		200	400	ns
E <sub>on</sub>	Turn-On Switching Loss	Inductive Load, T <sub>C</sub> = 125°C		1.50		mJ
E <sub>off</sub>	Turn-Off Switching Loss			2.00		mJ
E <sub>ts</sub>	Total Switching Loss			3.50	5.08	mJ
T <sub>sc</sub>	Short Circuit Withstand Time	V <sub>CC</sub> = 600 V, V <sub>GE</sub> = 15V @ T <sub>C</sub> = 100°C	10			μs
Q <sub>q</sub>	Total Gate Charge			95	140	nC
Q <sub>qe</sub>	Gate-Emitter Charge	$V_{CE} = 600 \text{ V}, I_{C} = 20\text{A},$		15	25	nC
Q <sub>gc</sub>	Gate-Collector Charge	V <sub>GE</sub> = 15V		43	65	nC
L <sub>e</sub>	Internal Emitter Inductance	Measured 5mm from PKG		14		nH



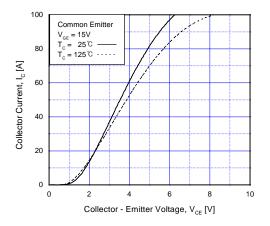
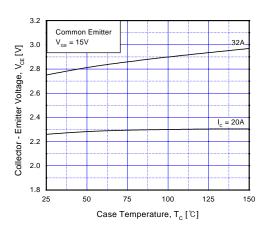


Fig 1. Typical Output Characteristics

Fig 2. Typical Saturation Voltage Characteristics



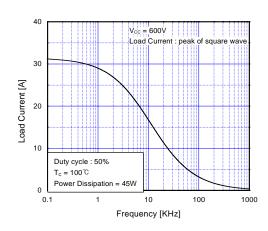
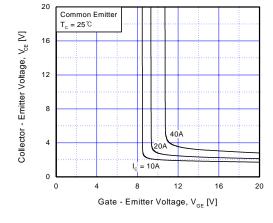


Fig 3. Saturation Voltage vs. Case
Temperature at Variant Current Level

Fig 4. Load Current vs. Frequency



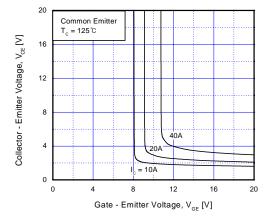
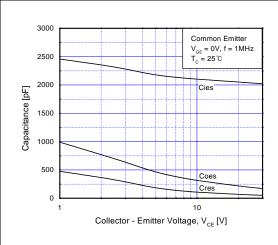


Fig 5. Saturation Voltage vs. V<sub>GE</sub>

Fig 6. Saturation Voltage vs.  $V_{\text{GE}}$ 

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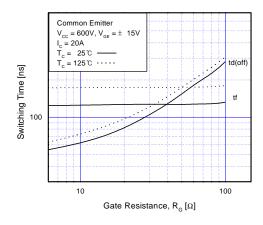


 $V_{CC} = 600V, V_{GE} = \pm 15V$   $V_{CC} = 25C \longrightarrow T_{C} = 125C \cdots$   $T_{C} =$ 

Common Emitter

Fig 7. Capacitance Characteristics

Fig 8. Turn-On Characteristics vs.
Gate Resistance



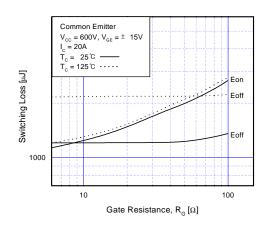
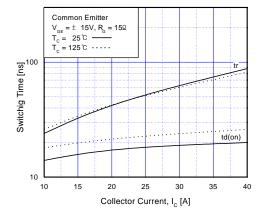


Fig 9. Turn-Off Characteristics vs.
Gate Resistance

Fig 10. Switching Loss vs. Gate Resistance



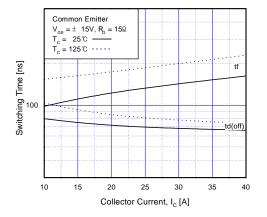
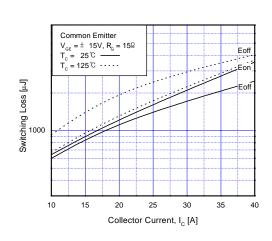


Fig 11. Turn-On Characteristics vs. Collector Current

Fig 12. Turn-Off Characteristics vs. Collector Current



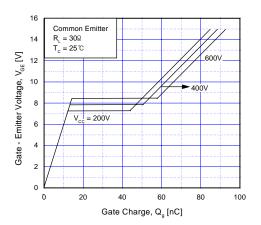
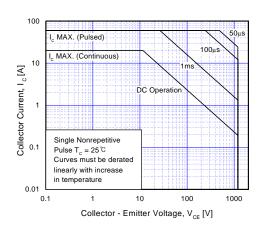


Fig 13. Switching Loss vs. Collector Current

Fig 14. Gate Charge Characteristics



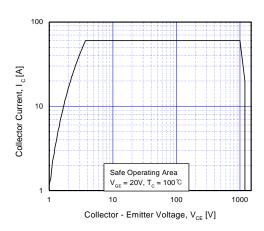


Fig 15. SOA Characteristics

Fig 16. Turn-Off SOA

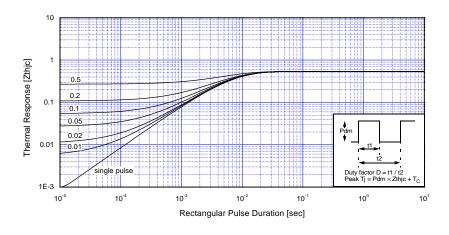
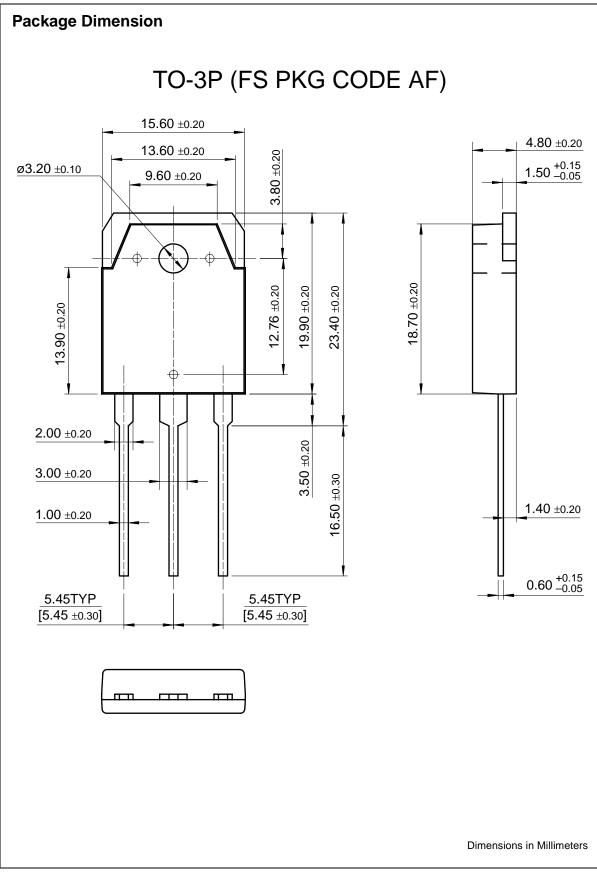


Fig 17. Transient Thermal Impedance of IGBT



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