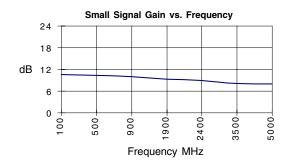


Product Description

Stanford Microdevices' SGA-4186 is a high performance cascadeable 50-ohm amplifier designed for operation at voltages as low as 3V. This RFIC uses the latest Silicon Germanium Heterostructure Bipolar Transistor (SiGe HBT) process featuring 1 micron emitters with F_T up to 65 GHz.

This circuit uses a darlington pair topology with resistive feedback for broadband performance as well as stability over its entire temperature range. Internally matched to 50 ohm impedance, the SGA-4186 requires only DC blocking and bypass capacitors for external components.



Electrical Specifications at Ta = 25C

SGA-4186

DC-6000 MHz Silicon Germanium Cascadeable Gain Block



Product Features

- Excellent Broadband Performance -DC-6000 MHz Operation
- Single Voltage Supply
- High Output Intercept: +28.3dBm typ. at 850 MHz
- Low Current Draw: 45mA at 3.2V typ.
- Input/Output Matched to 50 Ohms

Applications

- Line Drivers for Optical Fiber
- PA for Low Power Applications
- Broadband Test Equipment
- Drivers for CATV Amplifiers

Symbol	Parameters: Test Conditions: Z ₀ = 50 Ohms, Id = 45 ma, T = 25°C		Units	Min.	Тур.	Max.
P _{1dB}	Output Power at 1dB Compression	f = 850 MHz f = 1950 MHz	dBm dBm		14.6 12.4	
S ₂₁	Small Signal Gain	f = DC - 1000 MHz f = 1000 - 2400 MHz f = 2400 - 6000 MHz	dB dB	9.0	10.0 9.0 8.0	
S ₁₂	Reverse Isolation	f = DC - 1000 MHz f = 1000 - 2400 MHz f = 2400 - 6000 MHz	dB dB dB		16.1 17.0 18.2	
S ₁₁	Input VSWR	f = DC - 2400 MHz f = 2400 - 6000 MHz	-		1.33:1 1.74:1	
S ₂₂	Output VSWR	f = DC - 2400 MHz f = 2400 - 6000 MHz			1.40:1 1.63:1	
IP ₃	Third Order Intercept Point	f = 850 MHz f = 1950 MHz	dBm dBm		28.3 25.5	
NF	Noise Figure	f = DC - 1000 MHz f = 1000 - 2000 MHz	dB dB		4.7 5.3	
T _D	Group Delay	f = 1000 MHz	pS		119.0	
V _D	Device Voltage		V	2.8	3.2	3.6

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Phone: (800) SMI-MMIC





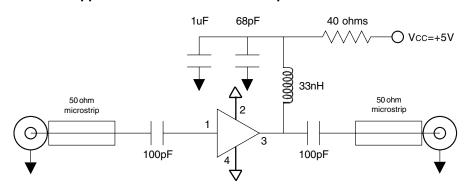
	Test				
Parameter	Min	Тур.	Max.	Unit	Condition
Device Bias					T= 25C
Operating Voltage		3.2		V	
Operating Current		45.0		mA	
500 MHz					T= 25C
Gain		10.4		dB	
Noise Figure		4.6		dB	
Output IP3		29.3		dBm	
Output P1dB		13.6		dBm	
nput Return Loss		25.6		dB	
İsolation		16.0		dB	
850 MHz					T= 25C
Gain		10.0		dB	
Noise Figure		4.7		dB	
Output IP3		28.3		dBm	
Output P1dB		14.6		dBm	
Input Return Loss		16.2		dB	
solation		16.6		dB	
1950 MHz					T= 25C
Gain		9.2		dB	
Noise Figure		5.2		dB	
Output IP3		25.5		dBm	
Output P1dB		12.4		dBm	
nput Return Loss		17.9		dB	
Isolation		17.7		dB	
2400 MHz					T= 25C
Gain		8.9		dB	
Noise Figure		5.3		dB	
Output IP3		24.1		dBm	
Output P1dB		11.3		dBm	
nput Return Loss		22.8		dB	
solation		18.2		dB	

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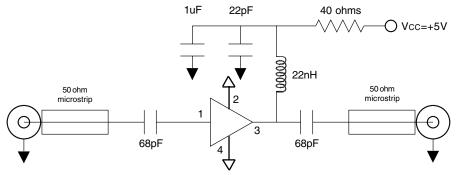


Pin #	Function	Description	Device Schematic	
1	RF IN	RF input pin. This pin requires the use of an external DC blocking capacitor chosen for the frequency of operation.		
2	GND	Connection to ground. Use via holes for best performance to reduce lead inductance as close to ground leads as possible.		
3		RF output and bias pin. DC voltage is present on this pin, therefore a DC blocking capacitor is necessary for proper operation.		
4	GND	Sames as Pin 2		

Application Schematic for +5V Operation at 900 MHz



Application Schematic for +5V Operation at 1900 MHz

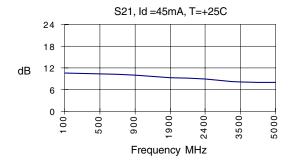


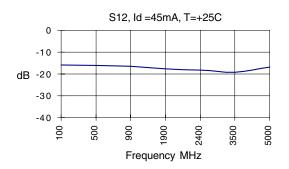
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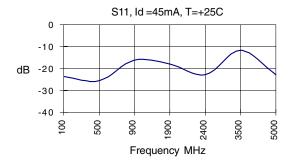
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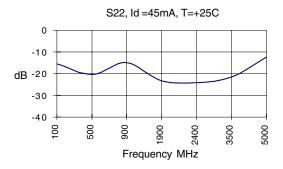


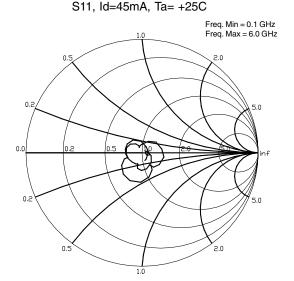


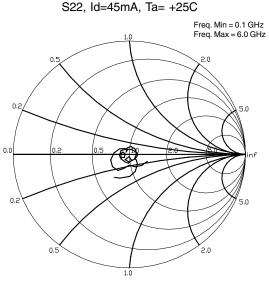












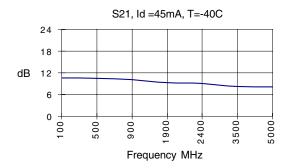
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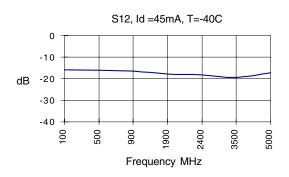
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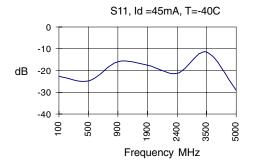
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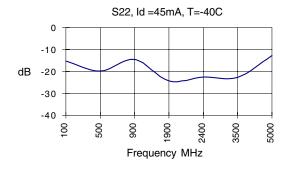


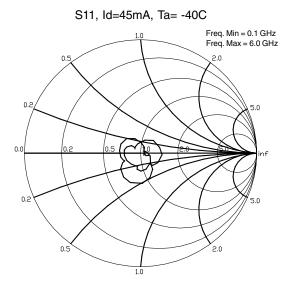


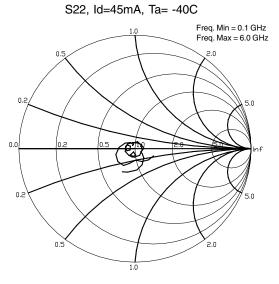








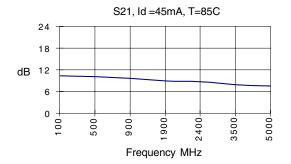


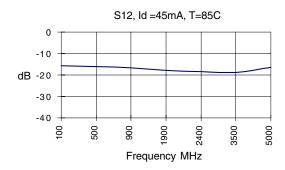


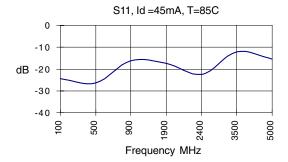
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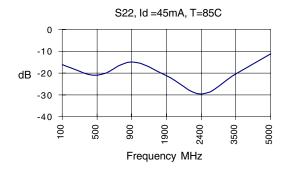


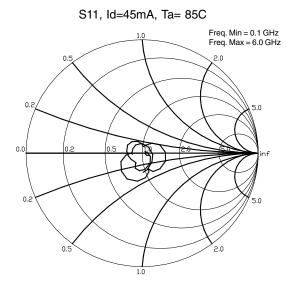


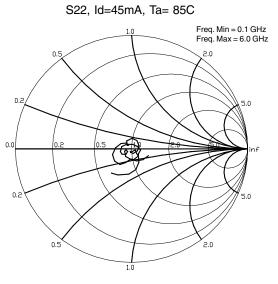












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Absolute Maximum Ratings

Parameter	Value	Unit
Supply Current	90	mA
Operating Temperature	-40 to +85	С
Maximum Input Power	+13	dBm
Storage Temperature Range	-40 to +85	С
Operating Junction Temperature	+150	С

Caution:



Operation of this device above any one of these parameters may cause permanent damage. Appropriate precautions in handling, packaging and testing devices must be observed.

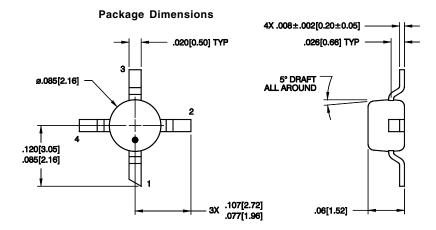
Thermal Resistance (Lead-Junction): 97° C/W

Part Number Ordering Information

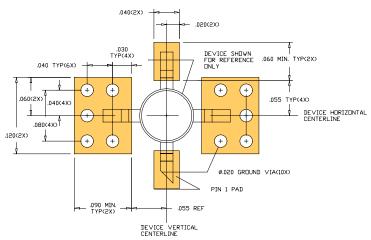
Part Number	Reel Size	Devices/Reel	
SGA-4186-TR1	7"	1000	
SGA-4186-TR2	13"	3000	

Recommended Bias Resistor Values					
Supply Voltage(Vs)	4V	5V	7.5V	9V	12V
Rbias (Ohms)	18	40	96	129	196

Pin Designation 1 RF in 2 GND 3 RF out and Bias 4 GND



PCB Pad Layout



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