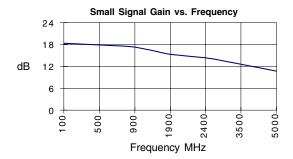




Product Description

Stanford Microdevices' SGA-3386 is a high performance cascadeable 50-ohm amplifier designed for operation at voltages as low as 2.5V. This RFIC uses the latest Silicon Germanium Heterostructure Bipolar Transistor (SiGe HBT) process featuring 1 micron emitters with F_T up to 65 GHz.

This circuit uses a darlington pair topology with resistive feedback for broadband performance as well as stability over its entire temperature range. Internally matched to 50 ohm impedance, the SGA-3386 requires only DC blocking and bypass capacitors for external components.



Electrical Specifications at Ta = 25C

SGA-3386

DC-3600 MHz Silicon Germanium Cascadeable Gain Block



Product Features

- DC-3600 MHz Operation
- Single Voltage Supply
- High Output Intercept: +24.3dBm typ. at 850 MHz
- Low Current Draw: 35mA at 2.5V typ.
- Low Noise Figure: 3.2dB typ. at 850 MHz

Applications

- Oscillator Amplifiers
- PA for Low Power Applications
- IF/ RF Buffer Amplifier
- Drivers for CATV Amplifiers

Symbol	Parameters: Test Conditions: Z ₀ = 50 Ohms, Id = 35 mA, T = 25°C		Units	Min.	Тур.	Max.
P _{1dB}	Output Power at 1dB Compression	f = 850 MHz f = 1950 MHz	dBm dBm		12.3 10.7	
S ₂₁	Small Signal Gain	f = DC - 1000 MHz f = 1000 - 2000 MHz f = 2000 - 3600 MHz	dB dB dB	15.7	17.4 15.3 13.0	
S ₁₂	Reverse Isolation	f = DC - 3600 MHz	dB		20.0	
S ₁₁	Input VSWR	f = DC-2400 MHz f = 2400 - 3600 MHz	-		1.33:1 1.58:1	
S ₂₂	Output VSWR	f = DC - 3600 MHz	-		1.17:1	
IP ₃	Third Order Intercept Point	f = 850 MHz f = 1950 MHz	dBm dBm		24.3 23.8	
NF	Noise Figure	f = DC-1000 MHz f = 1000 - 2400 MHz	dB dB		3.2 3.8	
T _D	Group Delay	f = 1000 MHz	pS		119.0	·
V _D	Device Voltage		V	2.2	2.5	2.8

The information provided herein is believed to be reliable at press time. Stanford Microdevices assumes no responsibility for inaccuracies or omissions. Stanford Microdevices assumes no responsibility for the use of this information, and all such information shall be entirely at the user's own risk. Prices and specifications are subject to change without notice. No patient rights or licenses to any of the circuits described herein are implied or granted to any third party. Stanford Microdevices does not authorize or warrant any Stanford Microdevices product for use in life-support devices and/or systems. Copyright 1999 Stanford Microdevices, Inc. All worldwide rights reserved.



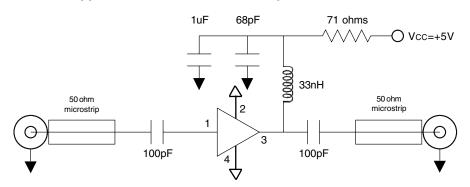


Specification					Test
Parameter	Min	Тур.	Max.	Unit	Condition
Device Bias					T= 25C
Operating Voltage		2.5		V	
Operating Current		35.0		mA	
500 MHz					T= 25C
Gain		17.9		dB	
Noise Figure		3.1		dB	
Output IP3		25.8		dBm	
Output P1dB		12.2		dBm	
Input Return Loss		19.8		dB	
Isolation		23.7		dB	
850 MHz					T= 25C
Gain		17.4		dB	
Noise Figure		3.2		dB	
Output IP3		24.3		dBm	
Output P1dB		12.3		dBm	
Input Return Loss		16.3		dB	
Isolation		21.0		dB	
1950 MHz					T= 25C
Gain		15.3		dB	
Noise Figure		3.7		dB	
Output IP3		23.8		dBm	
Output P1dB		10.7		dBm	
Input Return Loss		15.9		dB	
Isolation		25.6		dB	
2400 MHz					T= 25C
Gain		14.4		dB	
Noise Figure		3.8		dB	
Output IP3		23.6		dBm	
Output P1dB		9.9		dBm	
Input Return Loss		17.4		dB	
Isolation		20.6		dB	

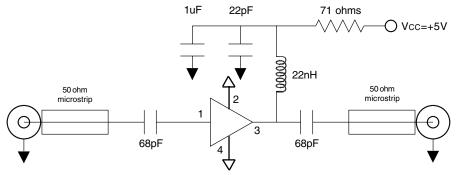


Pin #	Function	Description	Device Schematic
1	RF IN	RF input pin. This pin requires the use of an external DC blocking capacitor chosen for the frequency of operation.	
2	GND	Connection to ground. Use via holes for best performance to reduce lead inductance as close to ground leads as possible.	
3	RF OUT/ BIAS	RF output and bias pin. DC voltage is present on this pin, therefore a DC blocking capacitor is necessary for proper operation.	
4	GND	Sames as Pin 2	

Application Schematic for +5V Operation at 900 MHz



Application Schematic for +5V Operation at 1900 MHz

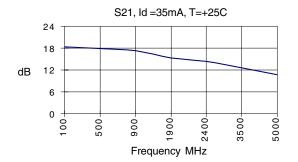


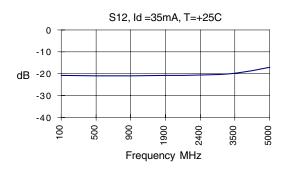
The information provided herein is believed to be reliable at press time. Stanford Microdevices assumes no responsibility for inaccuracies or omissions. Stanford Microdevices assumes no responsibility for the use of this information, and all such information shall be entirely at the user's own risk. Prices and specifications are subject to change without notice. No patent rights or licenses to any of the circuits described herein are implied or granted to any third party. Stanford Microdevices does not authorize or warrant any Stanford Microdevices product for use in life-support devices and/or systems.

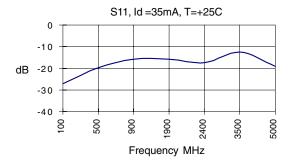
Copyright 1999 Stanford Microdevices, Inc. All worldwide rights reserved.

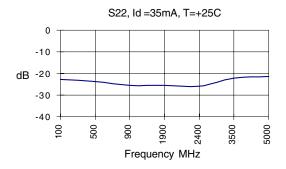


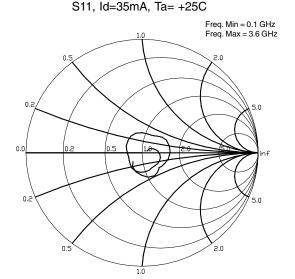


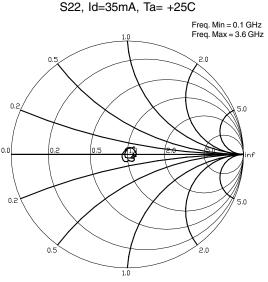










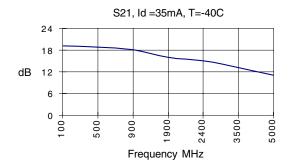


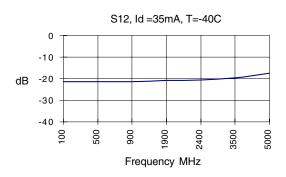
The information provided herein is believed to be reliable at press time. Stanford Microdevices assumes no responsibility for inaccuracies or omissions. Stanford Microdevices assumes no responsibility for the use of this information, and all such information shall be entirely at the user's own risk. Prices and specifications are subject to change without notice. No patent rights or licenses to any of the circuits described herein are implied or granted to any third party. Stanford Microdevices does not authorize or warrant any Stanford Microdevices product for use in life-support devices and/or systems.

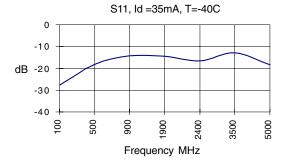
Copyright 1999 Stanford Microdevices, Inc. All worldwide rights reserved.

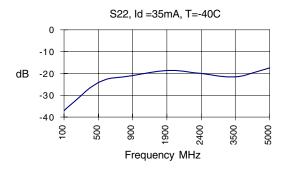


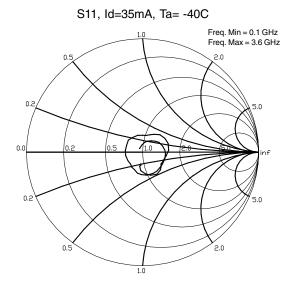


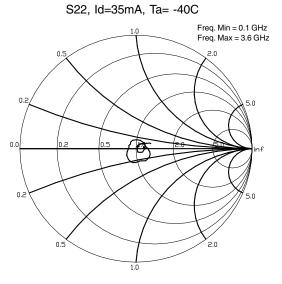










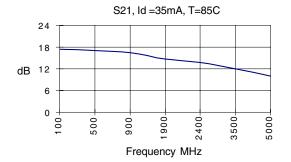


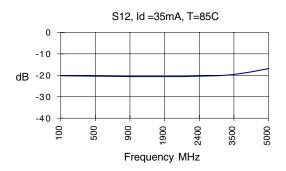
The information provided herein is believed to be reliable at press time. Stanford Microdevices assumes no responsibility for inaccuracies or omissions. Stanford Microdevices assumes no responsibility for the use of this information, and all such information shall be entirely at the user's own risk. Prices and specifications are subject to change without notice. No patent rights or licenses to any of the circuits described herein are implied or granted to any third party. Stanford Microdevices does not authorize or warrant any Stanford Microdevices product for use in life-support devices and/or systems.

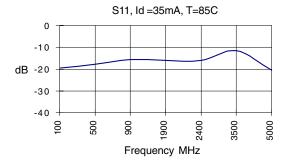
Copyright 1999 Stanford Microdevices, Inc. All worldwide rights reserved.

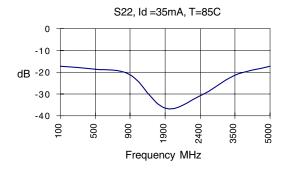


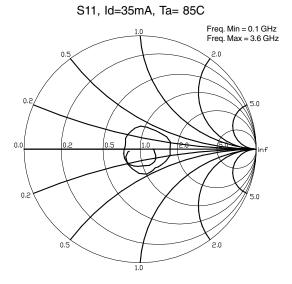


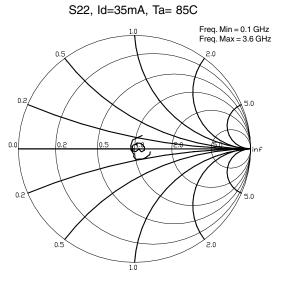












The information provided herein is believed to be reliable at press time. Stanford Microdevices assumes no responsibility for inaccuracies or omissions. Stanford Microdevices assumes no responsibility for the use of this information, and all such information shall be entirely at the user's own risk. Prices and specifications are subject to change without notice. No patent rights or licenses to any of the circuits described herein are implied or granted to any third party. Stanford Microdevices does not authorize or warrant any Stanford Microdevices product for use in life-support devices and/or systems.





Absolute Maximum Ratings

Parameter	Value	Unit
Supply Current	70	mA
Operating Temperature	-40 to +85	С
Maximum Input Power	+7	dBm
Storage Temperature Range	-40 to +85	С
Operating Junction Temperature	+150	С

Caution:



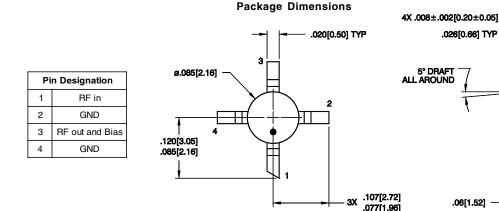
Operation of this device above any one of these parameters may cause permanent damage. Appropriate precautions in handling, packaging and testing devices must be observed.

Thermal Resistance (Lead-Junction): 97° C/W

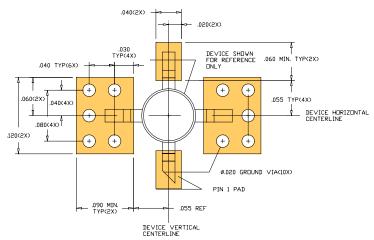
Part Number Ordering Information

Part Number	Reel Size	Devices/Reel
SGA-3386-TR1	7"	1000
SGA-3386-TR2	13"	3000

Recommended Bias Resistor Values					
Supply Voltage(Vs)	4V	5V	7.5V	9V	12V
Rbias (Ohms)	31	60	131	174	260



PCB Pad Layout



The information provided herein is believed to be reliable at press time. Stanford Microdevices assumes no responsibility for inaccuracies or omissions. Stanford Microdevices assumes no responsibility for the use of this information, and all such information shall be entirely at the user's own risk. Prices and specifications are subject to change without notice. No patent rights or licenses to any of the circuits described herein are implied or granted to any third party. Stanford Microdevices does not authorize or warrant any Stanford Microdevices product for use in life-support devices and/or systems.

Copyright 1999 Stanford Microdevices, Inc. All worldwide rights reserved.