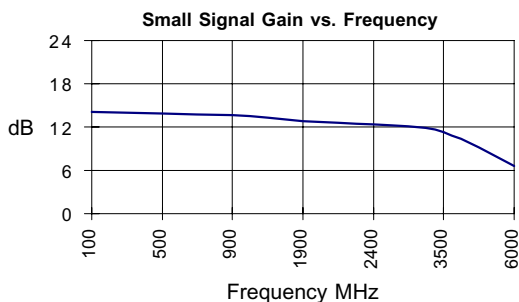


## Product Description

Stanford Microdevices' SGA-4263 is a high performance cascadeable 50-ohm amplifier designed for operation at voltages as low as 3.2V. This RFIC uses the latest Silicon Germanium Heterostructure Bipolar Transistor (SiGe HBT) process featuring 1 micron emitters with  $F_T$  up to 50 GHz.

This circuit uses a darlington pair topology with resistive feedback for broadband performance as well as stability over its entire temperature range. Internally matched to 50 ohm impedance, the SGA-4263 requires only DC blocking and bypass capacitors for external components.



**Preliminary**

## SGA-4263

### DC-3500 MHz Silicon Germanium HBT Cascadeable Gain Block



### Product Features

- DC-3500 MHz Operation
- Single Voltage Supply
- Low Current Draw: 45mA at 3.2V typ.
- High Output Intercept: +29.3dBm typ. at 850 MHz

### Applications

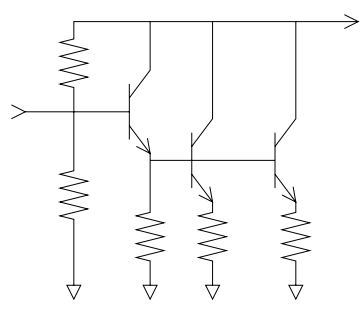
- Oscillator Amplifiers
- IF/ RF Buffer Amplifier
- Drivers for CATV Amplifiers
- PA for Low Power Applications
- Broadband Applications

Symbol	Parameters: Test Conditions: $Z_0 = 50 \text{ Ohms}$ , $I_d = 45 \text{ mA}$ , $T = 25^\circ\text{C}$		Units	Min.	Typ.	Max.
$P_{1dB}$	Output Power at 1dB Compression	$f = 850 \text{ MHz}$ $f = 1950 \text{ MHz}$	dBm dBm		14.2 12.5	
$S_{21}$	Small Signal Gain	$f = \text{DC} - 1000 \text{ MHz}$ $f = 1000 - 2000 \text{ MHz}$ $f = 2000 - 3500 \text{ MHz}$	dB dB dB	12.4	13.8 13.1 12.0	
$S_{12}$	Reverse Isolation	$f = \text{DC} - 1000 \text{ MHz}$ $f = 1000 - 2000 \text{ MHz}$ $f = 2000 - 3500 \text{ MHz}$	dB dB dB		18.0 18.9 18.7	
$S_{11}$	Input VSWR	$f = \text{DC} - 2000 \text{ MHz}$ $f = 2000 - 3500 \text{ MHz}$	-		1.1 1.2	
$S_{22}$	Output VSWR	$f = \text{DC} - 2000 \text{ MHz}$ $f = 2000 - 3500 \text{ MHz}$	-		1.3 1.6	
$IP_3$	Third Order Intercept Point Power out per Tone = 0 dBm	$f = 850 \text{ MHz}$ $f = 1950 \text{ MHz}$	dBm dBm		29.3 25.7	
NF	Noise Figure	$f = \text{DC} - 1000 \text{ MHz}$ $f = 1000 - 2400 \text{ MHz}$	dB dB		3.5 3.8	
$T_D$	Group Delay	$f = 1000 \text{ MHz}$	pS		129	
$V_D$	Device Voltage		V	2.9	3.2	3.5

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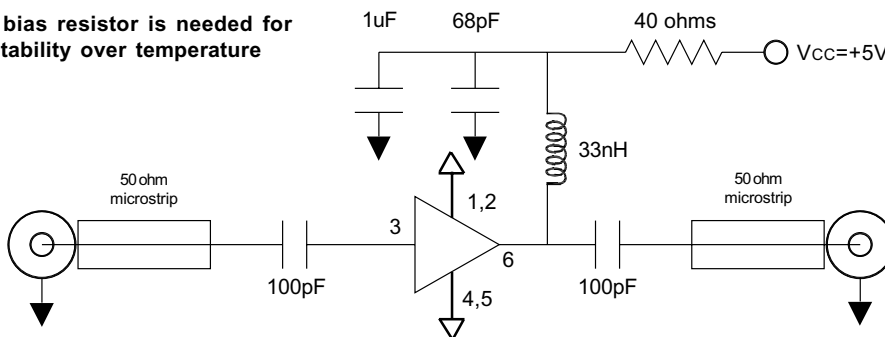
Parameter	Specification			Unit	Test Condition
	Min	Typ.	Max.		
<b>Bandwidth</b> Frequency Range	DC		3500	MHz	T= 25C
<b>Device Bias</b> Operating Voltage Operating Current		3.2 45		V mA	T= 25C
<b>500 MHz</b> Gain Noise Figure Output IP3 Output P1dB Input Return Loss Isolation		13.9 3.6 29.5 14.1 29.3 18.3		dB dB dBm dBm dB dB	T= 25C
<b>850 MHz</b> Gain Noise Figure Output IP3 Output P1dB Input Return Loss Isolation		13.7 3.4 29.3 14.2 29.1 18.5		dB dB dBm dBm dB dB	T= 25C
<b>1950 MHz</b> Gain Noise Figure Output IP3 Output P1dB Input Return Loss Isolation		12.7 3.7 25.7 12.5 27.8 19.0		dB dB dBm dBm dB dB	T= 25C
<b>2400 MHz</b> Gain Noise Figure Output IP3 Output P1dB Input Return Loss Isolation		12.4 4.3 23.6 11.3 22.9 19.0		dB dB dBm dBm dB dB	T= 25C

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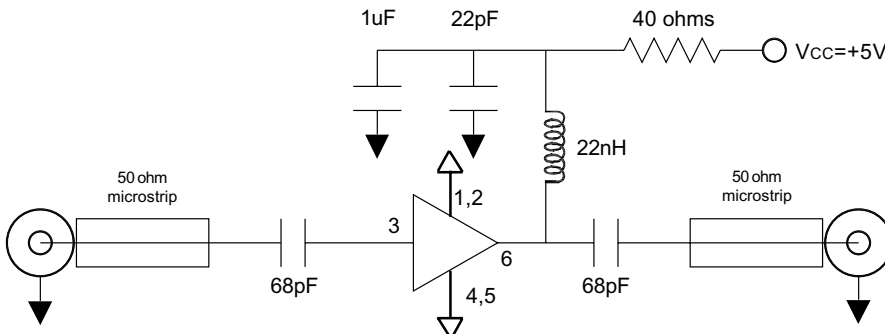
Pin #	Function	Description	Device Schematic
1	GND	Connection to ground. Use via holes for best performance to reduce lead inductance as close to ground leads as possible.	
2	GND	Sames as Pin 1	
3	RF IN	RF input pin. This pin requires the use of an external DC blocking capacitor chosen for the frequency of operation.	
4	GND	Sames as Pin 1	
5	GND	Sames as Pin 1	
6	RF OUT	RF output and bias pin. DC voltage is present on this pin, therefore a DC blocking capacitor is necessary for proper operation.	

### Application Schematic for +5V Operation at 900 MHz

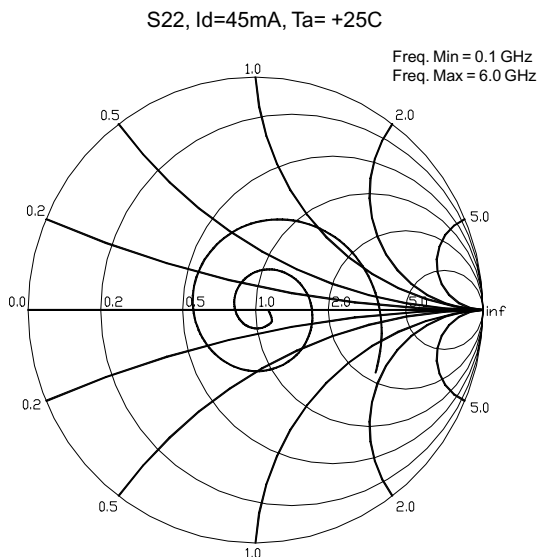
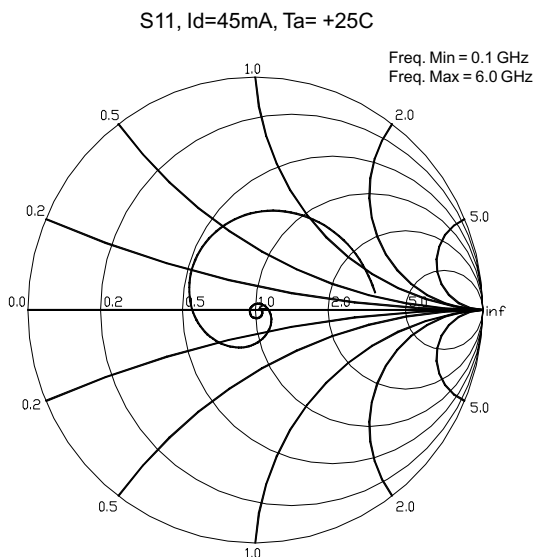
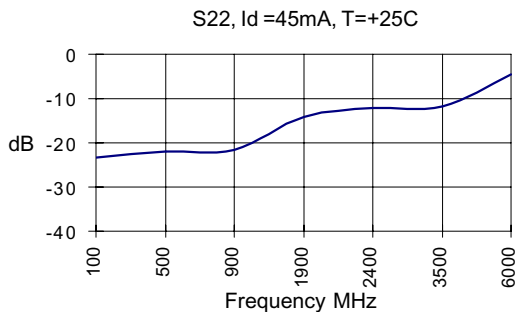
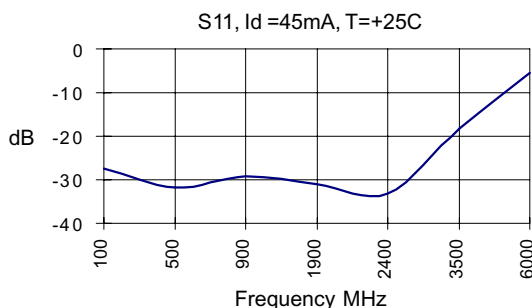
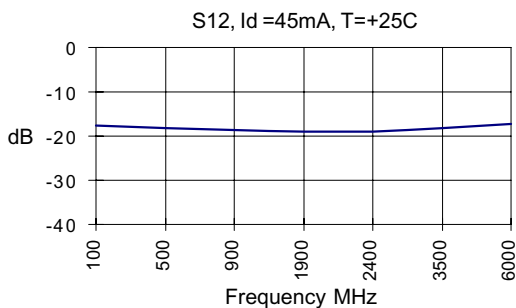
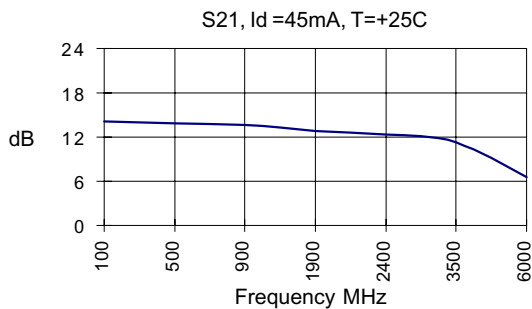
**Note: A bias resistor is needed for stability over temperature**



### Application Schematic for +5V Operation at 1900 MHz

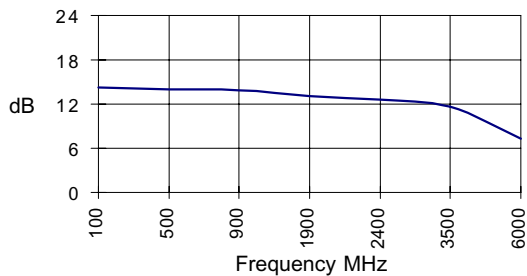


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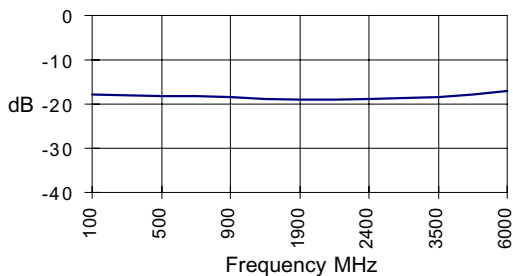


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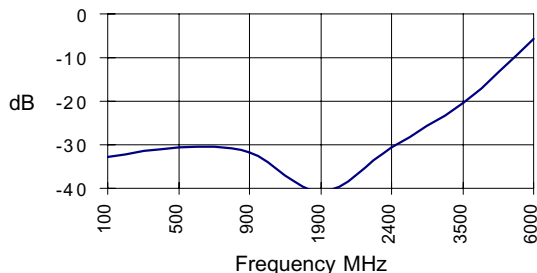
S21, Id =45mA, T=-40C



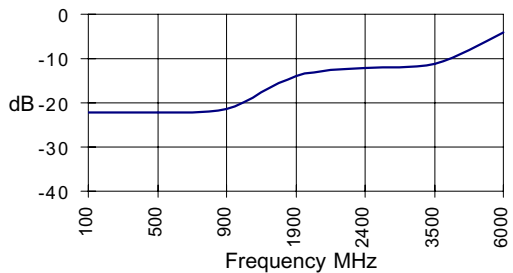
S12, Id =45mA, T=-40C



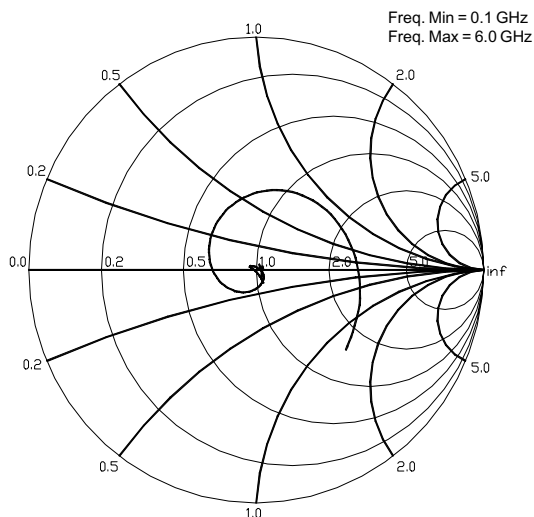
S11, Id =45mA, T=-40C



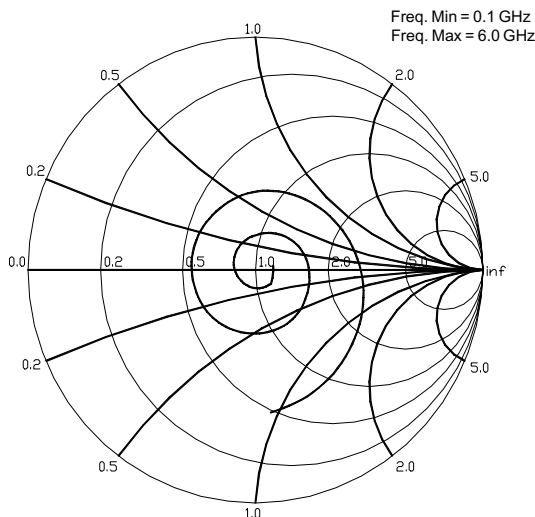
S22, Id =45mA, T=-40C



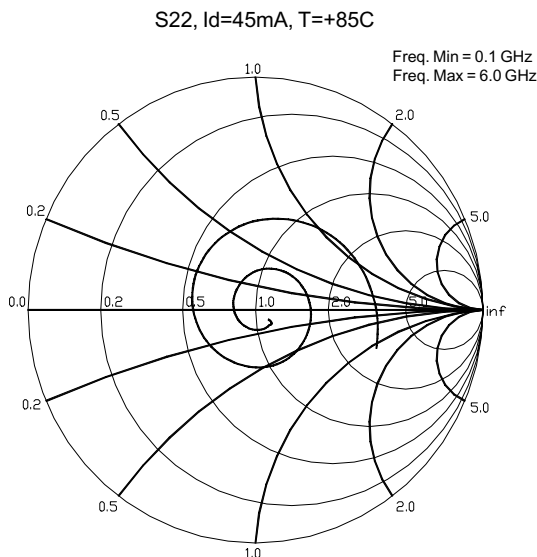
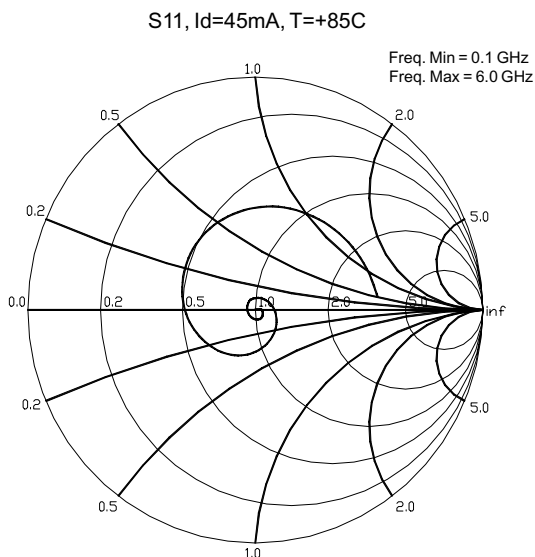
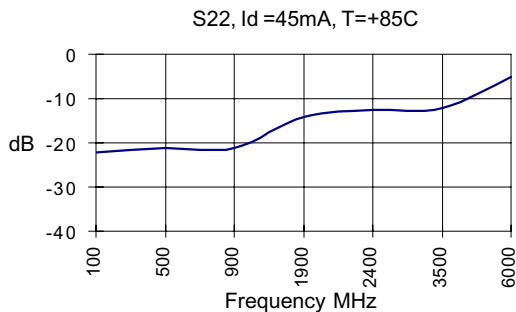
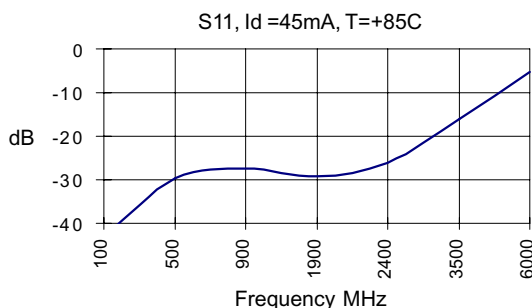
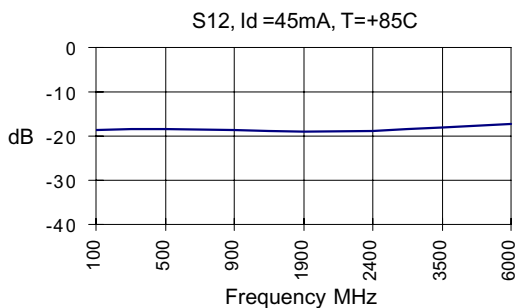
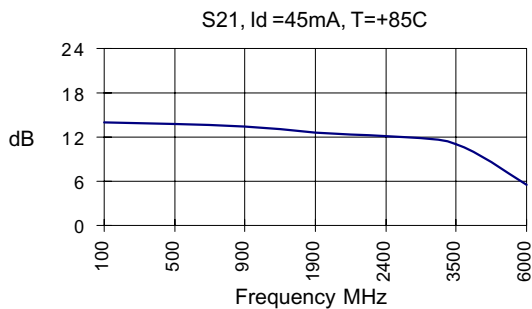
S11, Id=45mA, T=-40C



S22, Id=45mA, T=-40C



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### Absolute Maximum Ratings

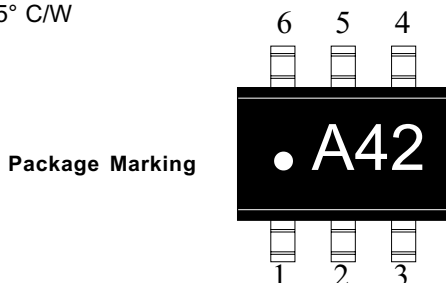
Parameter	Value	Unit
Supply Current	90	mA
Operating Temperature	-40 to +85	C
Maximum Input Power	+9	dBm
Storage Temperature Range	-40 to +85	C
Operating Junction Temperature	+125	C

### Caution:



Operation of this device above any one of these parameters may cause permanent damage. Appropriate precautions in handling, packaging and testing devices must be observed.

Thermal Resistance (Lead-Junction):  
255° C/W



### Part Number Ordering Information

Part Number	Reel Size	Devices/Reel
SGA-4263-TR1	7"	3000

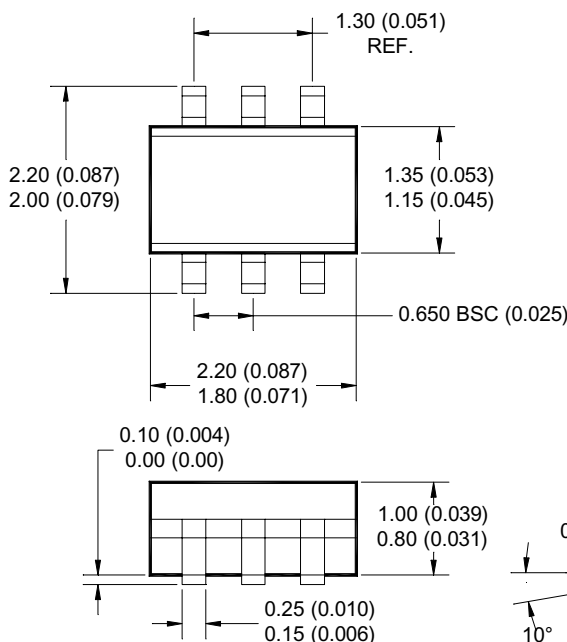
### Recommended Bias Resistor Values

Supply Voltage(Vs)	4V	5V	7.5V	9V	12V
Rbias (Ohms)	18	40	96	129	196

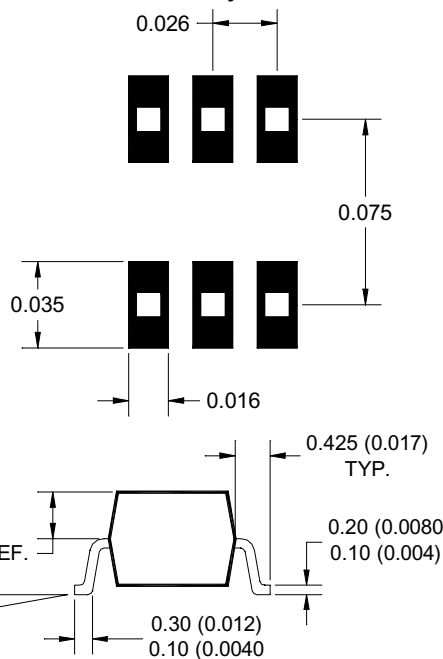
Pin Designation	
1	GND
2	GND
3	RF in
4	GND
5	GND
6	RF out

Note: Pin 1 is on lower left when you can read package marking

### Package Dimensions



### Pad Layout



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