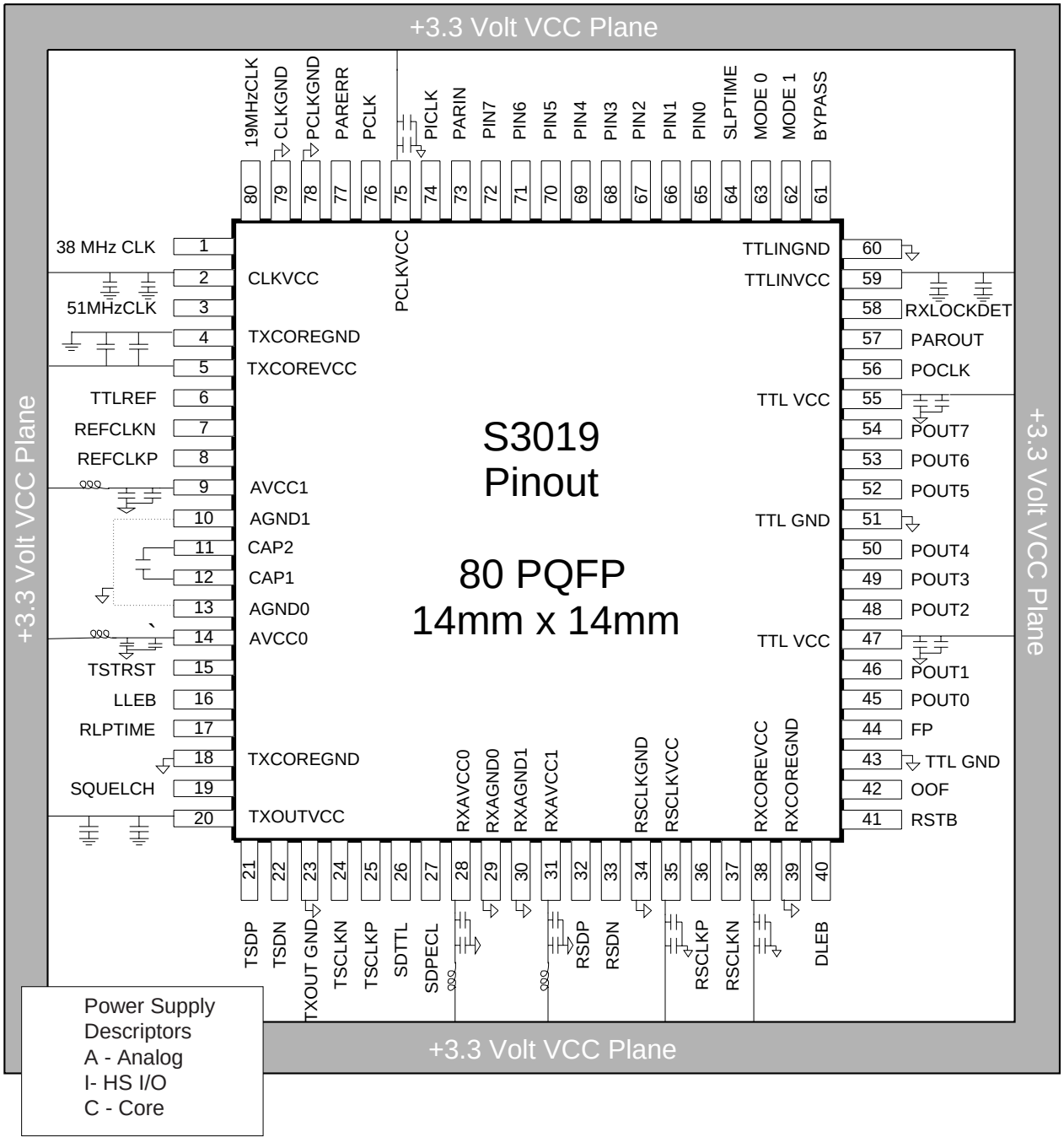
SONET/SDH/ATM S3019 EXAMPLE

The S3019 Transceiver includes the transmit Clock Synthesis PLL and clock recovery. The CAP1/CAP2 capacitor should be 0.01µF for the S3019. Figure 1 illustrates the connections for the S3019 transceiver device. The ground ring is shown around the loop filter capacitor. The ring should be completed under the device connecting pins 11 and 12. Please note that the ring should be directly connected to the ground plane to avoid current through the ground ring. The values of the decoupling components are 0.1µF paralleled with 100pf, X7R dielectric, EIA sizes 1206 and 805. Ferrite Bead Inductors are Murata BLM 31B601S or equivalent. All grounds must be tied directly to ground plane. (Note: Do not daisy chain grounds together.)

Figure 1



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