

ISDN S-INTERFACE TRANSFORMER

P/N:S5020 DATA SHEET

A. Electrical Specifications @25° C

- Turns Ratio($\pm 1\%$)(15KHz/0.1Vrms) : PIN9-7:2-17=2.5:1
PIN10-12:4-15=2.5:1
PIN1-2:18-17=5-4:14-15=1:1
- DCR : PIN 2-17=4-15=2.5 Ω Max
PIN 9-7=10-12=5.6 Ω Max
PIN 1-2=17-18=4-5=14-15=1.1 Ω Max
- Inductance @10KHz, 0.1Vrms : PIN 2-17=4-15=30 mH Min
PIN 1-2=17-18=4-5=14-15=5 mH +50/-30%
- Leakage Inductance @100KHz, 0.1Vrms(PIN 9-7, 10-12 Short): PIN 2-17=4-15=5 μ H Max
- Choke Leakage Inductance @100KHz, 0.1Vrms(17-18, 14-15 Short) : PIN 1-2=4-5=0.6 μ H Max
- Interwinding Capacitance @10KHz, 0.1Vrms PIN1-9=5-10=150 pF Max
- HI-POT : 1500Vrms/1mA 2Sec.PIN2-9, 4-10
500Vrms/1mA 2Sec.PIN3-16(1-9, 5-10 Short)

B. Schematics & Mechanical :

