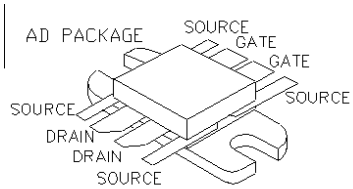
polyfet rf devices

SD702

General Description

Silicon VDMOS and LDMOS transistors designed specifically for broadband RF applications. Suitable for Militry Radios, Cellular and Paging Amplifier Base Stations, Broadcast FM/AM, MRI, Laser Driver and others.

"Polyfet"™ process features low feedback and output capacitances resulting in high F_t transistors with high input impedance and high efficiency.



SILICON GATE ENHANCEMENT MODE

RF POWER VDMOS TRANSISTOR

90.0 Watts Push - Pull

Package Style AD

HIGH EFFICIENCY, LINEAR
HIGH GAIN, LOW NOISE

ABSOLUTE MAXIMUM RATINGS (T = 25 °C)

Total Device Dissipation	Junction to Case Thermal Resistance	Maximum Junction Temperature	Storage Temperature	DC Drain Current	Drain to Gate Voltage	Drain to Source Voltage	Gate to Source Voltage
190 Watts	0.85 °C/W	200 °C	-65 °C to 150 °C	11.5 A	70V	70V	20 V

RF CHARACTERISTICS (90.0 WATTS OUTPUT)

SYMBOL	PARAMETER	MIN	TYP	MAX	UNITS	TEST CONDITIONS
Gps	Common Source Power Gain	10			dB	Idq = 0.60 A, Vds = 28.0 V, F = 500 MHz
η	Drain Efficiency		55		%	Idq = 0.60 A, Vds = 28.0 V, F = 500 MHz
VSWR	Load Mismatch Tolerance			20:1	Relative	Idq = 0.60 A, Vds = 28.0 V, F = 500 MHz

ELECTRICAL CHARACTERISTICS (EACH SIDE)

SYMBOL	PARAMETER	MIN	TYP	MAX	UNITS	TEST CONDITIONS
Bvdss	Drain Breakdown Voltage	65			V	Ids = 40.00 mA, Vgs = 0V
Idss	Zero Bias Drain Current			2.0	mA	Vds = 28.0 V, Vgs = 0V
Igss	Gate Leakage Current			1	uA	Vds = 0V Vgs = 30V
Vgs	Gate Bias for Drain Current	1		7	V	Ids = 0.20 A, Vgs = Vds
gM	Forward Transconductance		2.4		Mho	Vds = 10V, Vgs = 5V
Rdson	Saturation Resistance		0.50		Ohm	Vgs = 20V, Ids = 5.00 A
Idsat	Saturation Current		14.00		Amp	Vgs = 20V, Vds = 10V
Ciss	Common Source Input Capacitance		100.0		pF	Vds = 28.0 Vgs = 0V, F = 1 MHz
Crss	Common Source Feedback Capacitance		6.0		pF	Vds = 28.0 Vgs = 0V, F = 1 MHz
Coss	Common Source Output Capacitance		64.0		pF	Vds = 28.0 Vgs = 0V, F = 1 MHz

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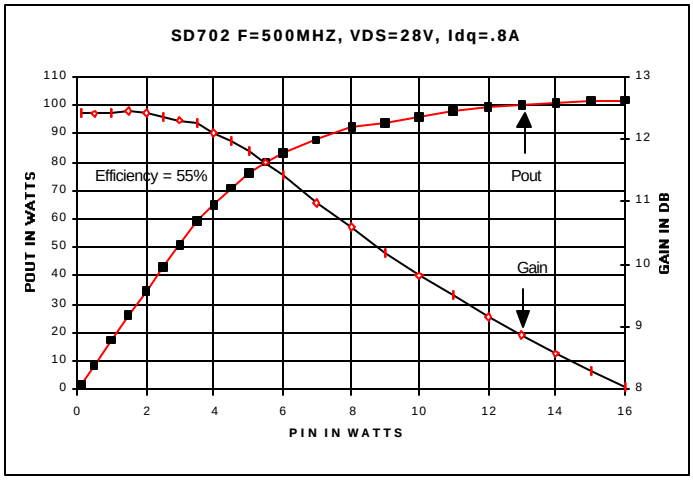
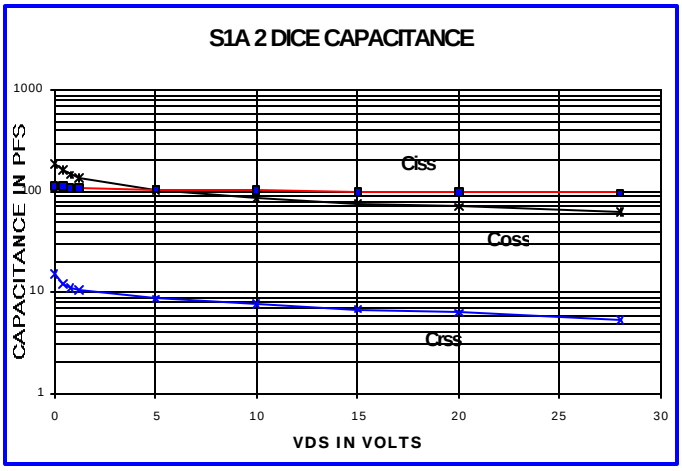
SD702

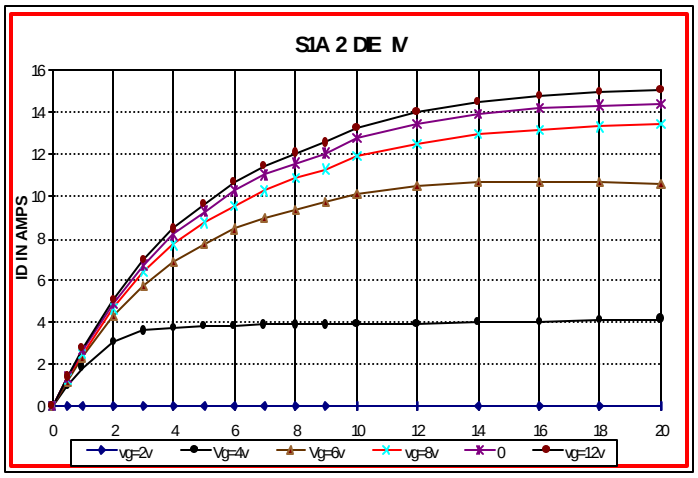
POUT VS PIN GRAPH



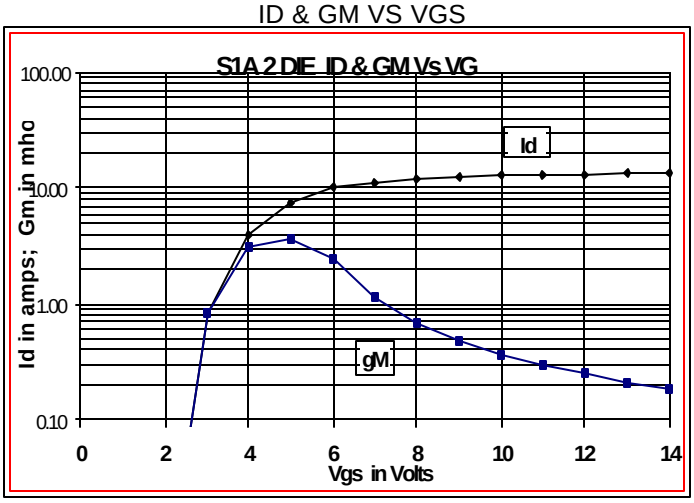
CAPACITANCE VS VOLTAGE



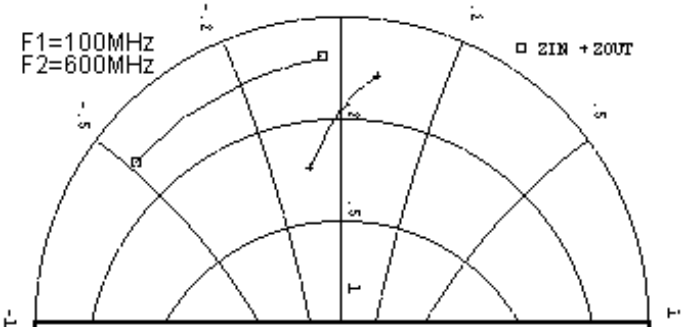
IV CURVE



ID & GM VS VGS

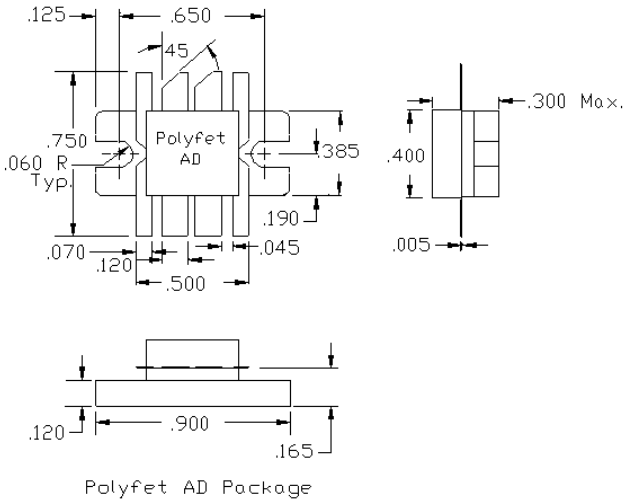


Zin Zout



SD702 Vdd=28V Idq=800mA Pout=90W						
Freq(MHz)	100	200	300	400	500	600
Zin	6.0	5.0	4.6	4.2	3.8	3.2
	-j25.6	-j16.2	-j11.0	-j6.0	-j2.2	-j1.2
Zout	17.6	11	7.4	6.2	5.4	5
	-j6.0	-j3.8	-j1.8	-j0.4	j2.0	j3.0

PACKAGE DIMENSIONS IN INCHES



Polyfet AD Package

Tolerance .XX +/-0.01 .XXX +/-.005 inches

POLYFET RF DEVICES

REVISION 03/28/2001

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