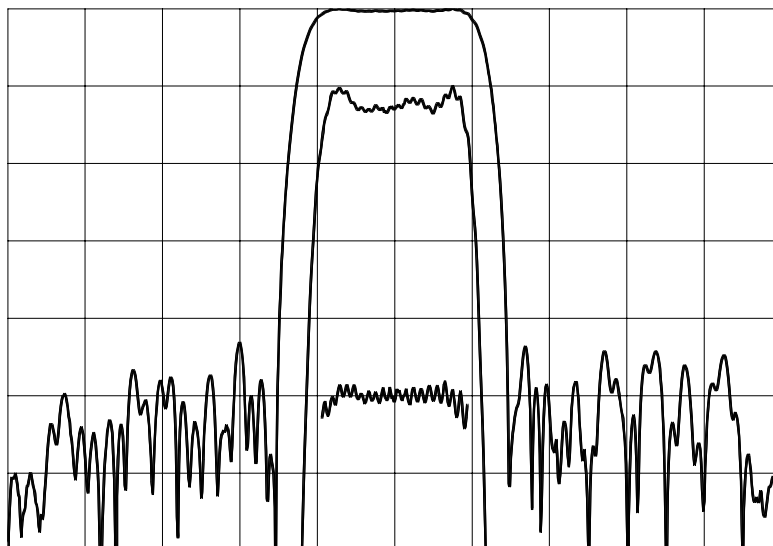


TYPICAL PERFORMANCE



Horizontal: 8 MHz/div

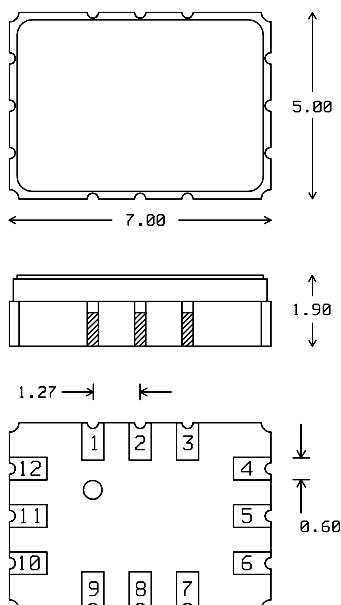
Vertical (from top): Magnitude 10 dB/div
Magnitude 1 dB/div
Group Delay 50 nsec/div

SPECIFICATION

Parameter	Min	Typ	Max	Units
Center Frequency (F_c) ¹		326.4		MHz
Insertion Loss		16	19	dB
2dB Bandwidth	15	17		MHz
40 dB Bandwidth		23.4	25	MHz
Rejection (10 MHz to 50 MHz)	40	45		dB
Rejection (50 MHz to F_c -50 MHz)	50	55		dB
Rejection (F_c -50 MHz to F_c -12.5 MHz)	40	43		
Rejection (F_c +12.5 MHz to F_c +50 MHz)	40	43		
Rejection (F_c +50 MHz to 450 MHz)	50	53		dB
Passband Amplitude Ripple ³		0.3	0.8	dB p-p
Passband Group Delay Ripple ²		30	50	ns p-p
Operating Temperature Range		23		°C
Source and Load Impedance		50		Ω

Notes: 1. Fixed reference. All specified bandwidths are centered at this frequency.
2. Over central 5 MHz.
3. Over central 15 MHz.

PACKAGE OUTLINE

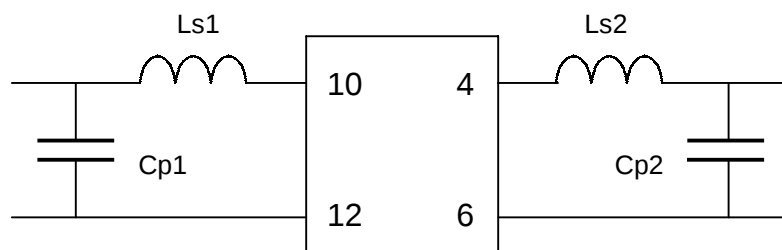


Units: mm

Pin Configuration:

Input: 10
Input Return: 12
Output: 4
Output Return: 6
Ground: 1,2,3,5,7,8,9,11

MATCHING CIRCUIT



Typical component values:
(minimum inductor Q = 45)

Ls1 = 24 nH
Cp1 = 20 pF

Ls2 = 22 nH
Cp2 = 22 pF

- Notes
- Requires 2% matching components.
 - Component values may change depending on board layout.
 - All pins other than 4,6,10 & 12 are connected together internally to the package

ISO 9001
Registered

Micro Networks Corporation

Worldwide Headquarters: 324 Clark Street Worcester, MA 01606. tel: 508-852-5400 fax: 508-852-8456
European Sales Headquarters: Hertosingel 20 6214 AD Maastricht, Netherlands. tel: +31-43-32-70915 fax: +31-43-32-70715
www.micronetworks.com