



PRELIMINARY

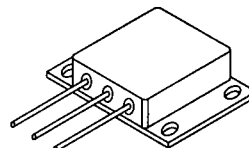
SOLID STATE DEVICES, INC

14849 Firestone Boulevard • La Mirada, CA 90638
Phone: (714) 670-SSDI (7734) • Fax: (714) 522-7424

Designer's Data Sheet

FEATURES:

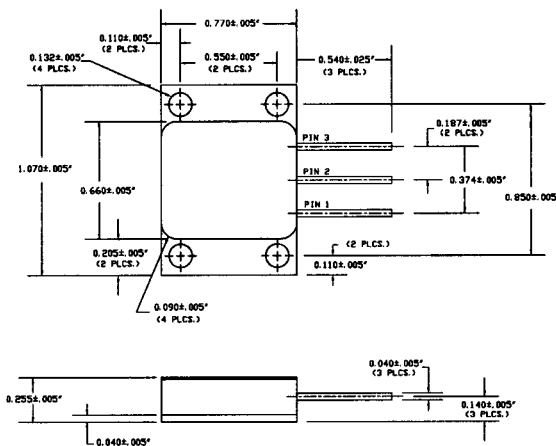
- Rugged construction with poly silicon gate
- Low RDS(on) and high transconductance
- Excellent high temperature stability
- Very fast switching speed
- Fast recovery and superior dv/dt performance
- Increased reverse energy capability
- Low input and transfer capacitance for easy paralleling
- Hermetically sealed surface mount power package
- TX, TXV and Space Level screening available
- Replaces: IRF340 Types

SFF100N10M
SFF100N10Z100 AMP
100 VOLTS
0.015Ω
N-CHANNEL
POWER MOSFET

MAXIMUM RATINGS

CHARACTERISTIC	SYMBOL	VALUE	UNIT
Drain to Source Voltage	V _{DS}	100	Volts
Gate to Source Voltage	V _{GS}	±20	Volts
Continuous Drain Current	I _D	100	Amps
Operating and Storage Temperature	T _{OP} & T _{STG}	-55 to +150	°C
Thermal Resistance, Junction to Case	R _{θJC}	0.42	°C/W
Total Device Dissipation @ TC=25°C Total Device Dissipation @ TC=55°C	P _D	300 200	Watts
Repetitive Avalanche Energy	EAS	30	mJ

PACKAGE OUTLINE: 3 PIN POWER PACK



NOTE: All specifications are subject to change without notification. SCD's for these devices should be reviewed by SSDI prior to release.

DATA SHEET #: F00309 A

SFF100N10M
SFF100N10Z

PRELIMINARY



SOLID STATE DEVICES, INC

14849 Firestone Boulevard · La Mirada, CA 90638
Phone: (714) 670-SSDI (7734) · Fax: (714) 522-7424

ELECTRICAL CHARACTERISTICS @ T_J=25°C (Unless Otherwise Specified)

RATING		SYMBOL	MIN	TYP	MAX	UNIT
Drain to Source Breakdown Voltage (V _{GS} =0 V, I _D =250μA)		BV_{DSS}	100	---	---	V
Drain to Source on State Resistance (V _{GS} =10 V, I _D =50% Rated ID)		R_{DS(on)}	---	---	0.015	Ω
On State Drain Current (V _{DS} >I _{D(on)} X R _{DS(on)} Max, V _{GS} =10 V)		I_{D(on)}	100	---	---	V/°C
Gate Threshold Voltage (V _{DS} ≥V _{GS} , I _D =4mA)		V_{GS(th)}	2.0	---	4.0	V
Forward Transconductance (V _{DS} >I _{D(on)} X R _{DS(on)} Max, I _{DS} =50% rated ID)		g_{fs}	30	49	---	S(Ω)
Zero Gate Voltage Drain Current (V _{DS} =max rated voltage, V _{GS} =0 V) (V _{DS} =80% rated V _{DS} , V _{GS} =0 V, T _A =125°C)		I_{DSS}	---	---	10 200	μA
Gate to Source Leakage Forward Gate to Source Leakage Reverse	At rated V _{GS}	I_{GSS}	---	---	+200 -200	nA
Total Gate Charge Gate to Source Charge Gate to Drain Charge	V _{GS} =10 Volts 50% rated V _{DS} 50% rated I _D	Q_g Q_{gs} Q_{gd}	---	380 45 150	---	nC
Turn on Delay Time Rise Time Turn Off Delay Time Fall Time	V _{DD} = 50% rated V _{DS} 50% rated I _D R _G =6.2 Ω V _{GS} =10V	t_{d(on)} t_r t_{d(off)} t_f	---	48 490 190 380	196 980 380 770	nsec
Diode Forward Voltage (I _S =rated I _D , V _{GS} =0 V, T _J =25°C)		V_{SD}	---	---	1.6	V
Diode Reverse Recovery Time Reverse Recovery Charge	T _J =25°C I _F =10A di/dt=100 A/μsec	t_{rr} Q_{RR}	---	---	200 ---	nsec μC
Input Capacitance Output Capacitance Reverse Transfer Capacitance	V _{GS} =0 Volts V _{DS} =25 Volts f= 1 MHz	C_{iss} C_{oss} C_{rss}	---	7600 3300 1200	10640 4630 2400	pF

For thermal derating curves and other characteristic curves please contact SSDI Marketing Department.