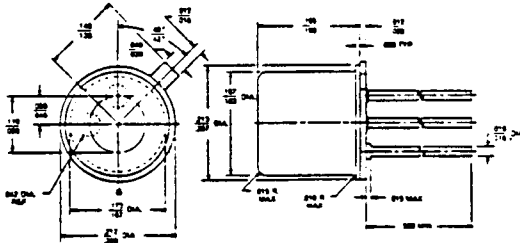


X00065

SFS1826 THROUGH SFS1829 1.6 AMP SILICON CONTROLLED RECTIFIER 200 - 400 VOLTS	SSDI 14849 FIRESTONE BL. LA MIRADA, CA 90638 (213) 921-9660
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CASE STYLE T0-18

FEATURES



- ▶ LOW-LEVEL GATE CHARACTERISTICS
- ▶ IGT = 200 μ A (MAX) @ 25C
- ▶ LOW HOLDING CURRENT I_H = 1mA MAX @ 25
- ▶ ANODE COMMON TO CASE
- ▶ HERMETICALLY SEALED

MAXIMUM RATINGS (T_J = 125 °C unless otherwise noted, RGK=10000)

RATING	SYMBOL	VALUE	UNIT
Peak Repetitive Forward and Reverse Blocking Voltage SFS1826 SFS1827 SFS1828 SFS1829	VDRM and VRRM	200 250 300 400	Volts
Non-repetitive Peak Reverse Blocking Voltage (t $\leq$ 5.0ms) SFS1826 SFS1827 SFS1828 SFS1829	VRSM	300 350 400 500	Volts
RMS On-State Current (All Conduction Angles)	IT(RMS)	1.6	Amps
Average On-State Current TC = 50 °C TA = 25 °C	IT(AV)	1.0 0.7	Amps
Peak Non-Repetitive Surge Current (One Cycle, 60Hz, TC = 80 °C)	ITSM	15	Amps
Peak Gate Power	PGM	0.1	Watt
Average Gate Power	PG(AV)	0.01	Watt
Peak Gate Current	IGM	0.1	Amps
Peak Gate Voltage	VGM	6.0	Volts
Operating Junction Temperature Range	TJ	-65 to +200	°C
Storage Temperature Range	Tstg	-65 to +200	°C
Thermal Resistance Junction to Case	θ JC	72	°C/W

ELECTRICAL CHARACTERISTICS (TC = 25 °C, RGK = 1000 Ω)

CHARACTERISTIC	SYMBOL	MIN	MAX	UNITS
Peak Reverse Blocking Current (Rated VRRM)	IRMO	-	1	μ A
Peak Forward Blocking Current (Rated VDRM) RGK = 1K Ω	IFMR	-	1	μ A
Peak On-State Voltage (IF = 1.6A Peak)	VF	-	1.3	Volts
Gate Trigger Current (Note 1) (VD = 6.0Vdc, RL = 100 Ω) (VD = 6.0Vdc, RL = 100 Ω , TC = -65 °C)	IGT	- -	200 350	μ A
Gate Trigger Voltage (VD = 6.0Vdc, RL = 100 Ω) (VD = 6.0Vdc, RL = 100 Ω , TC = -65 °C)	VGT	-	0.7 0.9	Volts
Holding Current (VD = 6.0Vdc) (VD = 6.0Vdc, TC = -65 °C)	IH	- -	0.3 2.0	mA

- Notes: 1. RGK current is not included in measurement.
 2. Thyristor devices shall not be tested with a constant current source for forward and reverse blocking capability such that the voltage applied exceeds the rated blocking voltage.
 3. Thyristor devices shall not have a positive bias applied to the gate concurrently with a negative potential applied to the anode.

CURRENT DERATING

Figure 1: CASE TEMPERATURE

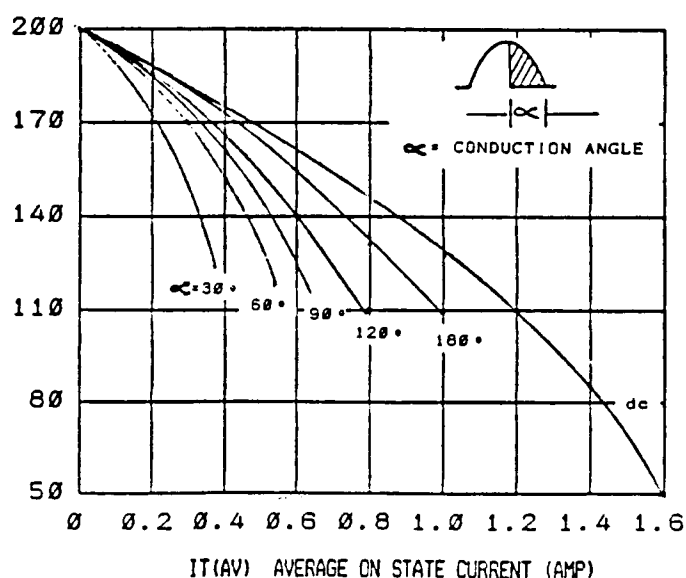
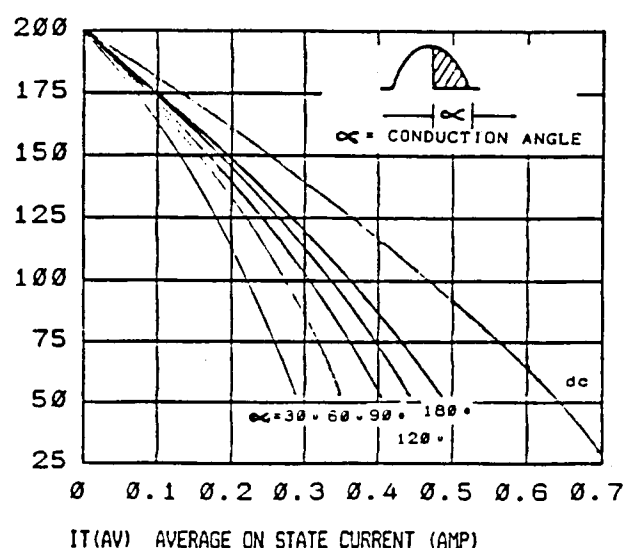


Figure 2: AMBIENT TEMPERATURE



SSDI

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