

**SDR1PSMS
thru
SDR1WSMS**

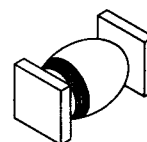
Designer's Data Sheet

FEATURES:

- Ultra Fast Recovery: 65 nsec Maximum
- Single Chip Construction
- PIV to 1800 Volts
- Low Reverse Leakage Current
- Hermetically Sealed Surface Mount package
- For High Efficiency Applications
- Available in axial lead versions
- Metallurgically Bonded
- TX, TXV and Space Level Screening Available

**0.75 AMP
1300-1800 VOLTS
65 nsec
ULTRA FAST
RECTIFIER**

**SURFACE MOUNT
SQUARE TAB**



RATING	SYMBOL	VALUE	UNIT
Peak Repetitive Reverse and DC Blocking Voltage SDR1PSMS SDR1RSMS SDR1SSMS SDR1TSMS SDR1VSMS SDR1WSMS	VRRM VRWM VR	1300 1400 1500 1600 1700 1800	Volts
Average Rectified Forward Current (Resistive Load, 60Hz, Sine Wave, TA=25°C)	IO	0.75	Amps
Peak Surge Current (8.3 ms Pulse, Half Sine Wave Superimposed on IO, allow junction to reach equilibrium between pulses, TA=25°C)	IFSM	20	Amps
Operating and storage temperature	Top & Tstg	-65 to +175	°C
Maximum Thermal Resistance Junction to End Tab	RθJE	28	°C/W

NOTE: All specifications are subject to change without notification.
SCD's for these devices should be reviewed by SSDI prior to release.

DATA SHEET #: RU0093 B

RMD

SDR1PSMS thru SDR1WSMS

PRELIMINARY



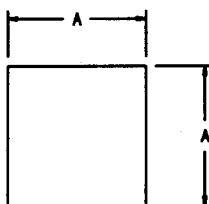
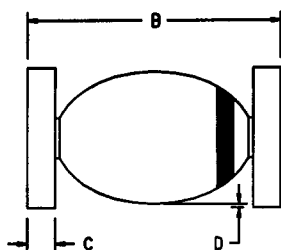
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ELECTRICAL CHARACTERISTICS

CHARACTERISTICS	SYMBOL	MAXIMUM	UNIT
Instantaneous Forward Voltage Drop (IF = 0.75 Adc, TA=25°C, 300μs Pulse)	VF	3.6	Vdc
Instantaneous Forward Voltage Drop (IF = 0.75 Adc, TA=-55°C, 300μs Pulse)	VF	3.8	Vdc
Reverse Leakage Current (Rated VR, TA=25°C, 300μs pulse minimum)	IR	5	μA
Reverse Leakage Current (Rated VR, TA=100°C, 300μs pulse minimum)	IR	1	mA
Junction Capacitance (VR = 100 Vdc, TA=25°C, f= 1MHz)	CJ	20	pf
Reverse Recovery Time (IF =500ma, IR = 1A, IRR=250mA, TA=25°C)	trr	65	nsec

CASE OUTLINE:



DIMENSIONS

DIM	MIN.	MAX.
A	.135"	.155"
B	.175"	.250"
C	.022"	.028"
D	.002"	---

Dimensions prior to solder dipping

TYPICAL OPERATING CURVES

TA=25°C Unless otherwise specified

